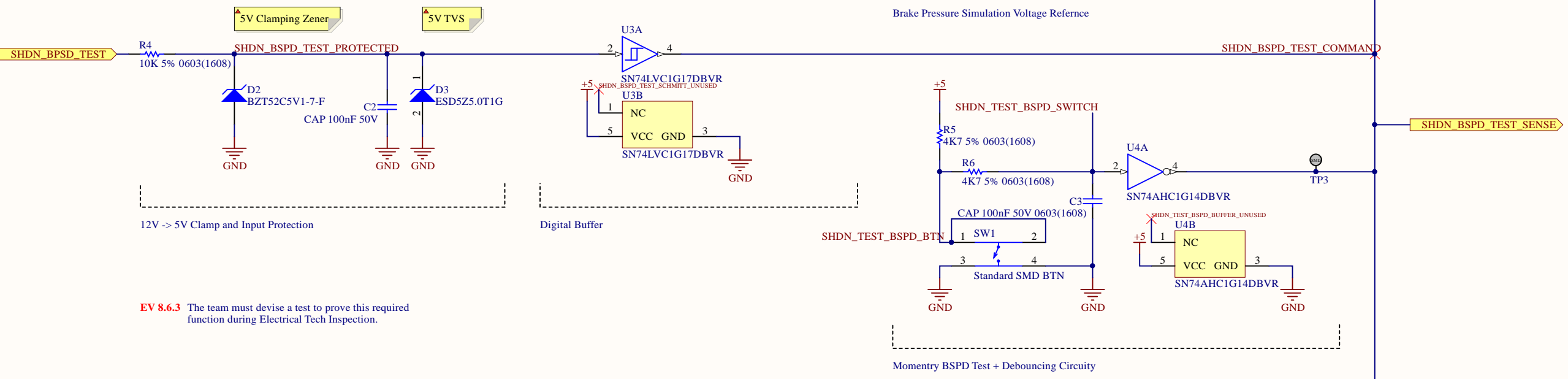
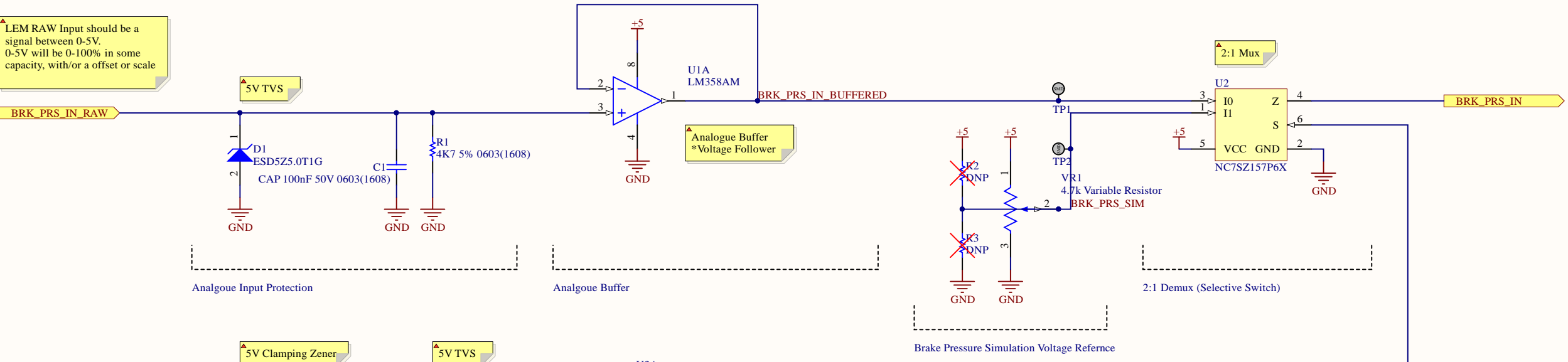


Title QUTMS Shutdown V2.1 - Shutdown Segment			
Size: A4	Number 3	Revision 6	QUT Motorsport, O-120 O Block, QUT Gardens Point Brisbane, QLD, Australia
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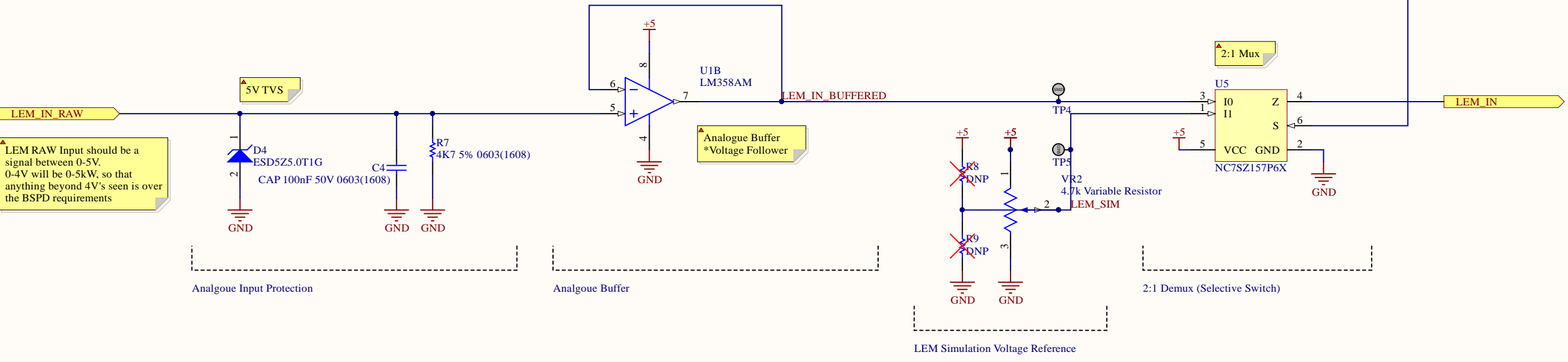


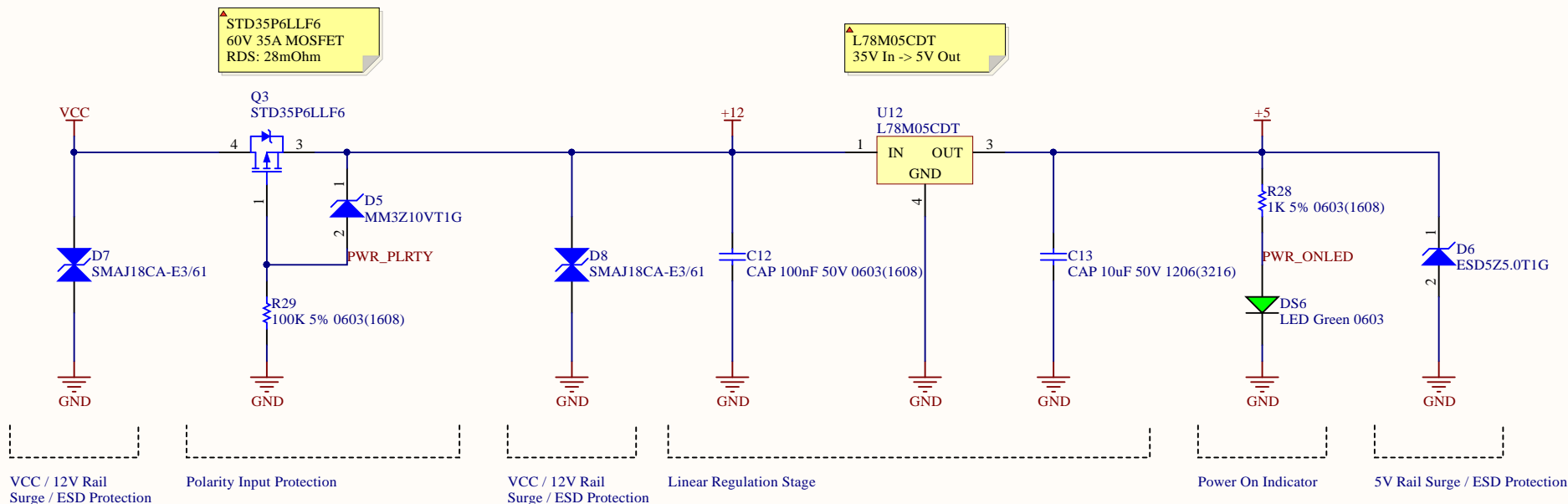
▲ LEM RAW Input should be a signal between 0-5V. 0-5V will be 0-100% in some capacity, with/or a offset or scale



EV 8.6.3 The team must devise a test to prove this required function during Electrical Tech Inspection.

▲ LEM RAW Input should be a signal between 0-5V. 0-4V will be 0-5kW, so that anything beyond 4V's seen is over the BSPD requirements





▲ Input power stage needs to be built for:
- 1 Lead Acid Battery -> 14.4V Max
- 4s#p Li-ion Battery -> 16.8V Max

- Polarity Protection MOSFET https://lcsc.com/product-detail/MOSFET_STMicroelectronics_STD35P6LLF6_STMicroelectronics-STD35P6LLF6_C165929.html 1st Choice
- Polarity Protection Zener https://lcsc.com/product-detail/MOSFET_International-Rectifier_IRFR5305TRPBF_International-Rectifier-IR-IRFR5305TRPBF_C2624.html 1st Choice
- 5V Rail TVS https://lcsc.com/product-detail/Zener-Diodes_ON-Semicon_MM3Z10VT1G_ON-Semicon-ON-MM3Z10VT1G_C39057.html
- VCC / 12V Rail TVS https://lcsc.com/product-detail/Diodes-ESD_ON-Semicon_ESD5Z5-0T1G_ON-Semicon-ON-ESD5Z5-0T1G_C82044.html
- Output Cap [https://lcsc.com/product-detail/Multilayer-Ceramic-Capacitors-MLCC-SMD-SMT_SAMSUNG_CL31A106KBHNNNE_10uF-106-10-50V_C13585.html](https://lcsc.com/product-detail/TVS_Littelfuse_SMAJ18CA_SMAJ18CA_C151240.html)
- Linear Reg Actual https://lcsc.com/product-detail/Linear-Voltage-Regulators_ON-Semicon_MC7805BDTRKG_ON-Semicon-ON-MC7805BDTRKG_C110908.html
- Listed https://lcsc.com/product-detail/Linear-Voltage-Regulators_ON-Semicon_MC7805BDTRKG_ON-Semicon-ON-MC7805BDTRKG_C110908.html
- Any DPak package linear reg (not low dropout!) will be sufficient, Just check the pinout just in case

Title QUTMS Shutdown V2.1 - Power Stage			
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A

A

B

B

C

C

D

D

