
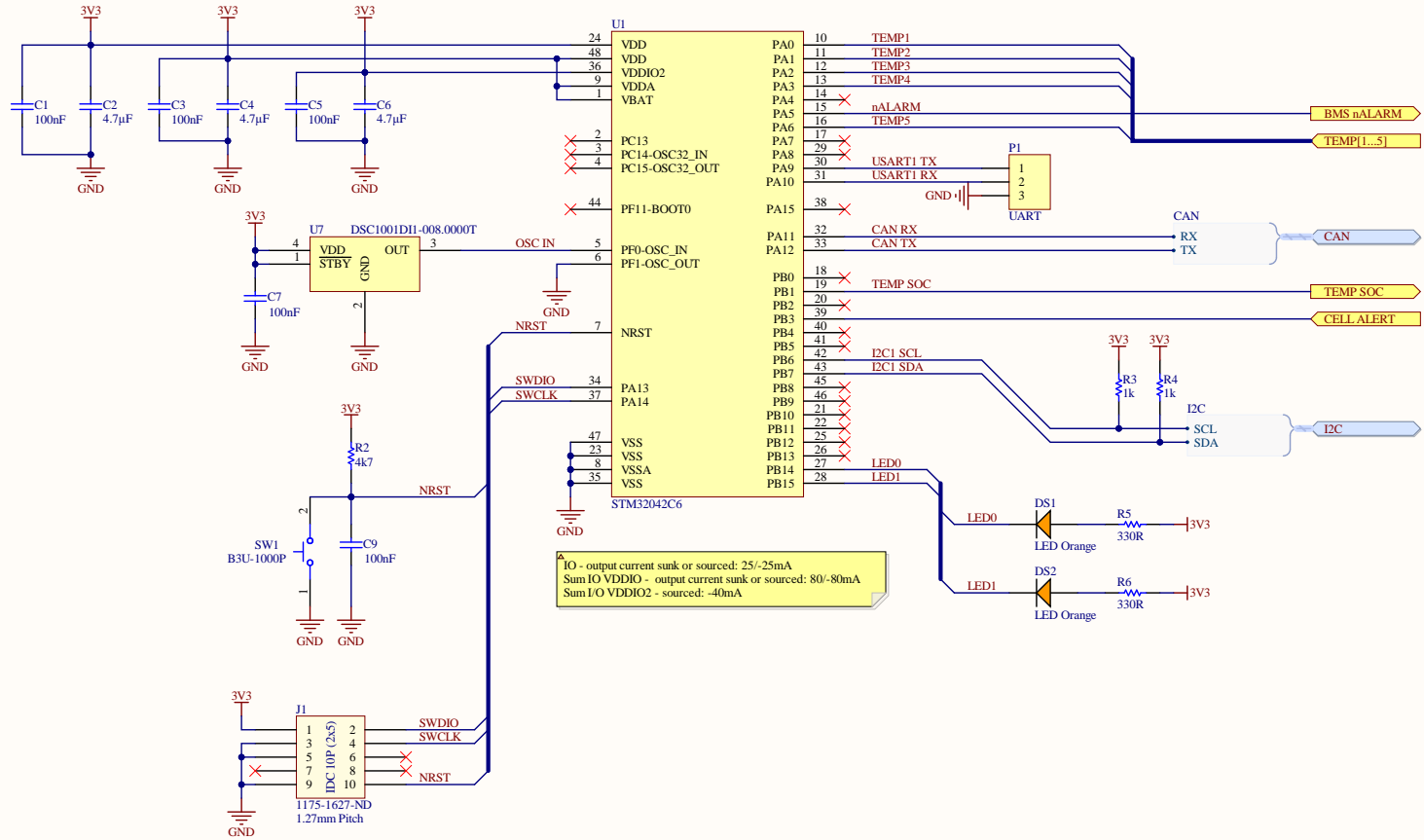


Revision History:

Rev	Date	Description

Sheet Title: BMS Master			
Project: Battery Managment System 2020		QUT Motorsport O-120, Gardens Point 2 George Street Brisbane, QLD 4000 Australia	
Size: A3	Number: 1	Version: 01	
Drawn By: Jonn Dillon		Revision: 01	
Print Date: 24/06/2020 Print Time: 10:30:15 AM		Sheet 1 of 10	
File Name: BMS-S01-V00-BatteryManagementSystem.SchDoc			

These caps must be placed as close as possible to, or below, the appropriate pins on the underside of the PCB to ensure good functionality.



Revision History:

Rev	Date	Description

Sheet Title: **Microcontroller**

Project: **Battery Management System 2020**

Size: A3

Number: 2

Drawn By: Zoe Goodward

Print Date: 24/06/2020

Version: 01

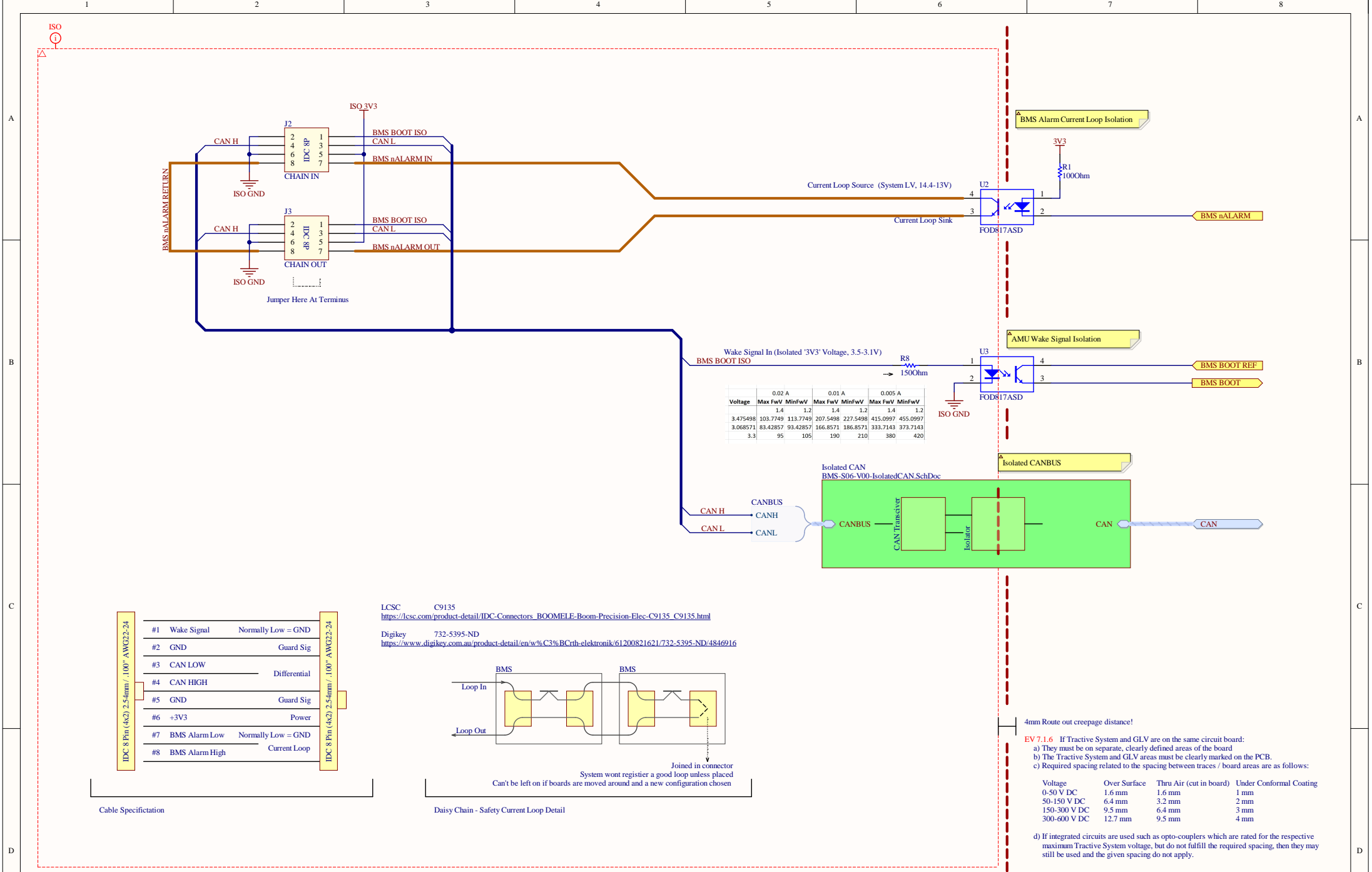
Revision: 01

Sheet 2 of 10

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2 George Street
Brisbane, QLD 4000



File Name: BMS-S04-V00-Microcontroller.SchDoc



Revision History:

Rev	Date	Description

Sheet Title: **Isolation Barrier**

Project: **Battery Management System 2020**

Size: A3

Number: 3

Print Date: 24/06/2020

Print Time: 10:30:15 AM

File Name: BMS-S05-V00-Isolation.SchDoc

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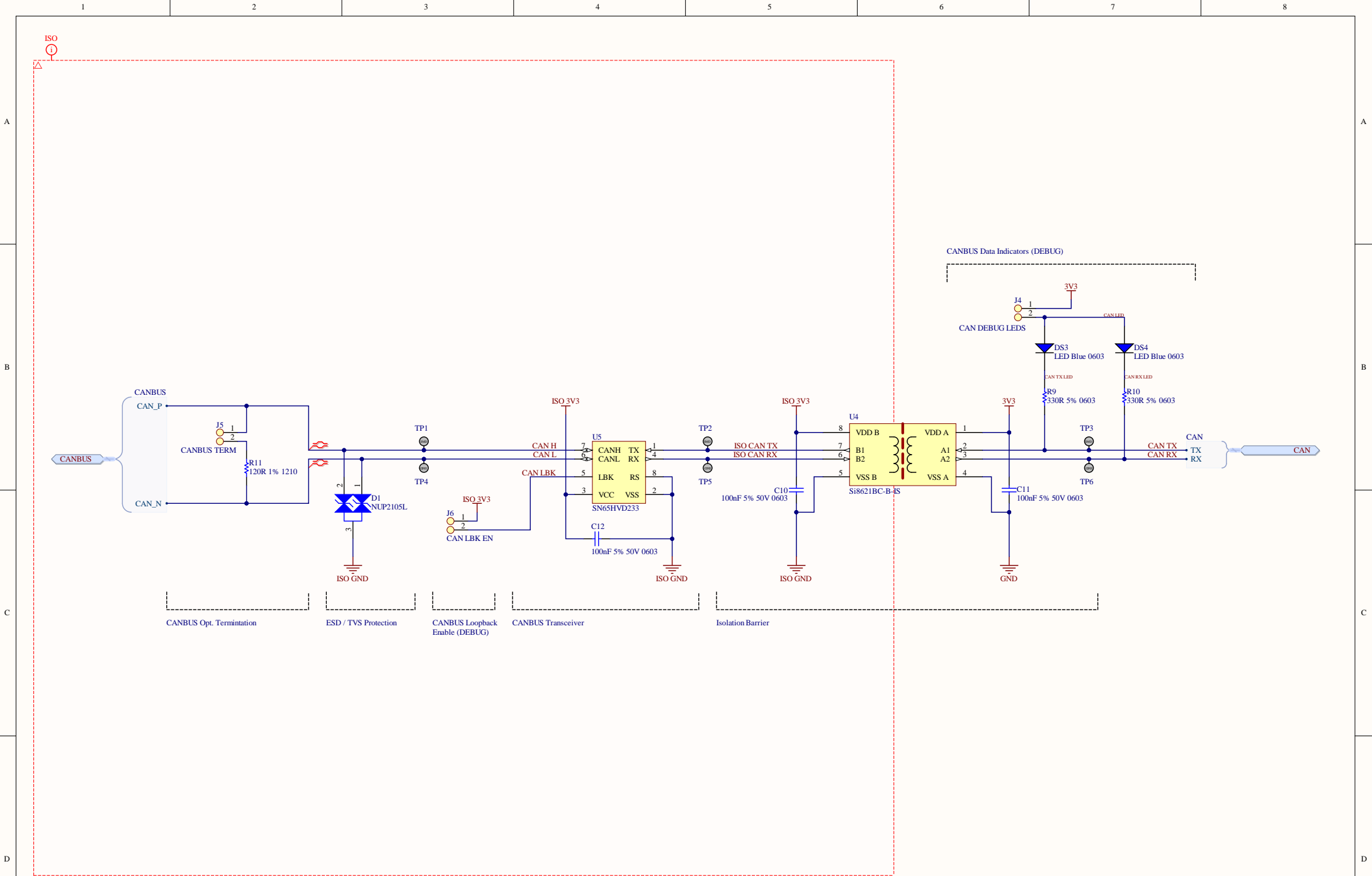
2 George Street

Brisbane, QLD 4000

Sheet 3 of 10

Australia





Revision History:		
Rev	Date	Description

Sheet Title: **Isolated CANBUS @ 3V3**

Project: **Battery Management System 2020**

Size: A3 Number: **4**

Drawn By: Jann Dillon

Print Date: 24/06/2020

Version: **01**

Revision: **01**

Sheet 4 of 10

Print Time: 10:30:16 AM

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File Name: BMS-S06-V00-IsolatedCAN.SchDoc

A

B

C

D

A

B

C

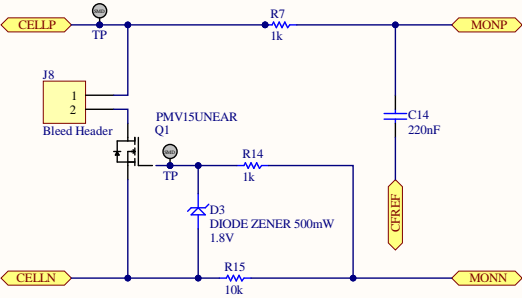
D

RC circuit provides filtering for cell voltage measurement, time constant should be set appropriately.

Balancing is stopped momentarily during cell voltage measurements, RC time constant needs to be low enough that voltage on C comes up to cell voltage before sample is taken.

2xR is placed across cell during balancing, R must be large enough so that current is limited to a suitably low value.

Zener is only required to protect gate of FET in case that multiple adjacent cells are balanced. Given max gate voltage will be nominally around 1.65V a standoff voltage of around 1.8V should be fine. Choose gate resistor with reference to Zener standoff current.



PH2925U, SUS 0.85, SOT-669 (5x5mm), 20A @ Vgs=1.5V, Vds=0.1V, up to 62.5W Pdis
PMV15UNEAR, SUS 0.16, SOT-23 (3x2.5mm), 5A @ Vgs=1.5V, Vds=0.1V, up to 8.3W Pdis, 0.6-1.4W typ

Revision History:

Rev	Date	Description

Sheet Title: **Cell Balancing**

Project: **Battery Managment System 2020**

Size: A3 Number: **6**

Drawn By: Sam Haines

Print Date: 24/06/2020

Version: **01**

Revision: **01**

Sheet 6 of 10

Print Time: 10:30:17 AM

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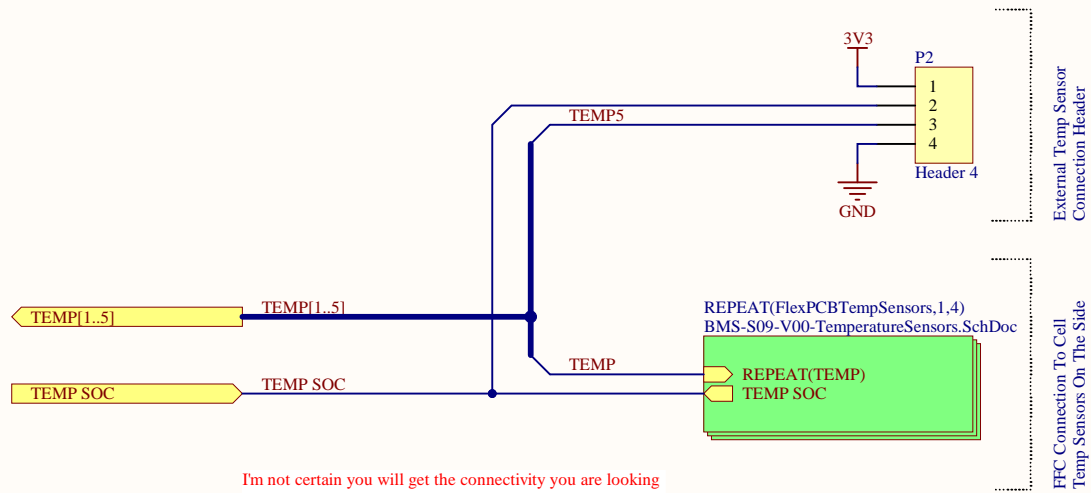
Brisbane, QLD 4000

Australia

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File Name: BMS-S03-V00-CellInterface.SchDoc



I'm not certain you will get the connectivity you are looking for with this strategy. Check the compiled version of the project carefully.

Revision History:

Rev	Date	Description

Sheet Title: **Extra Temperature Connection**

Project: **Battery Managment System 2020**

Size: A3 Number: 8

Version: 01

Revision: 01

Drawn By: Jonn Dillon

Sheet 8 of 10

Print Date: 24/06/2020 Print Time: 10:30:17 AM

File Name: BMS-S08-V00-TemperatureSensorInterface.SchDoc

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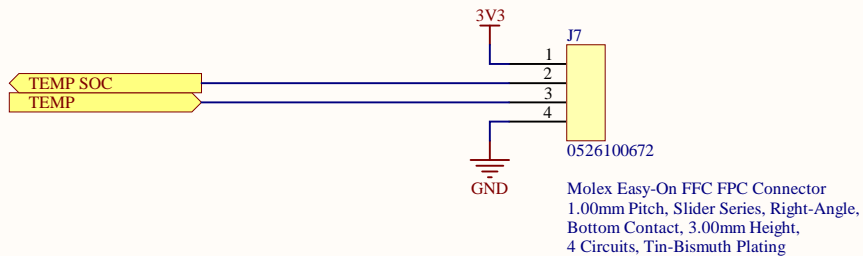
B

C

C

D

D



Revision History:

Rev	Date	Description

Sheet Title: **Temperature Sensors**Project: **Battery Managment System 2020**

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Brisbane, QLD 4000
Australia



Size: A3

Number: 9

Version: 01

Revision: *

Drawn By: Jonn Dillon

Sheet 9 of 10

Print Date: 24/06/2020 Print Time: 10:30:17 AM

File Name: BMS-S09-V00-TemperatureSensors.SchDoc

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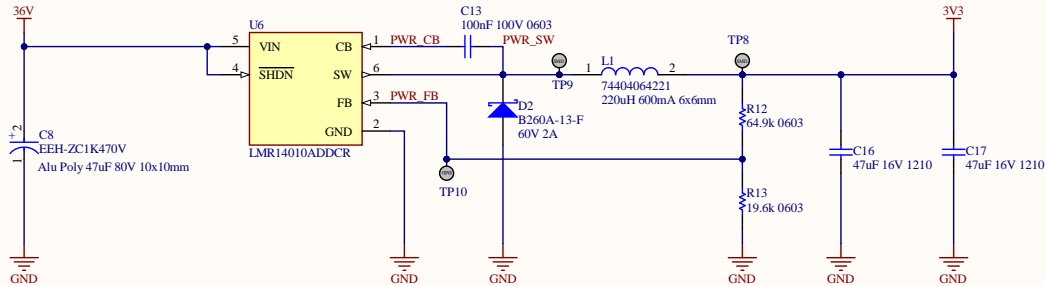
4

BMS Power Budget

Essential	STM32F042 @ 42Mhz, All peripherals enabled
25mA	BQ76930, Everything On
200uA	Si8621 @ 1Mbps (CANBUS Datarate)
3mA	DS18B20 @ Full Functionality x 50
75mA	

Debug	MCU Debug LEDs
6mA	CANBUS Debug LEDs
6mA	

Essential	303mA
Debug	315mA



Revision History:

Rev	Date	Description

Sheet Title: **Power Supply (20-40V to 3v3 @ 0.1-0.6A)**

Project: **Battery Management System 2020**

Size: A3 Number: **10**

Drawn By: Jann Dillon

Print Date: 24/06/2020 Print Time: 10:30:18 AM

Version: **01**

Revision: **01**

Sheet **10** of **10**

File Name: BMS-S07-V00-PSU.SchDoc

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