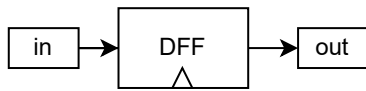
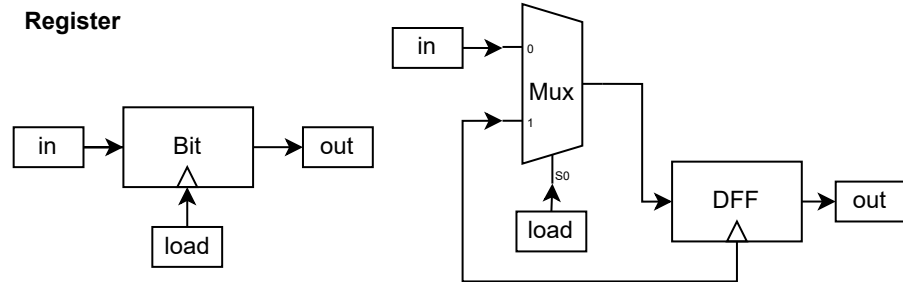


# Sequential Logic

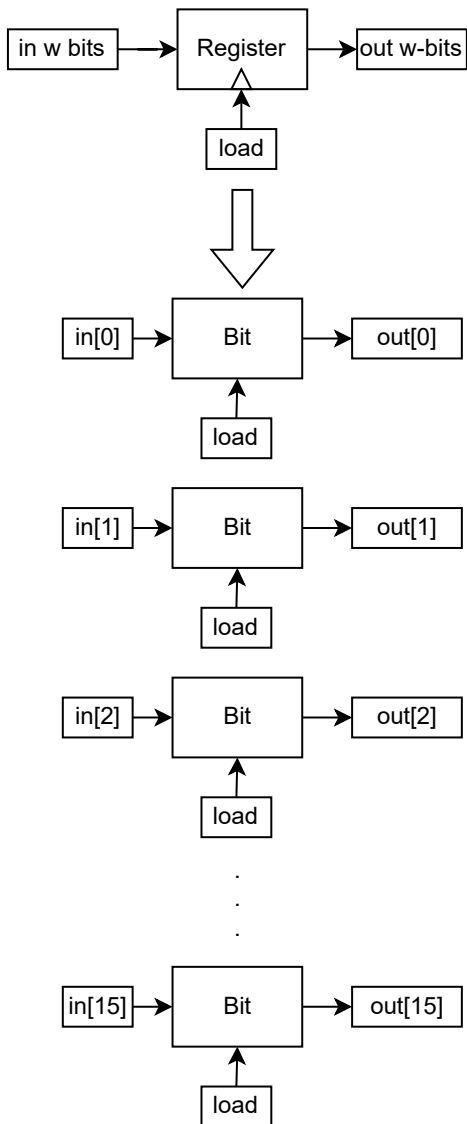
## Data Flip-Flop



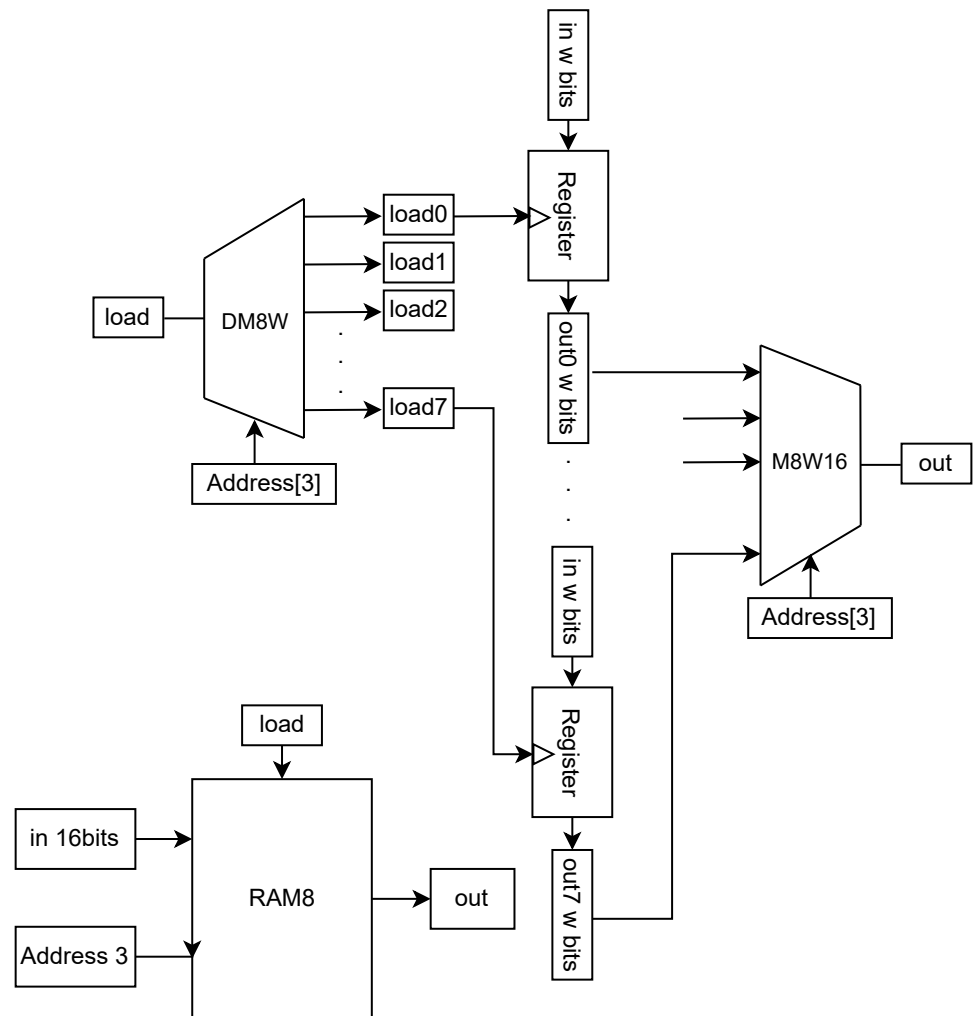
## Register



### 16 bits-Register



**RAM 8**



**PC**

