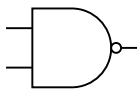
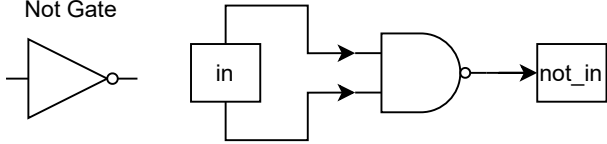


# Basic Logic Implementation with Nand Gate

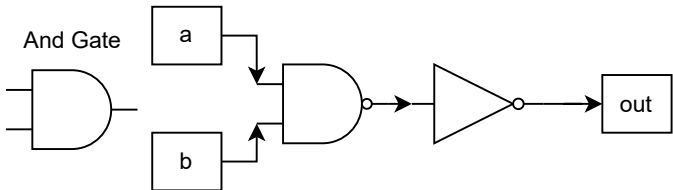
Nand Gate



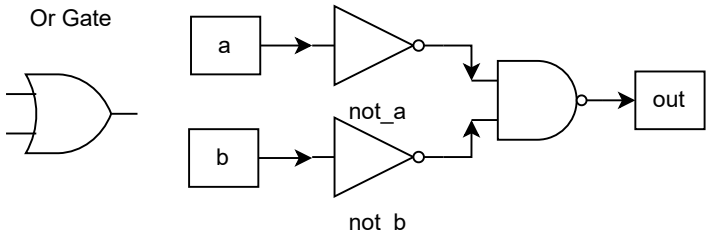
Not Gate



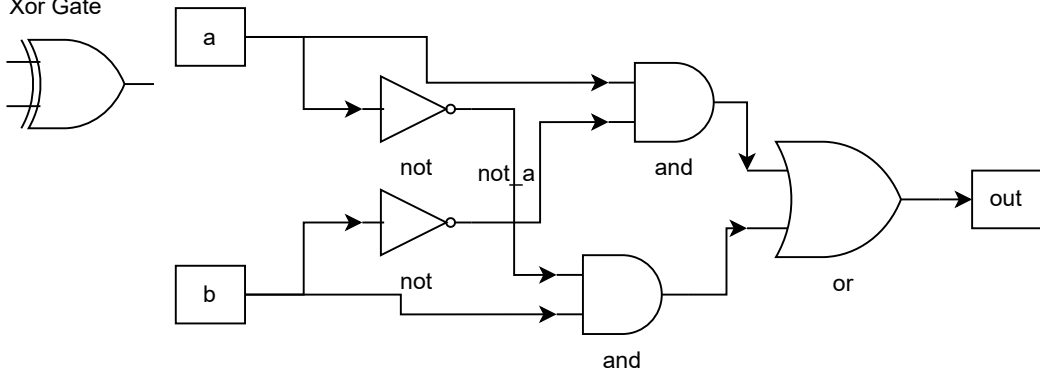
And Gate



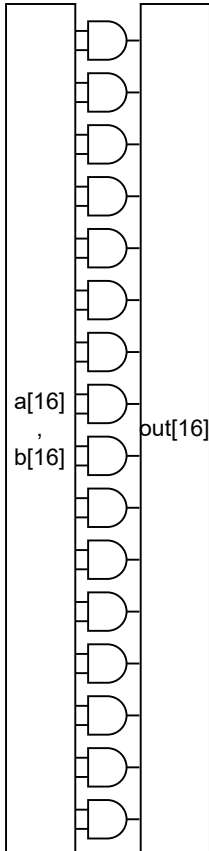
Or Gate



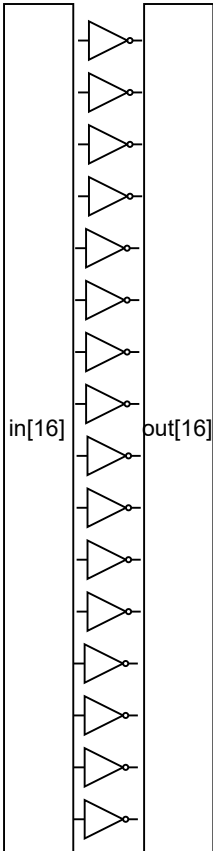
Xor Gate



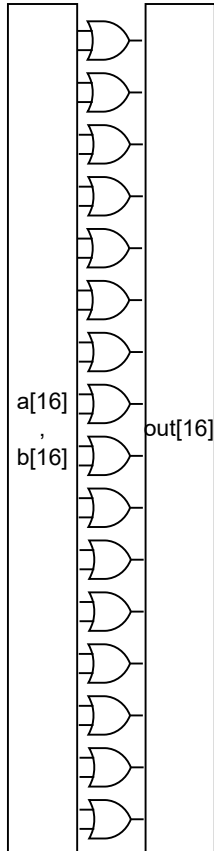
Add16



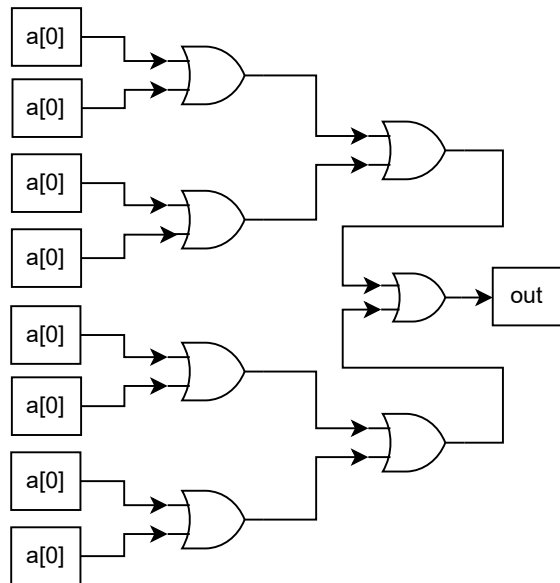
Not16



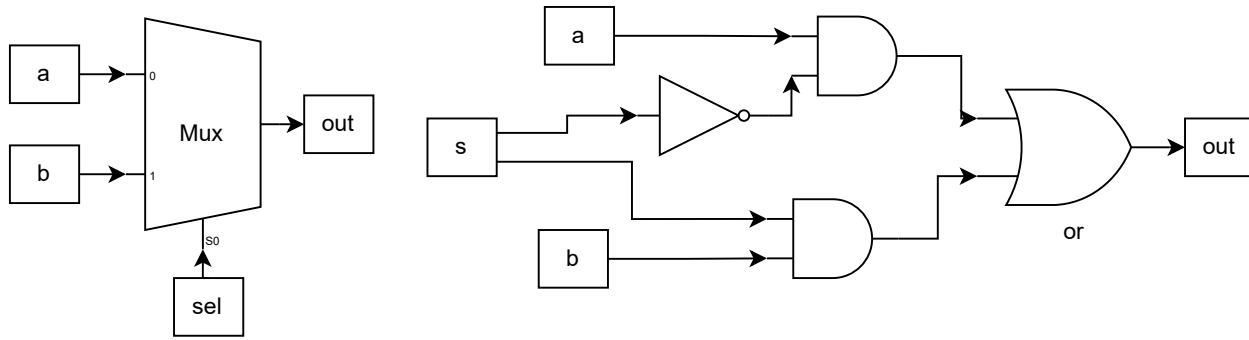
Or16



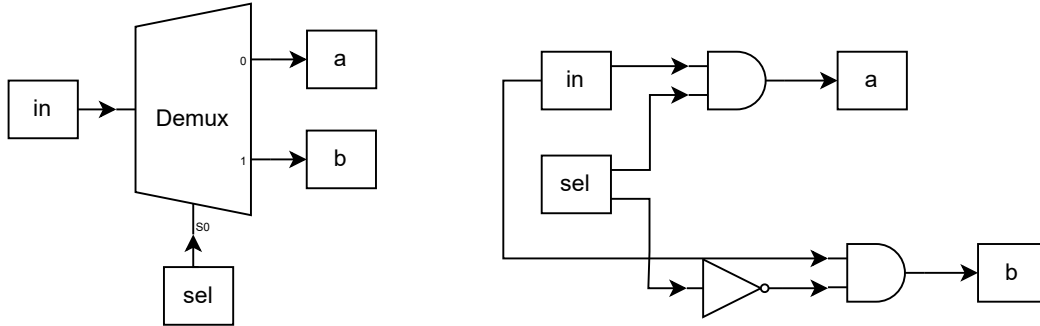
Or8Way



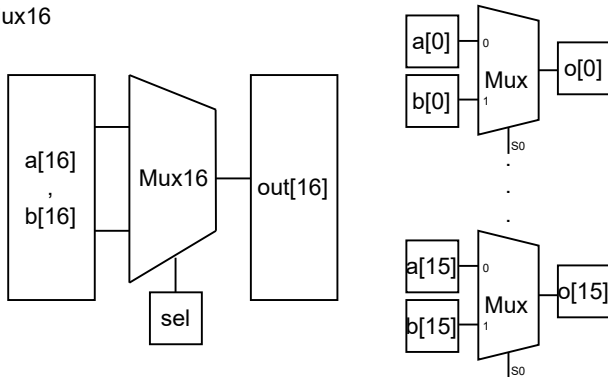
Mux



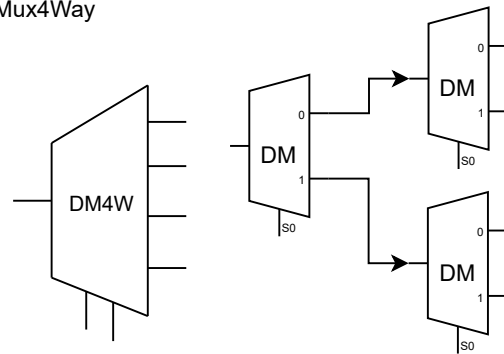
DMux



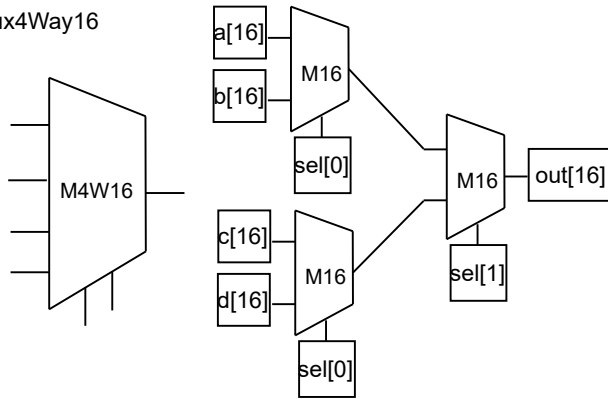
Mux16



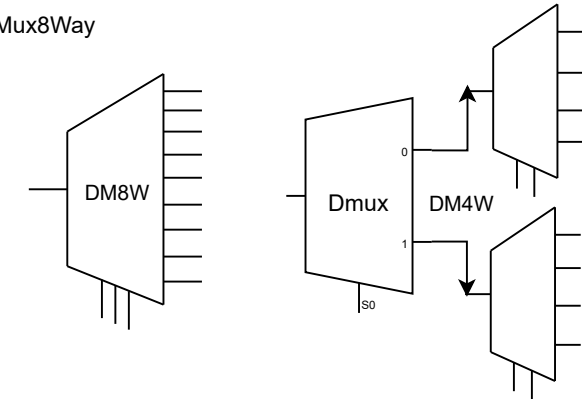
DMux4Way



Mux4Way16



DMux8Way



Mux8Way16

