

ECE571: Advanced Microprocessor Design – Homework 9
Spring 2016

Due: Thursday 14 April 2016, 3:30pm

Create a document that contains the answers to the questions below. A .pdf or .txt file is preferred but I can accept MS Office or Libreoffice format if necessary.

1. Read the following article:

- **A Validation of DRAM RAPL Power Measurements** by Desrochers, Paradis and Weaver

http://web.eece.maine.edu/~vweaver/classes/ece571/2016_memsys_rapl.pdf

2. Answer the following questions:

- (a) How was actual DRAM power measured for the test setup?
- (b) How were the RAPL results gathered?
- (c) Did the actual results match the DRAM results?
- (d) The paper looks at measuring DDR3 DRAM power. What challenges exist with measuring DDR4 power?
- (e) What additional experiments do you think could be run that would make this paper better?

3. Submitting your work.

- Create the document containing the answers to the questions asked.
- Please make sure your name appears in the document.
- e-mail the file to me by the homework deadline.