1. Answer the following questions:

(a) List two new changes in the skylake processor that reduce the power usage of the chip.

Intel Speed shift technology

New architecture

(b) Why did they move the FIVR (integrated voltage regulator) back off chip again?

There is the potential for Intel to use linear regulators at low power. Intel can find them to be beneficial to the design process.

(c) What is the purpose of the Intel Speed Shift technology?

It is a new methodology of allowing quick frequency changes and responding to power loads. The purpose is to save energy by faster migrating the control from the operating system back down to the hardware. Also the purpose is to keep the system responsiveness high but power consumption low.