2016 September

MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY	SUNDAY
29	30	31	01	02	03	04
05	06	07	08	09 Lecture 1: **Intro, ISAs**	10	11
12	13	14	15	16 Lecture 2: **ISAs continued, MIPS ISA**. HW1 Assigned	17	18
19	20	21	22	23 Lecture 3: **Single Cycle MIPS implementation, Pipelining**	24	25
26	27	28	29	30 Lecture 4: **Pipelined MIPS implementation**; HW 1 Due. **Lab 1**	01	02
03	04	Notes: Grading. Homeworks (20%), Labs (20%), Midterm (25%), Final (35%)				

2016 October

MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY	SUNDAY
26	27	28	29	30	01	02
03	04	05	06	07 Lecture 5: **Pipelining continued; Multi-level memory**	08	09
10	11	12	13	14 Lecture 5: **Caches**; **Lab 1 Due**. HW2 Assigned	15	16
17	18	19	20	21 Lecture 6: **Caches continued, Virtual Memory**	22	23
24	25	26	27	28 Lecture 7: **Branch Prediction**; HW2 Due, Lab 2	29	30
31	01	Notes: HW1: ISA concepts. Lab 1: MIPS Emulator/Simulator				

2016 November

MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY	SUNDAY
31	01	02	03	04 Midterm Review	05	06
07	08	09	10	11 Midterm; Lab 2 Due	12	13
14	15	16	17	18 Lecture 8: **Out-of-order scheduling;** HW3 Assigned	19	20
21	22	23	24	25 No Class; Thanksgiving	26	27
28	29	30	01	02 Lecture 9: **Out-of-order scheduling cont.**; Lab 3	03	04
05	06	Notes: HW2: Pipelining, caches Lab 2: Cache Modeling and Design				

2016 December

MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY	SUNDAY
28	29	30	01	02 Lecture 10: **Main memory and prefetching;** HW3 Due	03	04
05	06	07	08	09 Lecture 11: **Intro to multi-cores**; Lab 3 Due	10	11
12	13	14	15	16 **Final Exam**	17	18
19	20	21	22	23	24	25
26	27	28	29	30	31	01
02	03	Notes: HW3: Out-of-order scheduling; Lab 3: Branch prediction OR Tomosulo Scheduling				