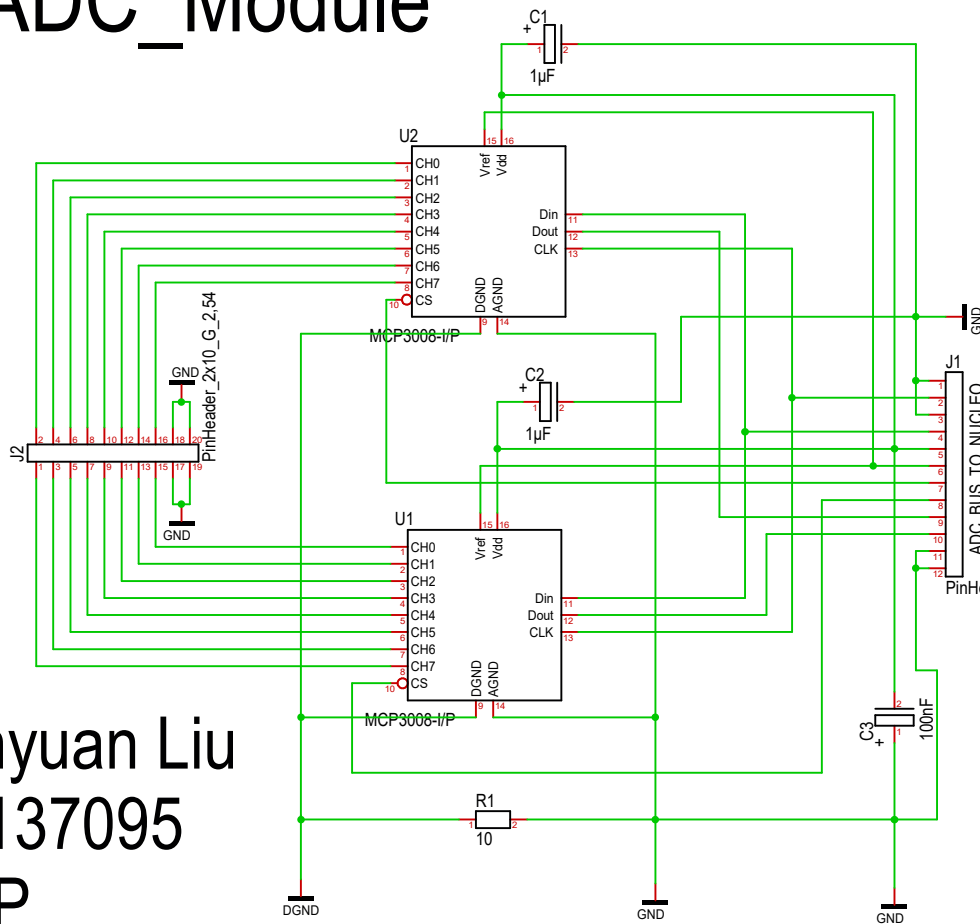


# ADC\_Module

ADC\_Analog\_Output

Qinyuan Liu  
20137095  
FYP



DAC_Header_1*12	Description	MCP3008 U1	MCP3008 U2	Nucleo Pin
Pin 1	GND	-	-	GND
Pin 2	CLK	CLK	CLK	TBD
Pin 3	GND	-	-	TBD
Pin 4	SPI_Data_In (Duo)	Din	Din	TBD
Pin 5	VDD	VDD	VDD	VDD
Pin 6	Vref	Vref	Vref	Vref
Pin 7	CS_U2	-	CS_U2	TBD
Pin 8	CS_U1	CS_U1	-	TBD
Pin 9	Dout_U2	-	Dout_U2	TBD
Pin 10	Dout_U1	-	Dout_U1	TBD
Pin 11	GND	A&D_GND	A&D_GND	GND
Pin 12	GND	A&D_GND	A&D_GND	GND