

Pin_Config_NUCL			
Connector	Pin	Pin Name	STM32 Pin
CN6 Power	1	NC	-
	2	IOREF	-
	3	RESET	NRST
	4	+3.3V	-
	5	+5V	-
	6	GND	-
	7	GND	-
	8	VIN	-
CN8 Analog	1	A0	PA0
	2	A1	PA1
	3	A2	PA4
	4	A3	PB0
	5	A4	PC1 or PB9(1)
	6	A5	PC0 or PB8(1)
CN5 Digital	10	D15	PB8
	9	D14	PB9
	8	AREF	-
	7	GND	-
	6	D13	PA5
	5	D12	PA6
	4	D11	PA7
	3	D10	PB6
	2	D9	PC7
	1	D8	PA9
CN9 Digital	8	D7	PA8
	7	D6	PB10
	6	D5	PB4
	5	D4	PB5
	4	D3	PB3
	3	D2	PA10
	2	D1	PA2
	1	D0	PA3

Pin_Config_NUCLEO Morpho Layout			
Pin Number	Name	Moule	Configerung
CN7 Odd Pins	-	-	-
1	PC10	-	-
3	PC12	-	-

5	VDD	-	-
7	BOOT0	-	-
9	-	-	-
11	-	-	-
13	PA13	-	-
15	PA14	-	-
17	PA15	-	-
19	GND	-	-
21	PB7	-	-
23	PC13	-	-
25	PC14	ADC_Header_Pin 7	GPIO_Output (Chip Select)
27	PC15	ADC_Header_Pin 8	GPIO_Output (Chip Select)
29	PH0	-	-
31	PH1	-	-
33	VBAT	-	-
35	PC2	-	-
CN7 Even Pins	-	-	-
2	PC11	-	-
4	PD2	-	-
6	E5V	-	-
8	GND	-	-
10	-	-	-
12	IOREF	-	-
14	RESET	-	-
16	+3.3V	-	-
18	+5V	-	-
20	GND	-	-
22	GND	-	-
24	VIN	-	-
26	-	-	-
28	PA0	-	-
30	PA1	-	-
32	PA4	-	-
34	PB0	-	-
36	PC1 or PB9	-	-
CN10 Odd Pins	-	-	-
1	PC9	-	-
3	PB8	-	-
5	PB9	-	-
7	AVDD	TBD	-

9	GND	DAC_Header, 1,2,3,7,11,12	GND
11	PA5	-	-
13	PA6	-	-
15	PA7	-	-
17	PB6	-	-
19	PC7	-	-
21	PA9	Digital_Out_Pin_4	GPIO_Output (Latch)
23	PA8	Digital_Out_Pin_7	GPIO_CLK
25	PB10	DAC_Header_Pin_9	SPI2_SCK
27	PB4	DAC_Header_Pin_6	GPIO_Output (Latch)
29	PB5	DAC_Header_Pin_4	TBD
31	PB3	DAC_Header_Pin_5	TBD
33	PA10	Digital_IN_Pin_4	GPIO_Output (Data_In)
CN10 Even Pins	-	-	-
2	PC8	-	-
4	PC6	-	-
6	PC5	-	-
8	U5V	-	-
10	-	-	-
12	PA12	-	-
14	PA11	-	-
16	PB12	DAC_Header_Pin_8	SPI2_MISO
18	PB11	-	-
20	GND	Digital_Out_Pin_1,3,6,8	GND
22	PB2	Digital_Out_Pin_5	GPIO_Output (Data Set)
24	PB1	-	-
26	PB15	-	-
28	PB14	-	-
30	PB13	-	-
32	AGND	-	-
34	PC4	-	-

EO Arduino Layout

Function	Moule	Configiration
-	-	-
3.3V Ref	ADC_Pin 6	3.3V Ref
RESET	-	-
3.3V input/output	ADC_Pin5, DAC_Pin10, Digital_in_Pin2, Digital_Out_Pin2,	3.3V
5V output	-	-
Ground	Digital_IN_Pin_1,2,4,5,6,8	GND
Ground	-	-
Power input	-	-
ADC12_IN5	-	-
ADC12_IN6	-	-
ADC12_IN9	-	-
ADC12_IN15	-	-
ADC123_IN2 (PC1) or I2C1_SDA (PB9)	-	-
ADC123_IN1 (PC0) or I2C1_SCL (PB8)	-	-
I2C1_SCL	-	-
I2C1_SDA	-	-
AVDD	-	-
Ground	DC_Header_Pin 1,3,11,1	GND
SPI1_SCK	ADC_Header_Pin 2	SPI1_SCK
SPI1_MISO	ADC_Header_Pin 9	SPI1_MISO
TIM17_CH1 or SPI1_MOSI	ADC_Header_Pin 4	SPI1_MOSI
TIM4_CH1 or SPI1_CS	-	-
TIM3_CH2	-	-
-	Digital_Out_Pin_4	GPIO_Output (Latch)
-	n_5, Digital_Out_Pin_7, D	CLK
TIM2_CH3	-	-
TIM3_CH1	-	-
-	-	-
TIM2_CH2	-	-
-	Digital_IN_Pin_4	GPIO_Output (Data_In)
USART2_TX	-	-
USART2_RX	-	-