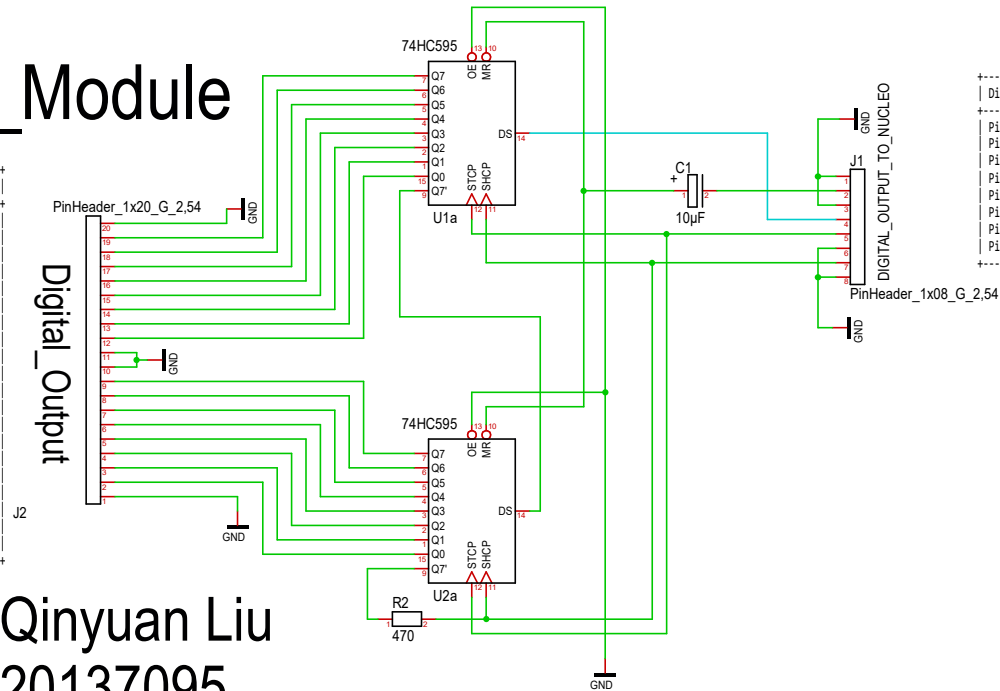


# Digital\_Output\_Module

| Header Pin | Description    | 74HC595 Connection |
|------------|----------------|--------------------|
| Pin 1      | GND            | -                  |
| Pin 2      | Digital_OUT_1  | U1_Q0              |
| Pin 3      | Digital_OUT_2  | U1_Q1              |
| Pin 4      | Digital_OUT_3  | U1_Q2              |
| Pin 5      | Digital_OUT_4  | U1_Q3              |
| Pin 6      | Digital_OUT_5  | U1_Q4              |
| Pin 7      | Digital_OUT_6  | U1_Q5              |
| Pin 8      | Digital_OUT_7  | U1_Q6              |
| Pin 9      | Digital_OUT_8  | U1_Q7              |
| Pin 10     | GND            | -                  |
| Pin 11     | GND            | -                  |
| Pin 12     | Digital_OUT_9  | U2_Q0              |
| Pin 13     | Digital_OUT_10 | U2_Q1              |
| Pin 14     | Digital_OUT_11 | U2_Q2              |
| Pin 15     | Digital_OUT_12 | U2_Q3              |
| Pin 16     | Digital_OUT_13 | U2_Q4              |
| Pin 17     | Digital_OUT_14 | U2_Q5              |
| Pin 18     | Digital_OUT_15 | U2_Q6              |
| Pin 19     | Digital_OUT_16 | U2_Q7              |
| Pin 20     | GND            | -                  |



| Digital_Output_BUS_8*1 | Description | 74HC595 U1 | 74HC595 U2 | NucLeo Pin |
|------------------------|-------------|------------|------------|------------|
| Pin 1                  | GND         | -          | -          | GND        |
| Pin 2                  | VDD         | -          | VDD        | VDD        |
| Pin 3                  | GND         | -          | -          | GND        |
| Pin 4                  | LATCH       | LATCH      | LATCH      | TBD        |
| Pin 5                  | DATA        | DATA       | Q7'        | TBD        |
| Pin 6                  | GND         | -          | -          | GND        |
| Pin 7                  | CLK         | CLK        | CLK        | TBD        |
| Pin 8                  | GND         | -          | -          | GND        |

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