Protection



Today

- Protection
- Security



Examples of OS Protection

- Memory protection
 - Between user processes
 - Between user and kernel
- File protection
 - Prevent unauthorized accesses to files



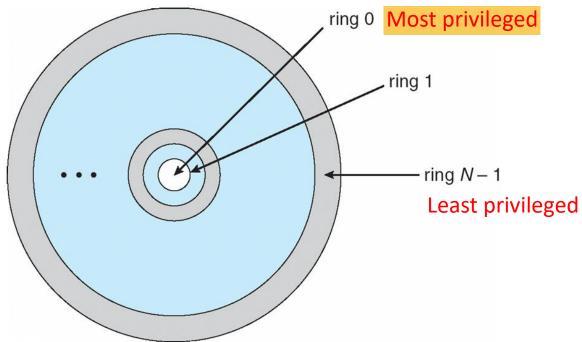
Principles of Protection

- Principle of least privilege
 - Programs and users should be given just enough privileges to perform their tasks
 - Limit the damage if the entity has a bug or abused



Protection Domains

- Let D_i and D_j be any two domain rings
- If $j < l \Rightarrow D_i \subseteq D_j$
- Kernel mode vs. user mode





Access Control Matrix

- **Domains** in rows
 - Domain: a user or a group of users
- Resources in columns
 - File, device, ...

E.g., User D1 can read F1 or F3

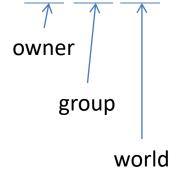
| object domain | F ₁ | F ₂ | F ₃ | printer |
|------------------|----------------|----------------|----------------|---------|
| D_1 | read | | read | |
| D_2 | | | | print |
| D_3 | | read | execute | |
| D_4 | read write | | read write | |

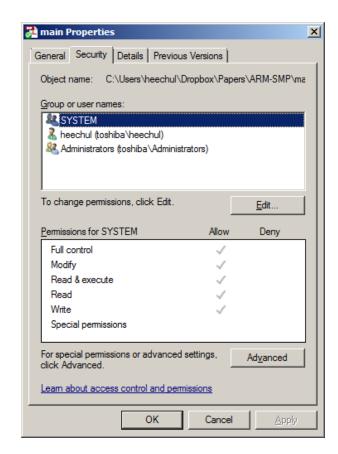


Method 1: Access Control List

Each object stores users and their permissions

-rw-rw-r-- heechul heechul 38077 Apr 23 15:16 main.tex







Method 2: Capability List

- Each domain tracks which objects can access
 - Page table: each process (domain) tracks all pages (objects) it can access



Summary

- Protection
 - Prevent unintended/unauthorized accesses
- Protection domains
 - Class hierarchy: root can to everything a normal user can do + alpha
- Access control matrix
 - Domains (Users) ← → Resources (Objects)
 - Resource oriented: Access control list
 - Domain oriented: Capability list



Security



Today

- Security basics
- Stack overflow
- Some recent security bugs





Security

- System secure if resources used and accessed as intended under all circumstances
 - Unachievable
- Intruders (crackers) attempt to breach security
- Threat is potential security violation
- Attack is attempt to breach security



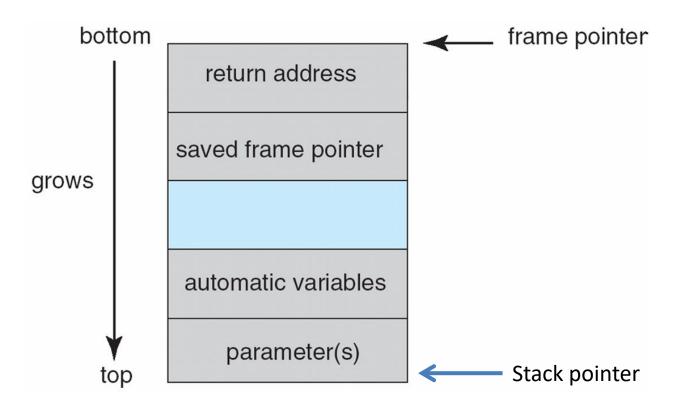
Threats: Software

Stack and Buffer Overflow

- Exploits a bug in a program (overflow either the stack or memory buffers)
- Failure to check bounds on inputs, arguments
- Write past arguments on the stack into the return address on stack
- When routine returns from call, returns to hacked address
 - Pointed to code loaded onto stack that executes malicious code
- Unauthorized user or privilege escalation



Stack Frame Layout





Code with Buffer Overflow

BUFFER_SIZE only has 256; if more than, will cause segmentation fault

```
#define BUFFER SIZE 256
int process_args(char *arg1)
    char buffer[BUFFER SIZE];
    strcpy(buffer, arg1);
int main(int argc, char *argv[])
    process_args(argv[1]);
```

What is wrong in this code?



Code with Buffer Overflow

```
#define BUFFER SIZE 256
int process args(char *arg1)
    char buffer[BUFFER SIZE];
    strcpy(buffer,arg1);
int main(int argc, char *argv[])
    process_args(argv[1]);
```

```
return address

saved frame pointer

buffer(BUFFER_SIZE - 1)

...

buffer(1)

buffer(0)

arg1
```

Stack layout after calling process_arg()



Code with Buffer Overflow

```
#define BUFFER SIZE 256
int process args(char *arg1)
    char buffer[BUFFER SIZE];
    strcpy(buffer, arg1);
int main(int argc, char *argv[])
    process_args(argv[1]);
```

```
return address

saved frame pointer

buffer(BUFFER_SIZE - 1)

...

buffer(1)

buffer(0)

arg1
```

Do you remember strcpy() in C?



Let's Get the Shell

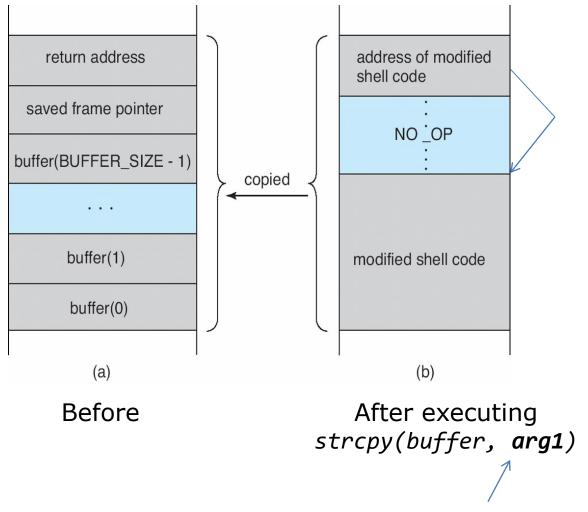
Steps

- Compile the code you want to illegitimately execute
- 'Carefully' modify the binary
- Pass the modified binary as string to the process_arg()

```
#include <stdio.h>
int main(int argc, char *argv[])
{
    execvp(''/bin/sh'', ''/bin/sh'', NULL);
    return 0;
}
```

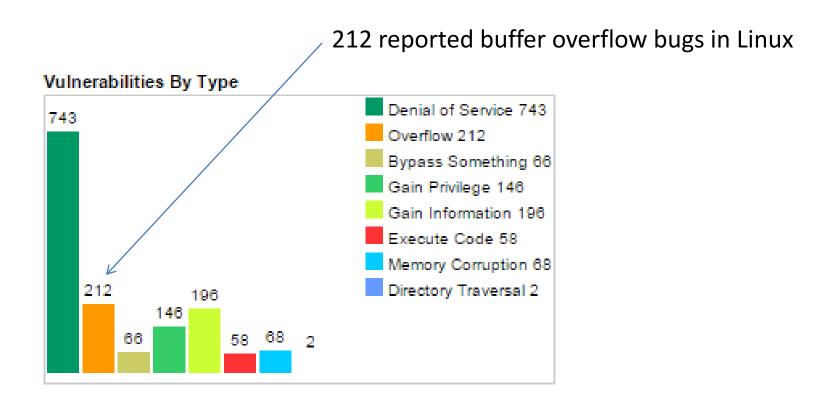


The Attack: Buffer Overflow





Linux Kernel Buffer Overflow Bugs





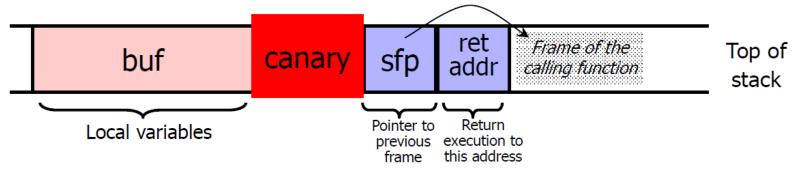
Linux Kernel Buffer Overflow Bugs

| and the second s | | | | | _ | _ | | - | and the second s |
|--|------------------------------|----------------|----------------|-------------|----------|-------------|--------------|------------------|--|
| 6 CVE-2010-2521 119 | DoS Exec Code Overflow | 2010- 09-07 | 2012- 03-19 | 10.0 | None | Remote | Low | Not required | Complete Complete Compl |
| Multiple buffer overflows in fs | s/nfsd/nfs4xdr.c in | the XDR | implement | tation in t | he NFS | server in t | he Linux k | ernel before 2.6 | 3.34-rc6 allow remote attack |
| to cause a denial of service (pnfsd4_decode_compound fun | | execute ar | bitrary cod | de via a ci | rafted N | FSv4 comp | oound WRI | TE request, rela | ated to the read_buf and |
| 9 <u>CVE-2009-0065</u> <u>119</u> | Overflow | 2009- 01-07 | 2012- 03-19 | 10.0 | Admin | Remote | Low | Not required | Complete Complete Comple |
| Buffer overflow in net/sctp/sm allows remote attackers to hav | | | | | | | • | | kernel before 2.6.28-git8 |
| 10 CVE-2008-5134 119 | Overflow | 2008- 11-18 | 2012- 03-19 | 10.0 | None | Remote | Low | Not required | Complete Complete Complete |
| Buffer overflow in the lbs_pro- allows remote attackers to hav | _ | | | | - | | ertas subsy | stem in the Lin | ux kernel before 2.6.27.5 |
| 11 <u>CVE-2008-3915</u> <u>119</u> | Overflow | 2008- 09-10 | 2012- 03-19 | 9.3 | None | Remote | Medium | Not required | Complete Complete Complete |
| Buffer overflow in nfsd in the related to decoding an NFSv4 | | e 2.6.26. | 4, when N | FSv4 is er | nabled, | allows rem | note attacke | ers to have an u | nknown impact via vectors |
| 12 <u>CVE-2008-3496</u> 119 | Overflow | 2008- 08-06 | 2012- 03-19 | 10.0 | None | Remote | Low | Not required | Complete Complete Comple |
| Buffer overflow in format desc (V4L) implementation in the L | | | | | | | | uvc_driver.c in | uvcvideo in the video4linux |
| 13 <u>CVE-2008-1673</u> <u>119</u> | DoS Exec Code Overflow | 2008- 06-09 | 2012- 11-26 | 10.0 | None | Remote | Low | Not required | Complete Complete Comple |



Run-Time Checking: StackGuard

- ◆Embed "canaries" (stack cookies) in stack frames and verify their integrity prior to function return
 - Any overflow of local variables will damage the canary



- Choose random canary string on program start
 - Attacker can't guess what the value of canary will be
- ◆Terminator canary: "\0", newline, linefeed, EOF
 - String functions like strcpy won't copy beyond "\0"



slide 4

22

PaX

- Linux kernel patch
- Goal: prevent execution of arbitrary code in an existing process's memory space
- Enable executable/non-executable memory pages
- Any section not marked as executable in ELF binary is non-executable by default
 - Stack, heap, anonymous memory regions
- Access control in mmap(), mprotect() prevents unsafe changes to protection state at runtime
- Randomize address space layout



Problem: Lack of Diversity

- Buffer overflow and return-to-libc exploits need to know the (virtual) address to hijack control
 - Address of attack code in the buffer
 - Address of a standard kernel library routine
- Same address is used on many machines
 - Slammer infected 75,000 MS-SQL servers using same code on every machine
- ◆Idea: introduce artificial diversity
 - Make stack addresses, addresses of library routines, etc. unpredictable and different from machine to machine

ASLR

- Address Space Layout Randomization
- Randomly choose base address of stack, heap, code segment
- Randomly pad stack frames and malloc() calls
- Randomize location of Global Offset Table
- Randomization can be done at compile- or linktime, or by rewriting existing binaries
 - Threat: attack repeatedly probes randomized binary



Goto Fail Bug

iOS 7.0.6

Data Security

Available for: iPhone 4 and later, iPod touch (5th generation), iPad 2 and later

Impact: An attacker with a privileged network position may capture or modify data in sessions protected by SSL/TLS

Description: Secure Transport *failed to validate* the authenticity of the connection. This issue was addressed by restoring missing validation steps.





Goto Fail Bug

```
err = 0
   hashOut.data = hashes + SSL MD5 DIGEST LEN;
   hashOut.length = SSL SHA1 DIGEST LEN;
   if ((err = SSLFreeBuffer(&hashCtx)) != 0)
   goto fail;
   if ((err = ReadyHash(&SSLHashSHA1, &hashCtx)) != 0)
   goto fail;
   if ((err = SSLHashSHA1.update(&hashCtx, &clientRandom)) != 0)
   goto fail;
   if ((err = SSLHashSHA1.update(&hashCtx, &serverRandom)) != 0)
   goto fail;
   if ((err = SSLHashSHA1.update(&hashCtx, &signedParams)) != 0)
   goto fail;
   goto fail;
                               MISTAKE!
                                         THIS LINE SHOULD NOT BE HERE
   if ((err = SSLHashSHA1.final (מומאות) יוביי := שו
   goto fail;
   err = sslRawVerify(...); // This code must be executed
fail:
   SSLFreeBuffer(&signedHashes);
  SSLFreeBuffer(&hashCtx);
   Return err;
```



- Synopsis
 - Due to a bug in OpenSSL (popular s/w for encrypted communication), web server's internal memory can be dumped remotely





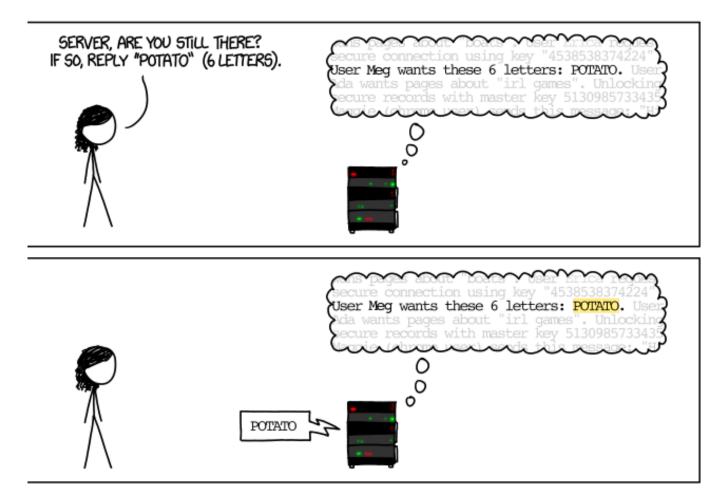




Image source: xkcd.com

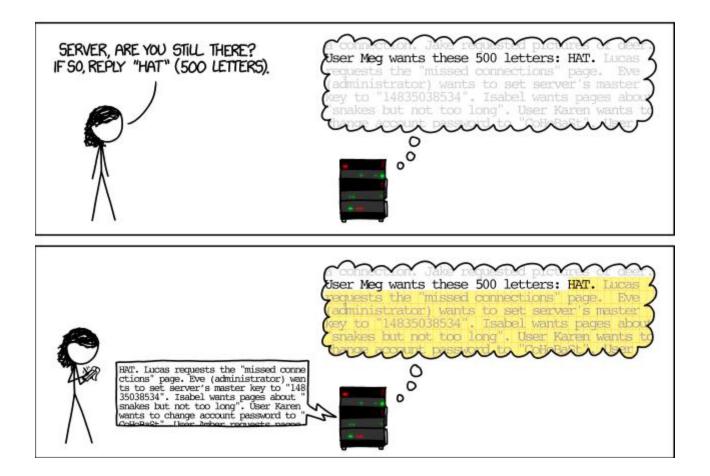




Image source: xkcd.com

```
struct {
  HeartbeatMessageType type;
                                                              Heartbeat
   uint16 payload length;
   opaque payload[HeartbeatMessage.payload_length];
                                                              req. message
   opaque padding[padding length];
} HeartbeatMessage
int tls1 process heartbeat(SSL *s)
                                                             Heartbeat
                                                             Response function
   /* Read type and payload length first */
   hbtvpe = *p++;
   n2s(p, payload); // payload = recv packet.payload length
   pl = p;
   if (hbtype == TLS1 HB REQUEST) {
      buffer = OPENSSL malloc(1 + 2 + payload + padding);
      bp = buffer;
     memcpy(bp, pl, payload);
      r = ssl3_write_bytes(s, TLS1_RT_HEARTBEAT, buffer, 3 + payload + padding);
```

Shellshock Bug

- Synopsis
 - You can remotely execute arbitrary programs on a server running a web server by simply sending a specially crafted http request.
 - Example

```
curl -H "User-Agent: () { :; }; /bin/eject" http://example.com/
```

- The problem
 - Fail to check the validity of a function definition before executing it

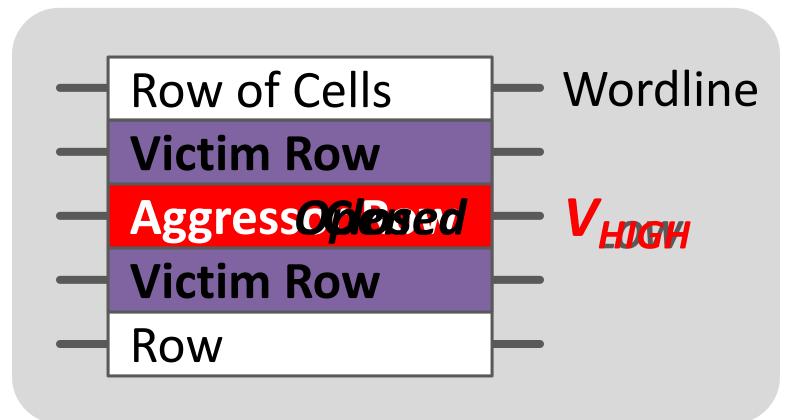


Threats: Hardware

- Disturbance errors in DRAM (*)
 - a.k.a. Row Hammer Bug
 - Repeated opening/closing a DRAM row can cause bit flips in adjacent rows.
 - In more than 80% DRAM modules between 2010 -2013
 - Google demonstrated successful hacking method utilizing the bug (**)
 - manipulate page tables at the user-level



DRAM Chip



Repeatedly opening and closing a row induces disturbance errors in adjacent



Drammer

- Successful exploit to gain root privilege of Android smartphones
 - Exploit row hammer bugs on mobile DRAM
 - Use Android's special memory allocation feature
 - Alter page table entries (privileged) by hammering nearby memory blocks (non-privileged)
 - [Demo]



Meltdown

- What is it?
 - An attack that exploits Intel CPU's flaw that allows any user-level process to read the content of the kernelonly accessible memory---usually the entire dram
- What's the impact?
 - An attacker can dump the entire memory, including password and other confidential information
- Which CPUs are affected?
 - Almost all Intel CPUs that do Out-of-Order Execution to improve performance

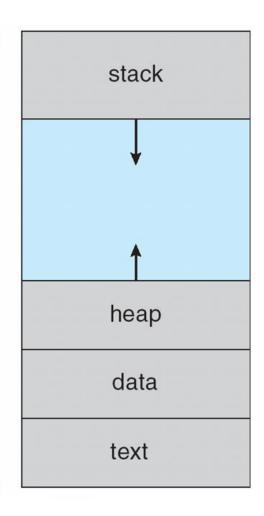


Virtual Memory

max

0

- Abstraction
 - A large (e.g., 4GB) linear address space for each process
- Reality
 - A limited (e.g., 1GB) amount of actual physical memory shared with many other processes
- How?



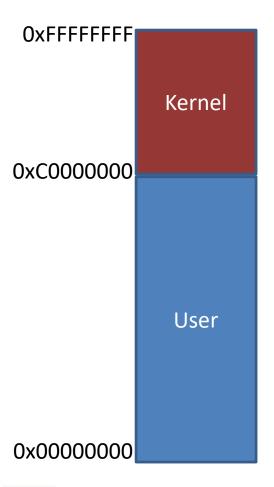


Properties of Virtual Memory

- Memory isolation among different processes
 - E.g., Process A cannot see process B's memory (vice versa.)
- What about memory isolation between kernel and user?
 - Q1. how does kernel map its own private memory?
 - Q2. how to prevent user processes from accessing the kernel mapped memory?



Kernel/User Virtual Memory



Kernel memory

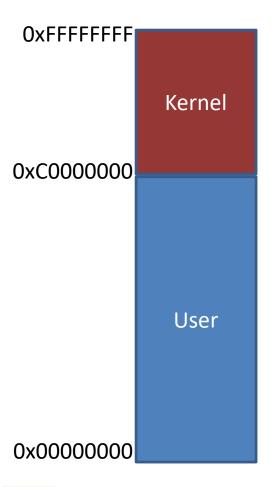
- Kernel code, data
- Identical to all address spaces
- Fixed 1-1 mapping of physical memory

User memory

- Process code, data, heap, stack,...
- Unique to each address space
- On-demand mapping (page fault)



Kernel/User Virtual Memory



- Every user-process has mappings to kernel memory
- But the kernel memory is only accessible at the kernel mode
 - when you execute system calls or interrupt handlers.
- Benefits of this design: **Performance**
 - Kernel can move data between user memory and kernel memory easily w/o changing the address space.



ARM Page Table

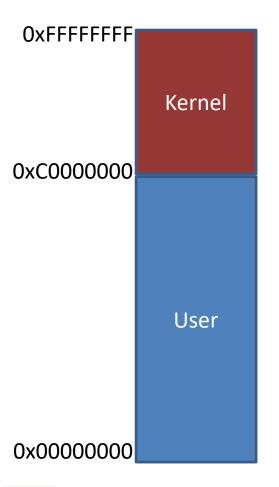
| | | | | | | | | - | \longrightarrow | | - |
|------------|------------------------------------|--|--------|---|---------------|--------------|-------------|---|-------------------|---|--------|
| Small page | Small page base address, PA[31:12] | | n G | s | A P [2] | TEX [2:0] | AP [1:0] | С | В | 1 | X N |

Table B3-4 VMSAv7 MMU access permissions

| AP[2] | AP[1:0] | Privileged permissions | User permissions | Description | |
|-------|---------|------------------------|---------------------|--|--|
| 0 | 00 | No access | No access | All accesses generate Permission faults | |
| 0 | 01 | Read/write | No access | Privileged access only | |
| 0 | 10 | Read/write | Read-only | Writes in User mode generate Permission faults | |
| 0 | 11 | Read/write | Read/write | Full access | |
| 1 | 00 | - | - | Reserved | |
| 1 | 01 | Read-only | No access | Privileged read-only | |
| 1 | 10 | Read-only | Read-only | Privileged and User read-only, deprecated in VMSAv7a | |
| 1 | 11 | Read-only | Read-only | Privileged and User read-only ^b | |



Kernel/User Virtual Memory



 Meltdown tricks the CPU so that the user can access its kernel memory

- How?
 - By exploiting weaknesses in Intel's outof-order execution engine

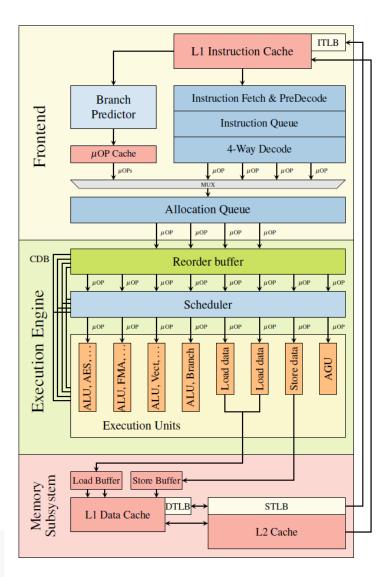


Out-of-Order Execution

- Background
 - A cache-miss can take ~100 cycles
 - Idling CPU while waiting data from memory is bad
- Out-of-order execution
 - A technique to minimize data waiting time by executing future instructions
 - Introduced in 1967 (Tomasulo algorithm)
- Most (all) high-performance CPUs use OoOE
 - Intel, AMD, ARM,



Out-of-Order Execution



- Instructions are fetched into a queue
- Any instructions
 whose data (operands)
 are ready are executed
 out-of-order (subject
 to data dependency)
- Results are "retired" in-order.



Speculative Execution

```
If (condition)
  Do something A1
  Do something A2
  Do something A3
} else {
  Do something B1
  Do something B2
  Do something B3
```

- Guess which branch to take.
- Speculatively execute instructions in the likely branch.
- If guessed wrong, squash the results
- But the side-effect (cache state change) remain



```
1 ; rcx = kernel address
2 ; rbx = probe array
3 retry:
4 mov al, byte [rcx]
5 shl rax, Oxc
6 jz retry
7 mov rbx, qword [rbx + rax]
```

This is it.



```
1 ; rcx = kernel address
2 ; rbx = probe array
3 retry:
4 mov al, byte [rcx]
5 shl rax, 0xc
6 jz retry
7 mov rbx, qword [rbx + rax]
```

 Step 1: load an attacker chosen kernel address into a register. This would raise an exception but it may take some time.



```
1 ; rcx = kernel address
2 ; rbx = probe array
3 retry:
4 mov al, byte [rcx]
5 shl rax, Oxc
6 jz retry
7 mov rbx, qword [rbx + rax]
```

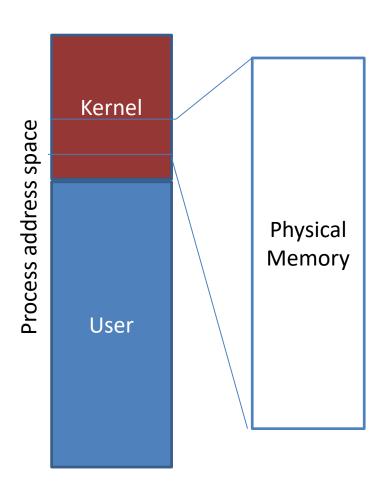
• Step 2: access memory based on the secret content of the *rax* register. This access will be in the cache.

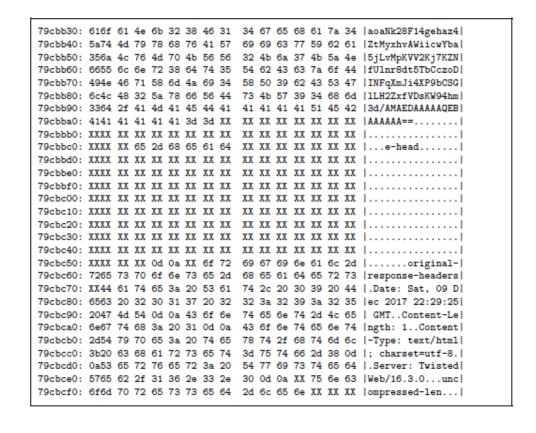


```
1 ; rcx = kernel address
2; rbx = probe array
3 retry:
4 mov al, byte [rcx]
5 shl rax, 0xc
6 jz retry
7 mov rbx, qword [rbx + rax]
```

 Step 3: measure the access timing of the probe array (one per page) to determine which is in the cache. Which in turn tell what was the content of the kernel address [rcx]







• Entire physical memory is 1-to-1 mapped into part of kernel memory. So, you can dump it.



Summary

- System security
 - Increasingly important
 - Bank accounts (money theft)
 - Cars (remote control of steering, breaking of Jeep cars)
 - Airplanes (remote control of drones)
 - Understand threats in both software and hardware
 - Exploiting the threats often require deep understanding in OS

