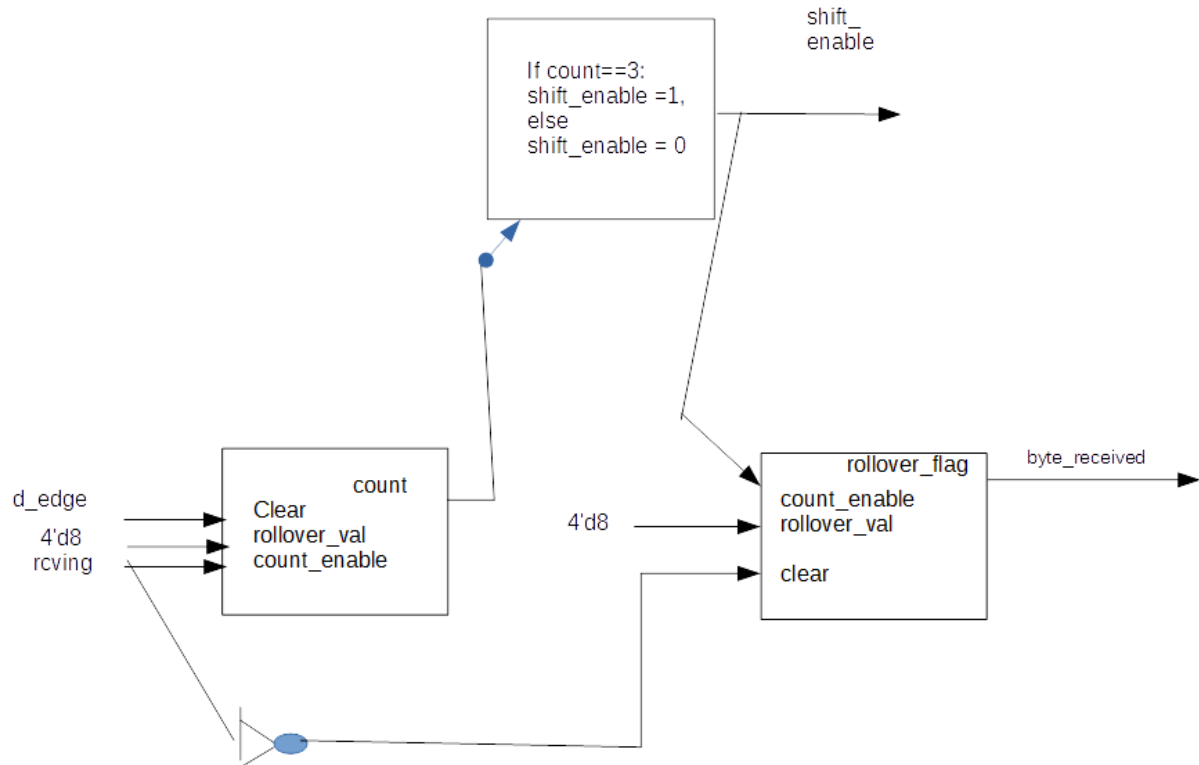
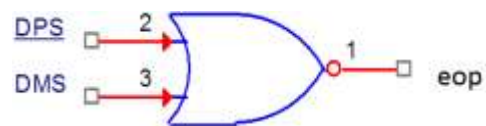


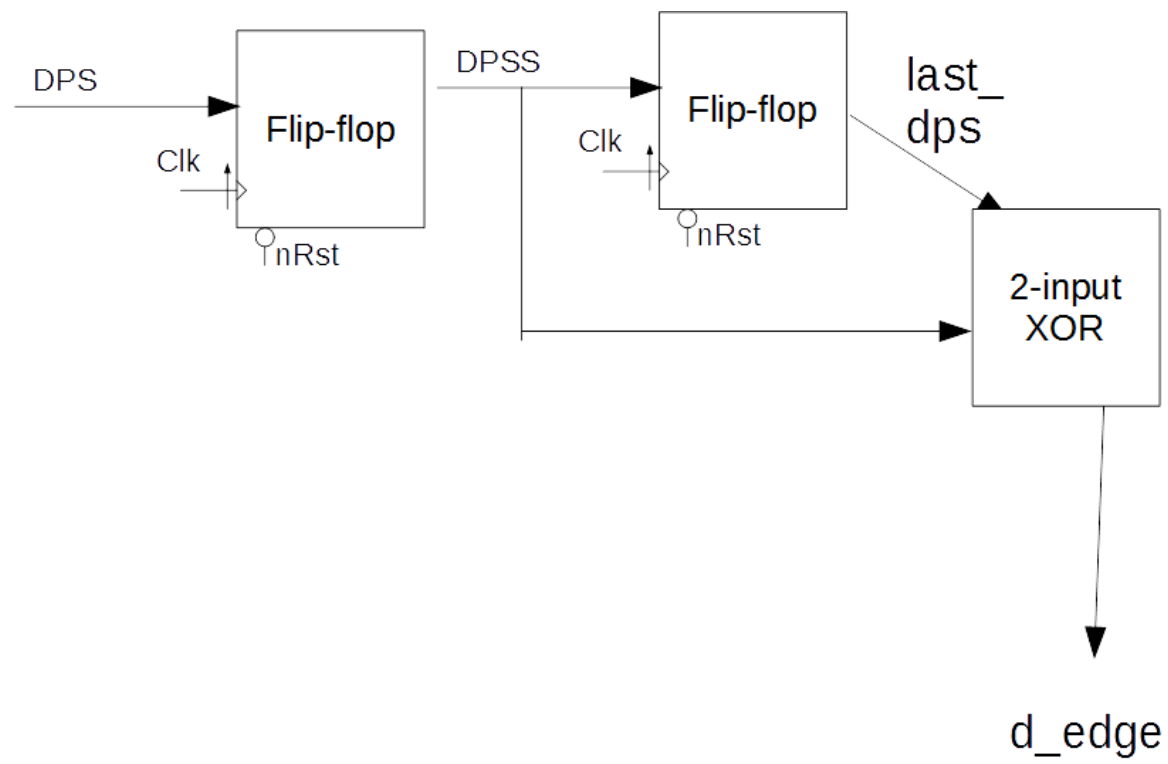
TIMER block diagram using flex counters



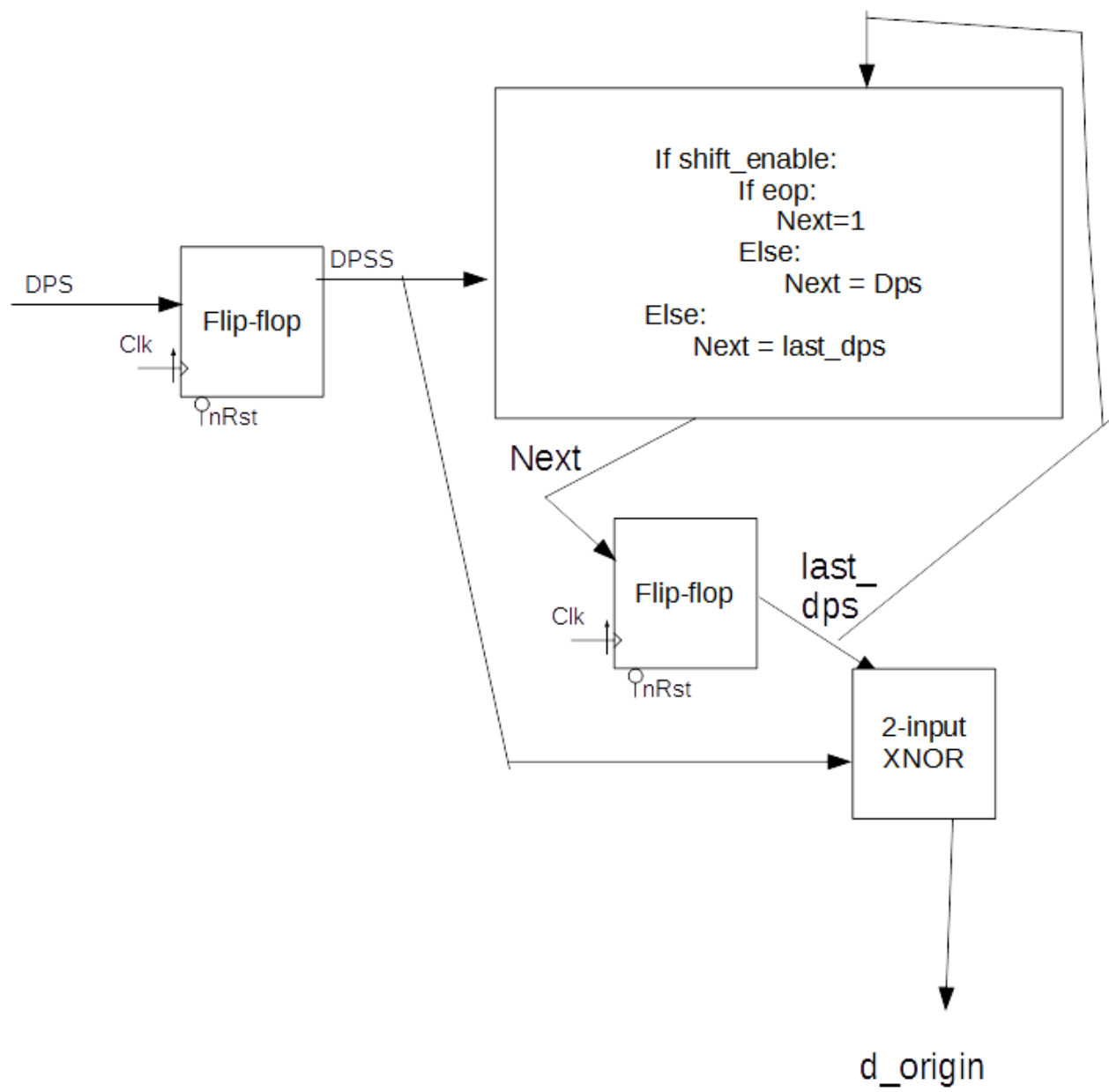
EOP DETECTOR



EDGE DETECT



DECODE RTL



CONTROLLER RTL

