ESP32 WROOM

Pinos de Entrada e Saída / Pefrifericos

Manuel Lameira V1.0

XTAL / SPI	RTC / UART	TOUCH / DAC	ADC	GPIO	PIN			PIN	GPIO	ADC	TOUCH	RTC / UART	SPI
						× ×							
	Power supply.				VDD 33			GND					
	Chip-enable signal. Active high.				EN	WIF ESP-WROOM-32	223	IO 23	GPIO 23				VSPI D
	RTC_GIPO 0		ADC 1_0	GPIO 36	SEN_VP	(C ()	₹ 1	IO 22	GPIO 22			U0 RTS	VSPI WP
	RTC_GIPO 3		ADC 1_3	GPIO 39	SEN_VN	R 211-101007	를 함(*)	TX D0	GPIO 1			U0 TXD	
	RTC_GIPO 4		ADC 1_6	GPIO 34	IO 34	FCC ID 2AC72 ESPWROOMS2	B B	RX D0	GPIO 3			U0 RXD	
	RTC_GIPO 5		ADC 1_7	GPIO 35	IO 35			IO 21	GPIO 21				VSPI HD
XTAL_P 32K	RTC_GIPO 9	Touch 9	ADC 1_4	GPIO 32	IO 32			GND					
XTAL_N 32K	RTC_GIPO 8	Touch 8	ADC 1_5	GPIO 33	IO 33	C)ğ ——nsaassaasa		IO 19	GPIO 19			U0 CTS	VSPI Q
	RTC_GIPO 6	DAC 1	ADC 2_8	GPIO 25	IO 25	(*) SZ0	2	IO 18	GPIO 18				VSPI CLK
	RTC_GIPO 7	DAC 2	ADC 2_9	GPIO 26	IO 26		ESB ESS	IO 5	GPIO 5				VSPI CSO
	RTC_GIPO 17	Touch 7	ADC 2_7	GPIO 27	IO 27			IO 17	GPIO 17			U2 TXD	
HSPI CLK	RTC_GIPO 16	Touch 6	ADC 2_6	GPIO 14	IO 14			IO 16	GPIO 16			U2 RXD	
HSPI Q	RTC_GIPO 15	Touch 5	ADC 2_5	GPIO 12	IO 12	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		10 4	GPIO 4	ADC 2_0	Touch 0	RTC_GIPO 10	HSPI HD
					GND	2		10 0	GPIO 0	ADC 2_1	Touch 1	RTC_GIPO 11	
HSPI D	RTC_GIPO 14	Touch 4	ADC 2_4	GPIO 13	IO 13		7 S()	10 2	GPIO 2	ADC 2_2	Touch 2	RTC_GIPO 12	HSPI WP
SPI HD	U1 RXD			GPIO 9	SD 2	(a) (b) (b) (c) (c) (c) (c) (c) (c) (c) (c) (c) (c		IO 15	GPIO 15	ADC 2_3	Touch 3	RTC_GIPO 13	HSPI CSO
SPI WP	U1 TXD			GPIO 10	SD 3	()8	E()	SD 1	GPIO 8			U2 CTS	SPI D
SPI CSO	U1 RTS			GPIO 11	CMD		E S	SD 0	GPIO 7			U2 RTS	SPI Q
					5V	(V) 8) Š(CLK	GPIO 6			U1 CTS	SPI CLK