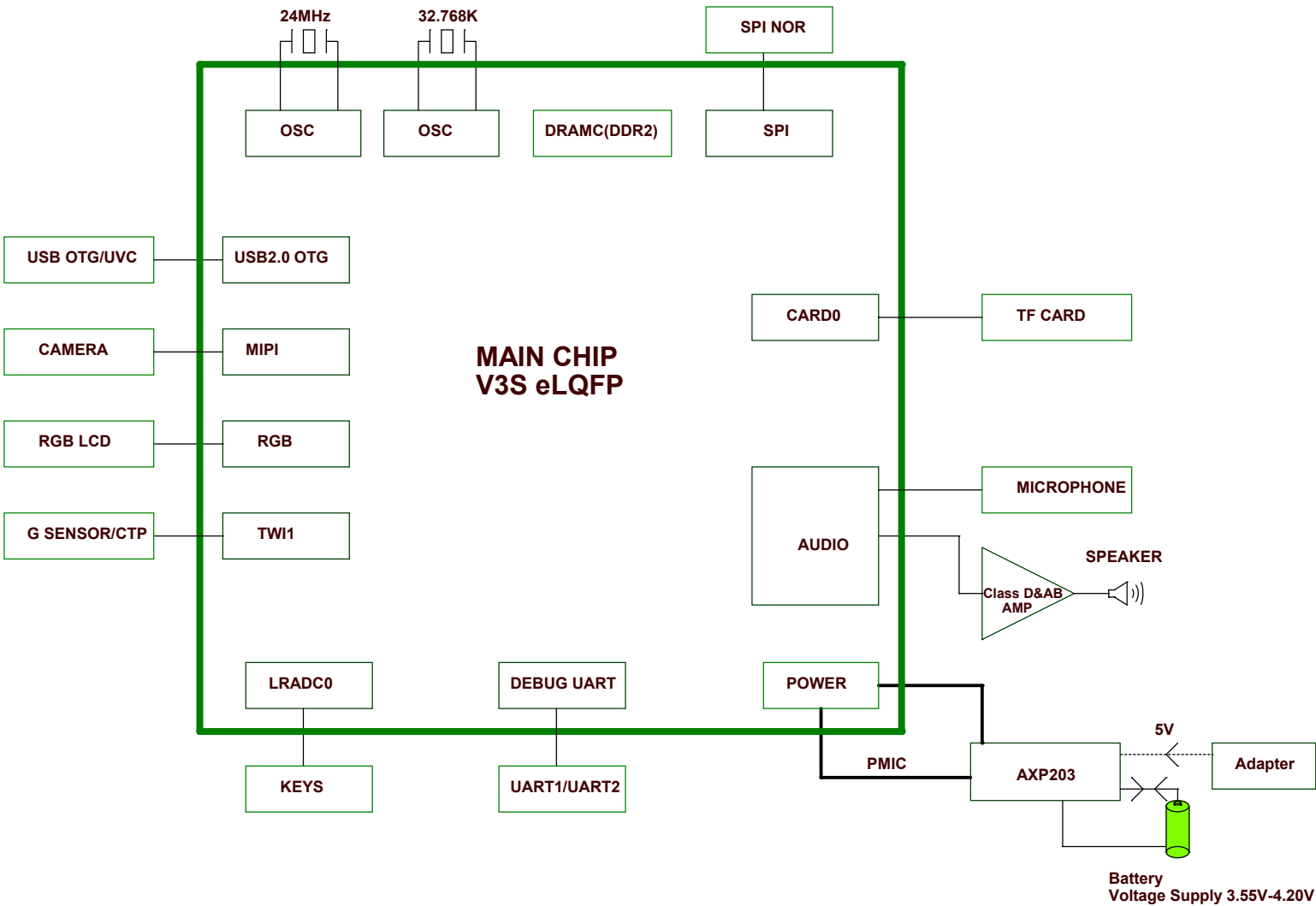


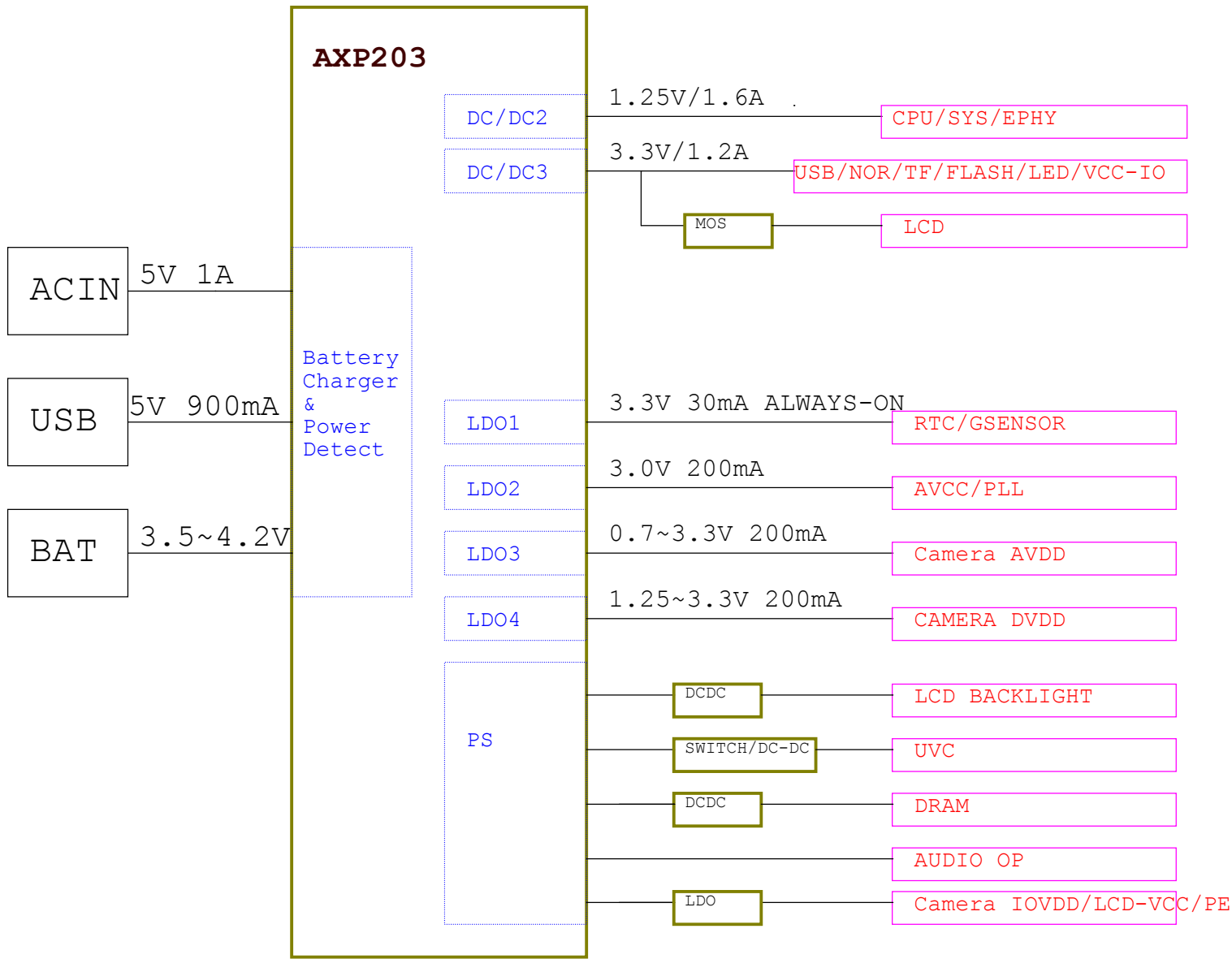
Schematics Index:

- P01: REVISION HISTORY
P02: BLOCK
P03: POWER TREE
P04: GPIO ASSIGNMENT
P05: CPU
P06: POWER
P07: CAMERA-MIPI
P08: RGB LCD 2.7
P09: NOR NANDFlash/TF Card
P10: SPEAKER/MICROPHONE/HEADPHONE/GPS
P11: USB/WIFI/KEY/GSENSOR
P12: OPTION:RGB LCD 4.3 / CTP

Revision	Description	Date	Drawn	Checked
Ver 0.1	Initial	2015-02-04	HW	
Ver 1.0	changelist.txt	2015-05-14	WJW	

BLOCK DIAGRAM





PIN	Define	CFG	Function
PB0	CTP-WAKE	1	CTP
PB1	CTP-INT	0	
PB2	LCD-BL-EN1	1	LCD
PB3	LCD-PWR-EN1	1	
PB4	PWM0	2	AUDIO
PB5	PA-SHDN	1	
PB6	TWI0-SCK	2	PMU
PB7	TWI0-SDA	2	
PB8	TWI1_SCK	2	G-SENSOR
PB9	TWI1_SDA	2	

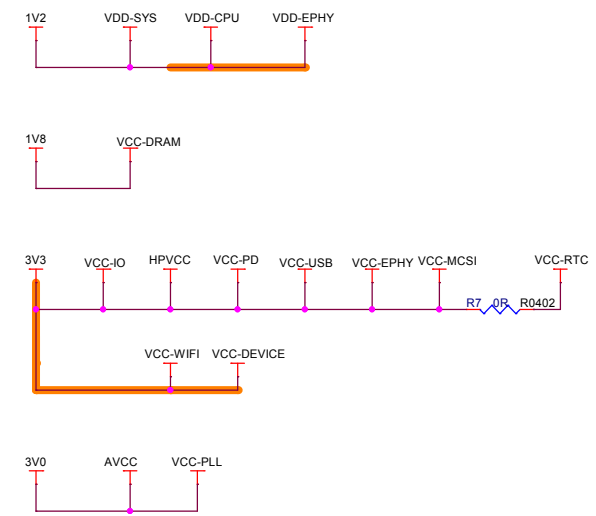
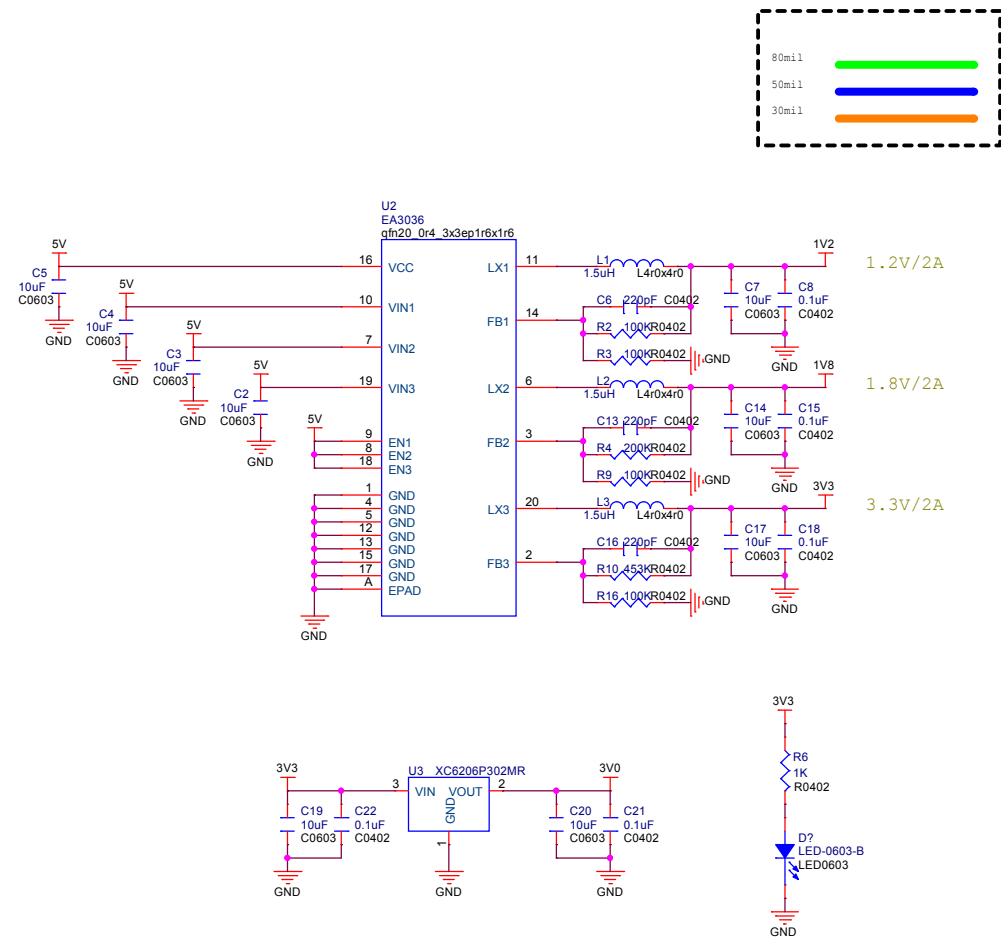
PIN	Define	CFG	Function
PC0	SPI0_MISO	3	NOR/ NAND
PC1	SPI0_CLK	3	
PC2	SPI0_CS	3	
PC3	SPI0_MOSI	3	

PIN	Define	CFG	Function
PE0	LCD_CLK	3	LCD
PE1	LCD_DE	3	
PE2	LCD_HSYNC	3	
PE3	LCD_VSYNC	3	
PE4	LCD_D2	3	
PE5	LCD_D3	3	
PE6	LCD_D4	3	
PE7	LCD_D5	3	
PE8	LCD_D6	3	
PE9	LCD_D7	3	
PE10	LCD_D8	3	
PE11	LCD_D11	3	
PE12	LCD_D12	3	
PE13	LCD_D13	3	
PE14	LCD_D14	3	
PE15	LCD_D15	3	
PE16	LCD_D18	3	
PE17	LCD_D19	3	
PE18	LCD_D20	3	
PE19	LCD_D21	3	
PE20	CSI-MIPI-MCLK3	3	MIPI
PE21	CSI-SDA	2	
PE22	CSI-SCK	2	LCD
PE23	LCD_D22	3	
PE24	LCD_D23	3	

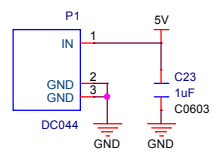
PIN	Define	CFG	Function
PF0	SDC0-D1	2	TF CARD
PF1	SDC0-D0	2	
PF2	SDC0-CLK	2	
PF3	SDC0-CMD	2	
PF4	SDC0-D3	2	
PF5	SDC0-D2	2	
PF6	SDC0-DET	0	

PIN	Define	CFG	Function
PG0	MIPI-CSI-RESET	1	GPIO
PG1	MIPI-CSI-PWDN	1	
PG2	GS-INT	0	
PG3	USB-DRVBUS	1	
PG4	USB-ID	0	
PG5	PMU-IRQ	0	

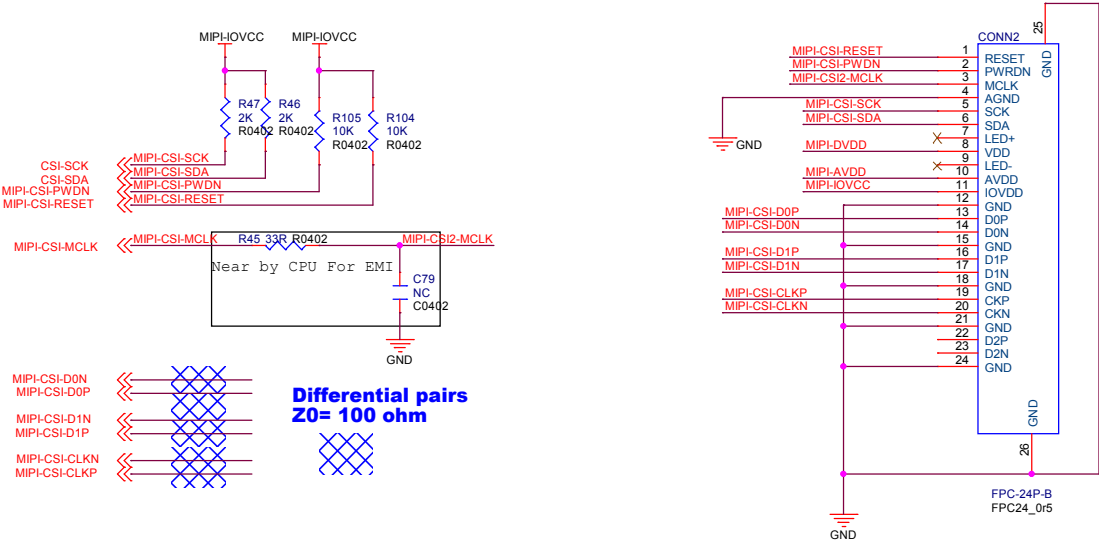
POWER



5V DC INPUT



CAMERA



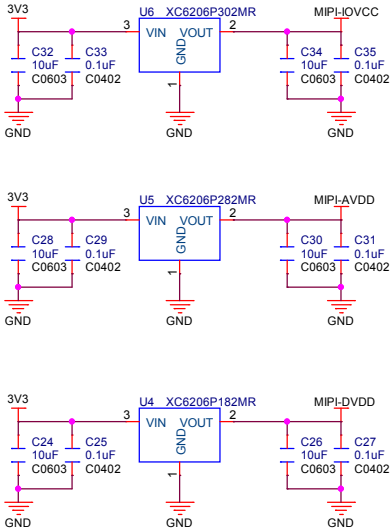
CAMERA POWER

AR0330:
AVDD 2.8V (2.7V~2.9V)
DVDD 1.8V (1.7V~1.9V)
IOVCC 3.0V (2.7V~3.1V)

OV2710:
AVDD 3.3V (3.0V~3.6V)
DVDD 1.5V (1.425V~1.575V)
IOVCC 3.0V (1.7V~3.6V)

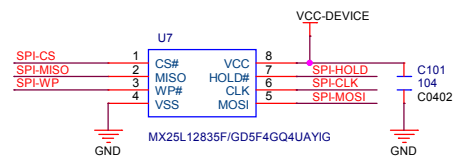
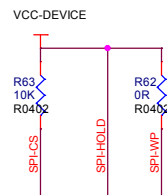
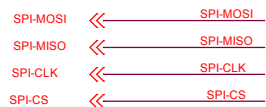
JX-H22
AVDD 2.8V (2.6V~3.0V)
DVDD 2.8V (2.6V~3.0V)
IOVCC 3.0V (1.7V~3.0V)

GC1004:
AVDD 3.3V (3.0V~3.6V)
DVDD 1.6V (1.5V~1.8V)
IOVCC 3.0V (1.7V~3.6V)



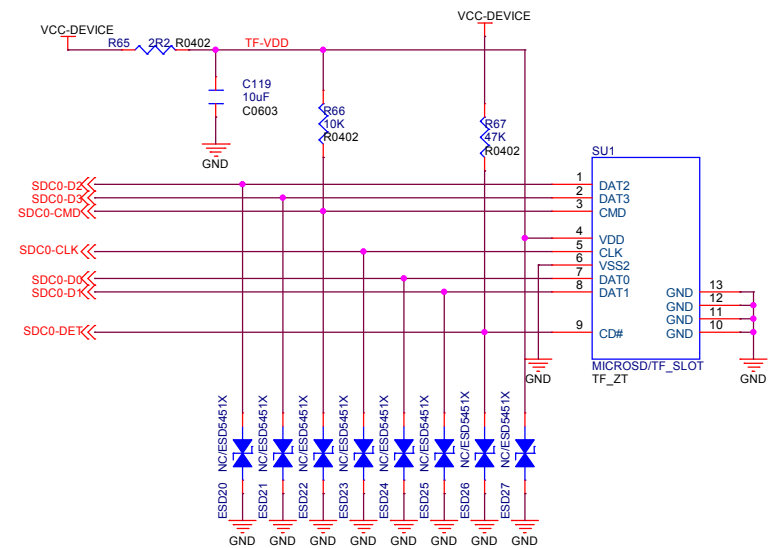
Flash

NOR/NAND



U7 Mount MX25L12835F

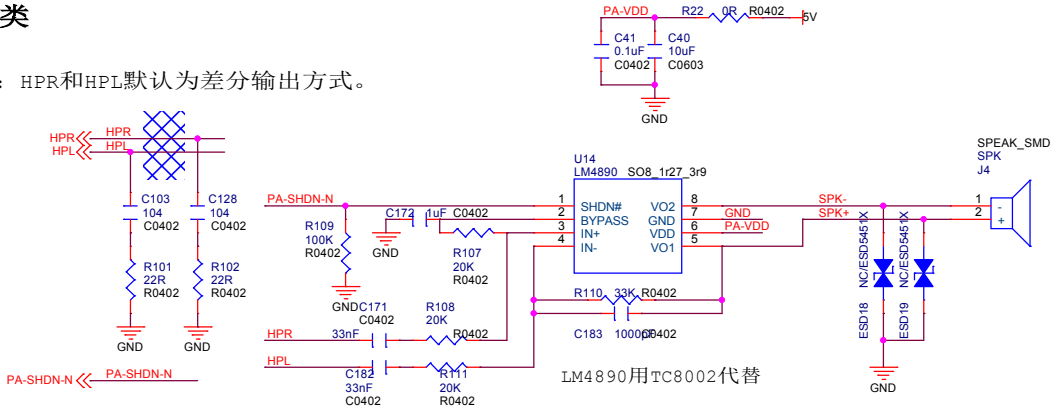
SD CARD



Speaker

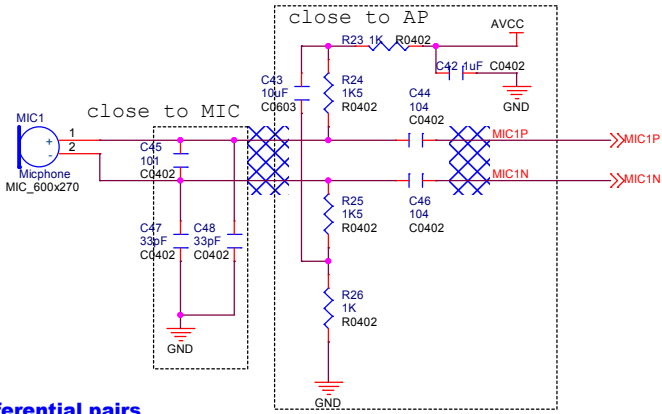
AB类

注：HPR和HPL默认为差分输出方式。



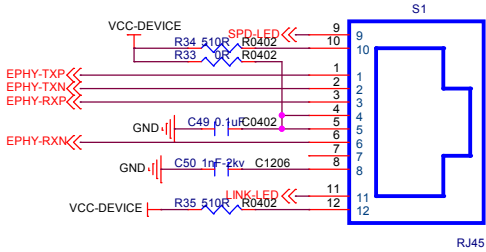
Differential pairs

Analog Microphone (Main MIC)

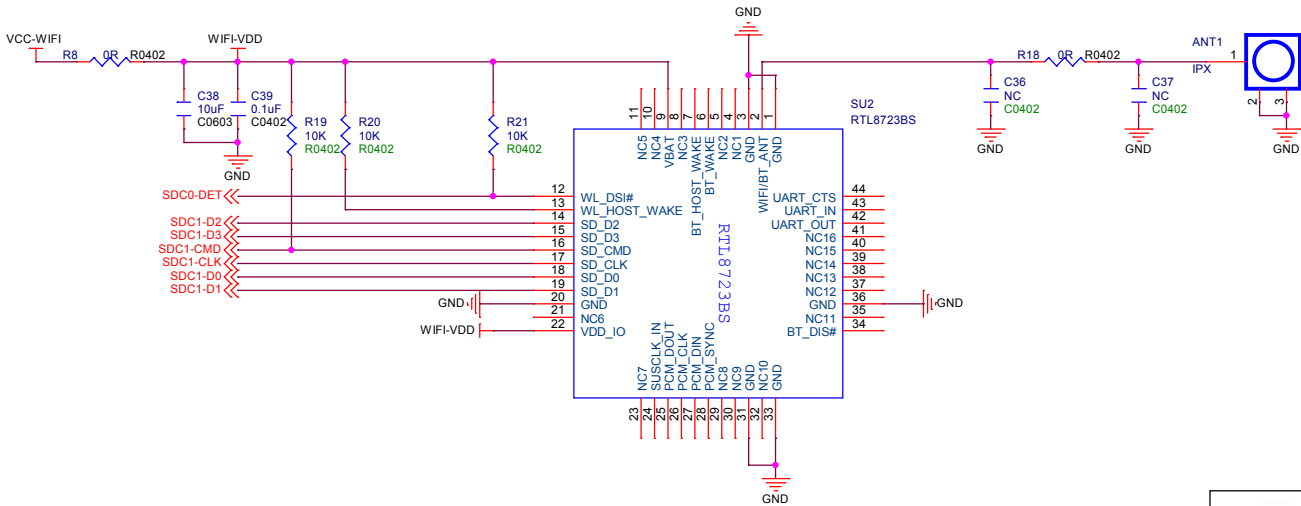


Differential pairs

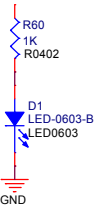
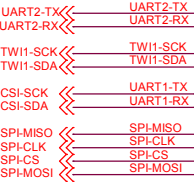
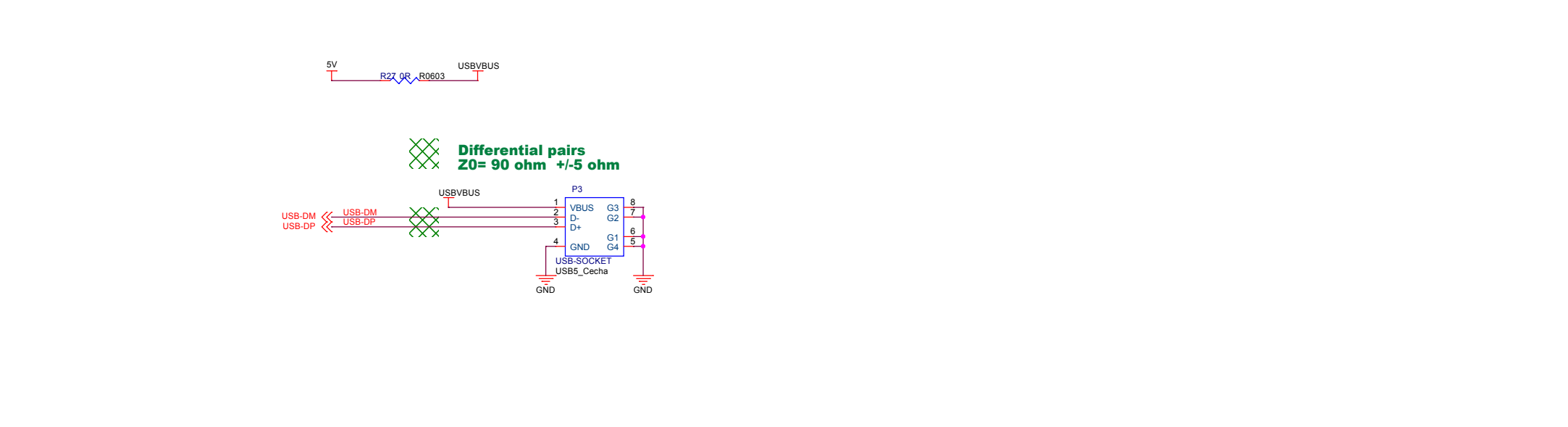
ETHERNET



SDIO WIFI

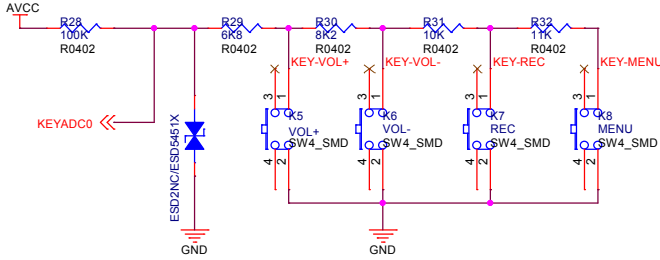


USB OTG

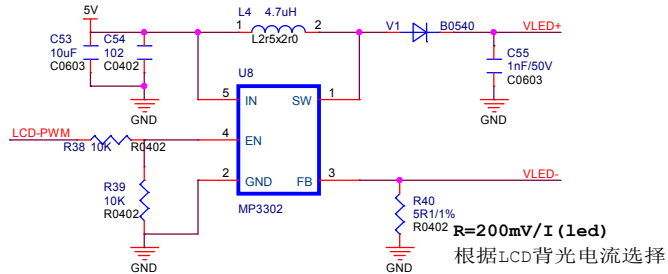
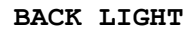
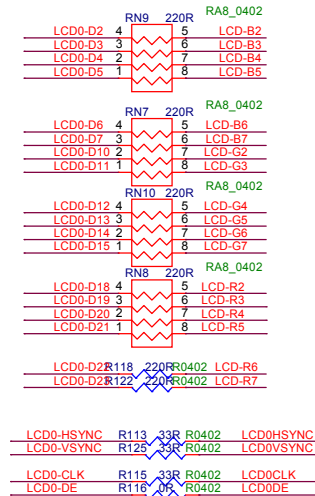


KEY

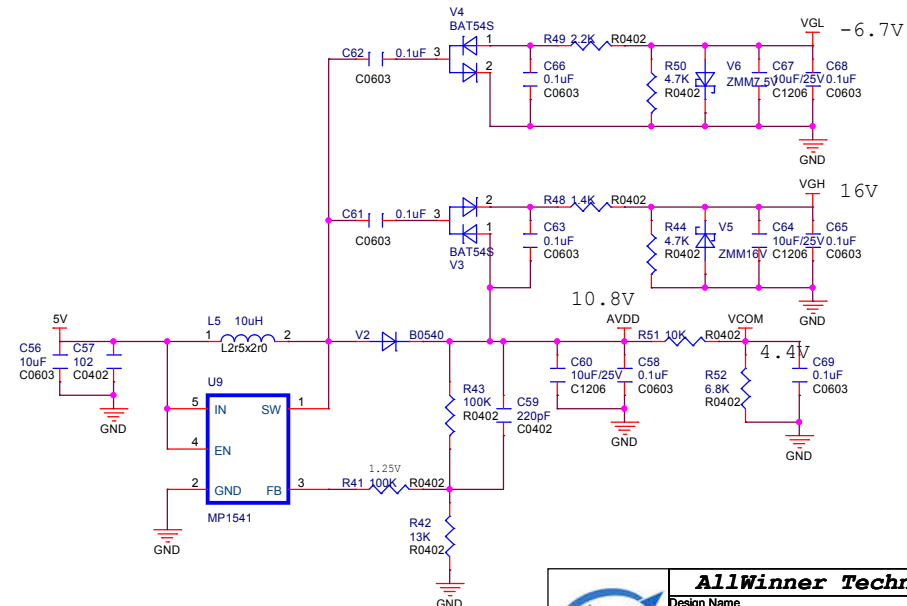
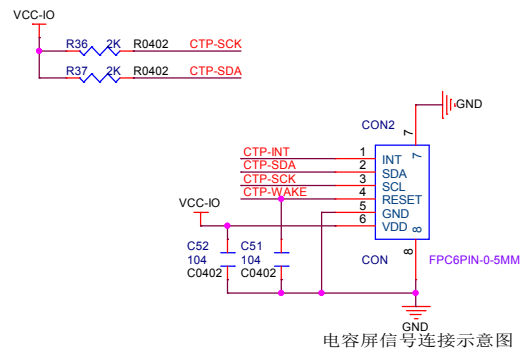
注：每隔0.2V一个按键



7" 480*800



CTP-WAKE <== CTP-WAKE
CTP-INT <== CTP-INT
TWIO-SCK <== CTP-SCK
TWIO-SDA <== CTP-SDA



AllWinner Technology Co.,Ltd

Design Name

V3S_STD_CDR_V1_0

Size A3	Page Name RGB LCD
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Rev

Date: Thursday, March 22, 2018

Sheet 12 of 12