1. Description

1.1. Project

Project Name	quadrar-loraone-devboard-demo
Board Name	NUCLEO-L152RE
Generated with:	STM32CubeMX 6.0.1
Date	10/30/2020

1.2. MCU

MCU Series	STM32L1
MCU Line	STM32L151/152
MCU name	STM32L152RETx
MCU Package	LQFP64
MCU Pin number	64

1.3. Core(s) information

|	
Core(s)	Arm Cortex-M3

2. Pinout Configuration
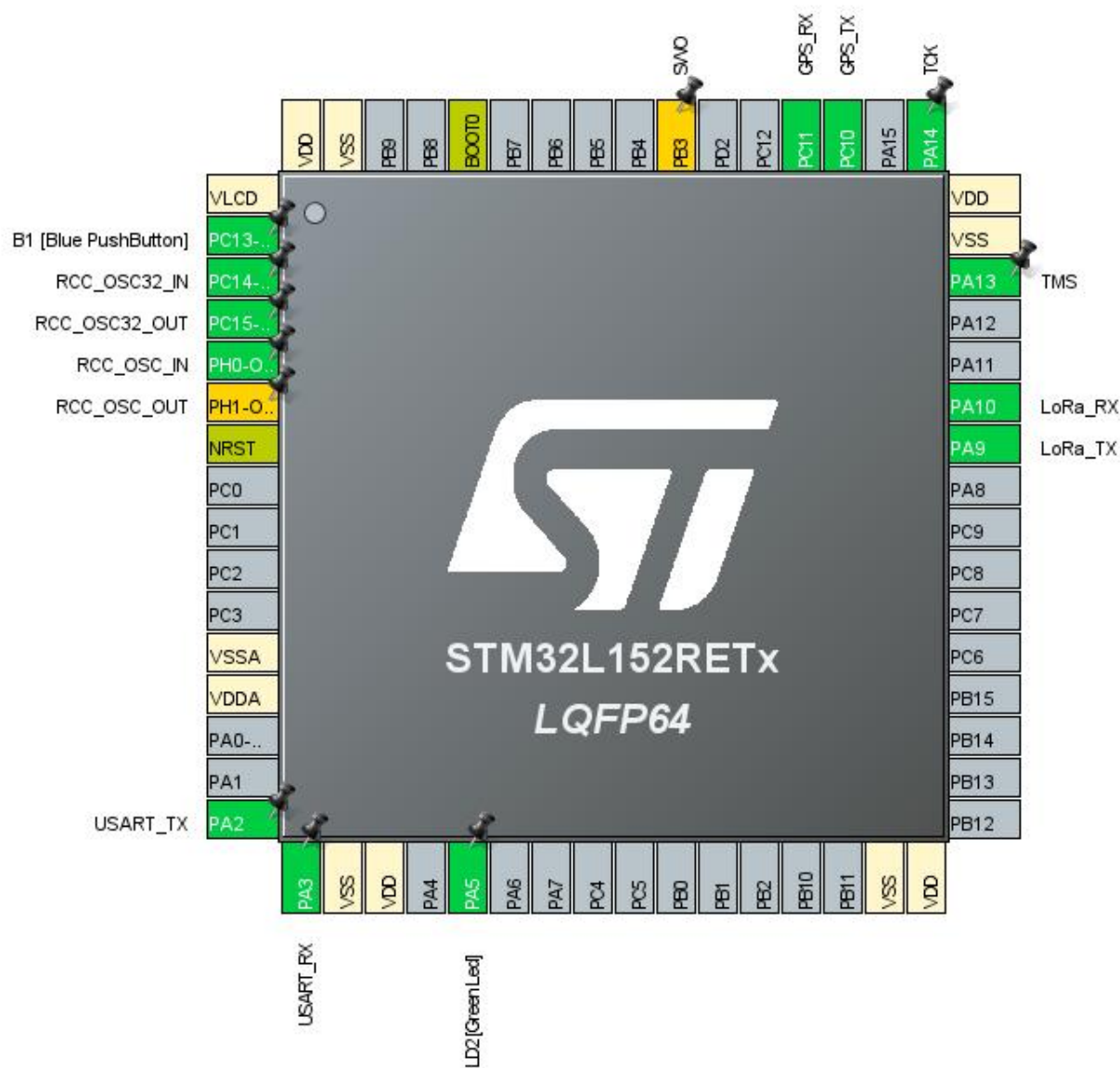
3. Pins Configuration

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VLCD	Power		
2	PC13-WKUP2	I/O	GPIO_EXTI13	B1 [Blue PushButton]
3	PC14-OSC32_IN	I/O	RCC_OSC32_IN	
4	PC15-OSC32_OUT	I/O	RCC_OSC32_OUT	
5	PH0-OSC_IN	I/O	RCC_OSC_IN	
6	PH1-OSC_OUT *	I/O	RCC_OSC_OUT	
7	NRST	Reset		
12	VSSA	Power		
13	VDDA	Power		
16	PA2	I/O	USART2_TX	USART_TX
17	PA3	I/O	USART2_RX	USART_RX
18	VSS	Power		
19	VDD	Power		
21	PA5 **	I/O	GPIO_Output	LD2 [Green Led]
31	VSS	Power		
32	VDD	Power		
42	PA9	I/O	USART1_TX	LoRa_TX
43	PA10	I/O	USART1_RX	LoRa_RX
46	PA13	I/O	SYS_JTMS-SWDIO	TMS
47	VSS	Power		
48	VDD	Power		
49	PA14	I/O	SYS_JTCK-SWCLK	TCK
51	PC10	I/O	UART4_TX	GPS_TX
52	PC11	I/O	UART4_RX	GPS_RX
55	PB3 *	I/O	SYS_JTDO-TRACESWO	SWO
60	BOOT0	Boot		
63	VSS	Power		
64	VDD	Power		

** The pin is affected with an I/O function

 * The pin is affected with a peripheral function but no peripheral mode is activated

4. Clock Tree Configuration
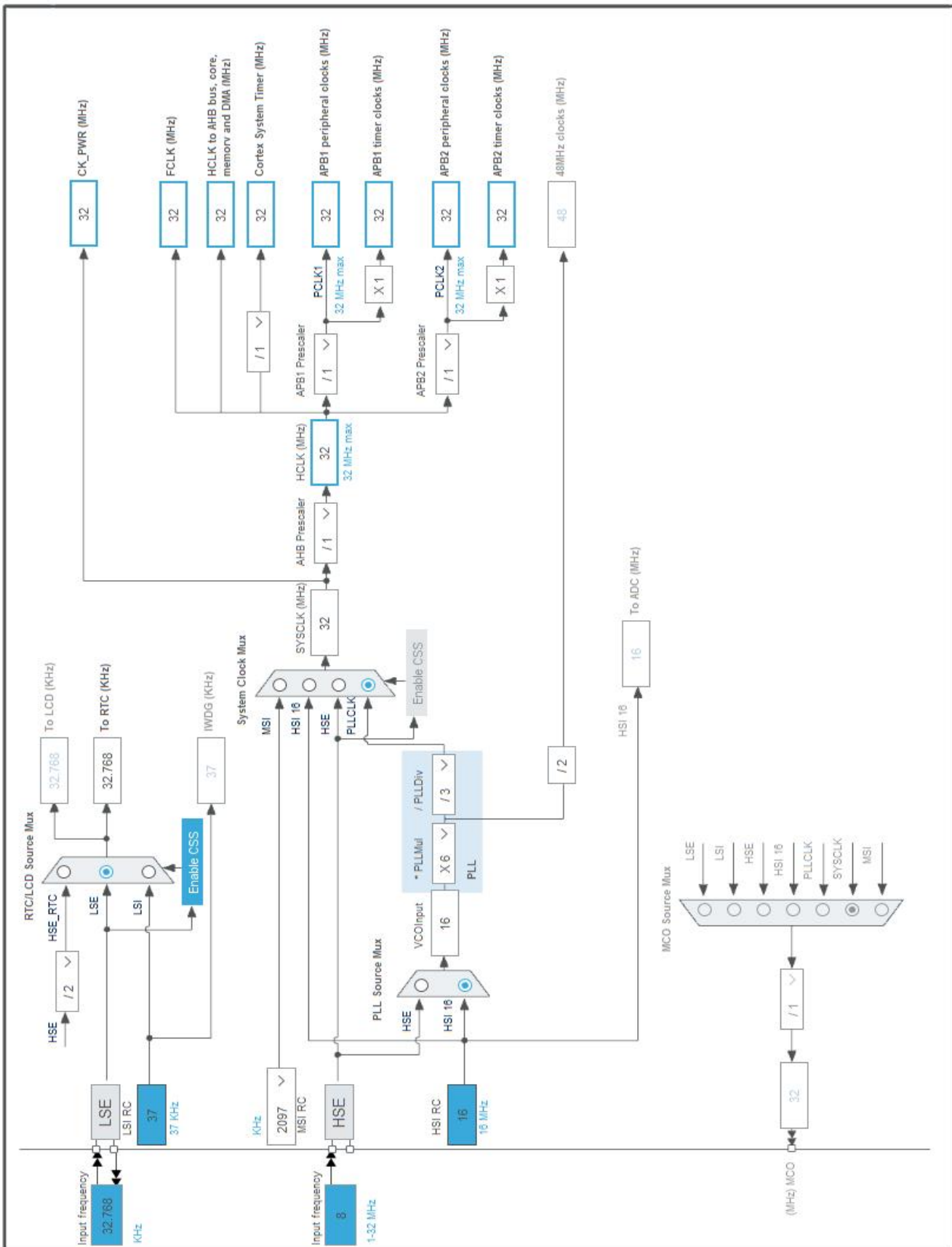
5. Software Project

5.1. Project Settings

Name	Value
Project Name	quadrar-loraone-devboard-demo
Project Folder	D:\Workspace\STM32CubeIDE\workspace_1.2.0\quadrar-loraone-devboard-
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_L1 V1.10.1
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	Yes
Enable Full Assert	No

5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	IP Instance Name
1	MX_GPIO_Init	GPIO
2	SystemClock_Config	RCC
3	MX_USART2_UART_Init	USART2
4	MX_RTC_Init	RTC
5	MX_UART4_Init	UART4
6	MX_USART1_UART_Init	USART1

6. Power Consumption Calculator report

6.1. Microcontroller Selection

Series	STM32L1
Line	STM32L151/152
MCU	STM32L152RETx
Datasheet	DS10002_Rev8

6.2. Parameter Selection

Temperature	25
Vdd	3.0

6.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

6.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP
Vdd	3.0	3.0
Voltage Source	Battery	Battery
Range	Range1-High	NoRange
Fetch Type	FLASH	n/a
CPU Frequency	32 MHz	0 Hz
Clock Configuration	HSI PLL	ALL CLOCKS OFF
Clock Source Frequency	16 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	8.7 mA	560 nA
Duration	0.1 ms	0.9 ms
DMIPS	33.0	0.0
Ta Max	103.8	105
Category	In DS Table	In DS Table

6.5. Results

Sequence Time	1 ms	Average Current	870.5 µA
Battery Life	5 months, 9 days, 22 hours	Average DMIPS	33.0 DMIPS

6.6. Chart

Consumption Profile by Step
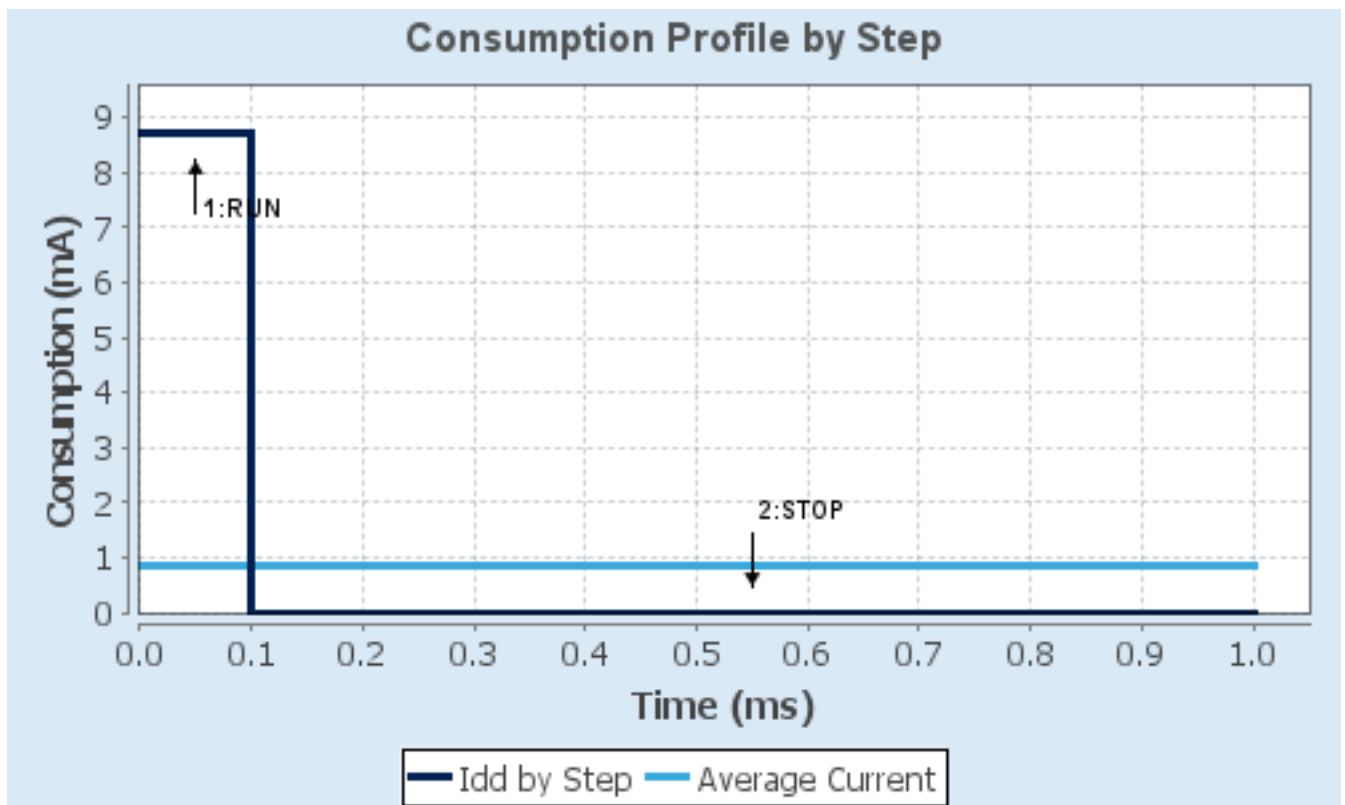
7. IPs and Middleware Configuration

7.1. GPIO

7.2. RCC
High Speed Clock (HSE): BYPASS Clock Source
Low Speed Clock (LSE) : Crystal/Ceramic Resonator
7.2.1. Parameter Settings:

System Parameters:

VDD voltage (V)	3.3
Instruction Cache	Enabled
Prefetch Buffer	Disabled
Data Cache	Enabled
Flash Latency(WS)	1 WS (2 CPU cycle)

RCC Parameters:

HSI Calibration Value	16
MSI Calibration Value	0
HSE Startup Timout Value (ms)	100
LSE Startup Timout Value (ms)	5000

Power Parameters:

Power Regulator Voltage Scale	Power Regulator Voltage Scale 1

7.3. RTC
mode: Activate Clock Source
WakeUp: Internal WakeUp
7.3.1. Parameter Settings:

General:

Hour Format	Hourformat 24
Asynchronous Predivider value	127
Synchronous Predivider value	255

Calendar Time:

Data Format	BCD data format
Hours	0
Minutes	0
Seconds	0
Day Light Saving: value of hour adjustment	Daylightsaving None

Store Operation	Storeoperation Reset

Calendar Date:

Week Day	Monday
Month	January
Date	1
Year	0

Wake UP:

Wake Up Clock	**1 Hz with 1 bit added to Wake Up Counter ***
Wake Up Counter	0

7.4. SYS
Debug: Serial Wire
Timebase Source: SysTick

7.5. UART4
Mode: Asynchronous
7.5.1. Parameter Settings:

Basic Parameters:

Baud Rate	**9600 ***
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples

7.6. USART1
Mode: Asynchronous
7.6.1. Parameter Settings:

Basic Parameters:

Baud Rate	**9600 ***
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples

7.7. USART2
Mode: Asynchronous
7.7.1. Parameter Settings:

Basic Parameters:

Baud Rate	115200
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples

* User modified value

8. System Configuration

8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	PC14-OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	
	PC15-OSC32_OUT	RCC_OSC32_OUT	n/a	n/a	n/a	
	PH0-OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
SYS	PA13	SYS_JTMS-SWDIO	n/a	n/a	n/a	TMS
	PA14	SYS_JTCK-SWCLK	n/a	n/a	n/a	TCK
UART4	PC10	UART4_TX	Alternate Function Push Pull	Pull-up	**High ***	GPS_TX
	PC11	UART4_RX	Alternate Function Push Pull	Pull-up	**High ***	GPS_RX
USART1	PA9	USART1_TX	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	LoRa_TX
	PA10	USART1_RX	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	LoRa_RX
USART2	PA2	USART2_TX	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	USART_TX
	PA3	USART2_RX	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	USART_RX
Single Mapped Signals	PH1-OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
	PB3	SYS_JTDO-TRACESWO	n/a	n/a	n/a	SWO
GPIO	PC13-WKUP2	GPIO_EXTI13	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	B1 [Blue PushButton]
	PA5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Very Low	LD2 [Green Led]

8.2. DMA configuration

nothing configured in DMA service

8.3. NVIC configuration

8.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Pre-fetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
RTC wake-up interrupt through EXTI line 20	true	0	0
USART1 global interrupt	true	0	0
UART4 global interrupt	true	0	0
Flash global interrupt	unused		
RCC global interrupt	unused		
USART2 global interrupt	unused		
EXTI line[15:10] interrupts	unused		

8.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	true	true	false
Hard fault interrupt	true	true	false
Memory management fault	true	true	false
Pre-fetch fault, memory access fault	true	true	false
Undefined instruction or illegal state	true	true	false
System service call via SWI instruction	true	true	false
Debug monitor	true	true	false
Pendable request for system service	true	true	false
System tick timer	true	true	true
RTC wake-up interrupt through EXTI line 20	true	true	true
USART1 global interrupt	true	true	true
UART4 global interrupt	true	true	true

* User modified value

9. System Views

9.1. Category view

9.1.1. Current

10. Docs & Resources

Type	Link
Datasheet	http://www.st.com/resource/en/datasheet/DM00098321.pdf
Reference manual	http://www.st.com/resource/en/reference_manual/CD00240193.pdf
Programming manual	http://www.st.com/resource/en/programming_manual/CD00228163.pdf
Errata sheet	http://www.st.com/resource/en/errata_sheet/DM00104204.pdf
Application note	http://www.st.com/resource/en/application_note/CD00160362.pdf
Application note	http://www.st.com/resource/en/application_note/CD00167594.pdf
Application note	http://www.st.com/resource/en/application_note/CD00211314.pdf
Application note	http://www.st.com/resource/en/application_note/CD00259245.pdf
Application note	http://www.st.com/resource/en/application_note/CD00264342.pdf
Application note	http://www.st.com/resource/en/application_note/CD00264379.pdf
Application note	http://www.st.com/resource/en/application_note/CD00270081.pdf
Application note	http://www.st.com/resource/en/application_note/CD00273528.pdf
Application note	http://www.st.com/resource/en/application_note/CD00280599.pdf
Application note	http://www.st.com/resource/en/application_note/DM00024853.pdf
Application note	http://www.st.com/resource/en/application_note/DM00025071.pdf
Application note	http://www.st.com/resource/en/application_note/DM00032987.pdf
Application note	http://www.st.com/resource/en/application_note/DM00040808.pdf
Application note	http://www.st.com/resource/en/application_note/DM00042534.pdf
Application note	http://www.st.com/resource/en/application_note/DM00073742.pdf
Application note	http://www.st.com/resource/en/application_note/DM00085385.pdf
Application note	http://www.st.com/resource/en/application_note/DM00087593.pdf
Application note	http://www.st.com/resource/en/application_note/DM00129215.pdf
Application note	http://www.st.com/resource/en/application_note/DM00158601.pdf
Application note	http://www.st.com/resource/en/application_note/DM00160482.pdf
Application note	http://www.st.com/resource/en/application_note/DM00188260.pdf

Application note   http://www.st.com/resource/en/application_note/DM00141025.pdf

Application note   http://www.st.com/resource/en/application_note/DM00220769.pdf

Application note   http://www.st.com/resource/en/application_note/DM00206898.pdf

Application note   http://www.st.com/resource/en/application_note/DM00257177.pdf

Application note   http://www.st.com/resource/en/application_note/DM00272912.pdf

Application note   http://www.st.com/resource/en/application_note/DM00226326.pdf

Application note   http://www.st.com/resource/en/application_note/DM00236305.pdf

Application note   http://www.st.com/resource/en/application_note/DM00296349.pdf

Application note   http://www.st.com/resource/en/application_note/DM00327191.pdf

Application note   http://www.st.com/resource/en/application_note/DM00354244.pdf

Application note   http://www.st.com/resource/en/application_note/DM00315319.pdf

Application note   http://www.st.com/resource/en/application_note/DM00380469.pdf

Application note   http://www.st.com/resource/en/application_note/DM00395696.pdf

Application note   http://www.st.com/resource/en/application_note/DM00445657.pdf

Application note   http://www.st.com/resource/en/application_note/DM00493651.pdf

Application note   http://www.st.com/resource/en/application_note/DM00536349.pdf

Application note   http://www.st.com/resource/en/application_note/DM00660597.pdf