

A

10

B

10

C

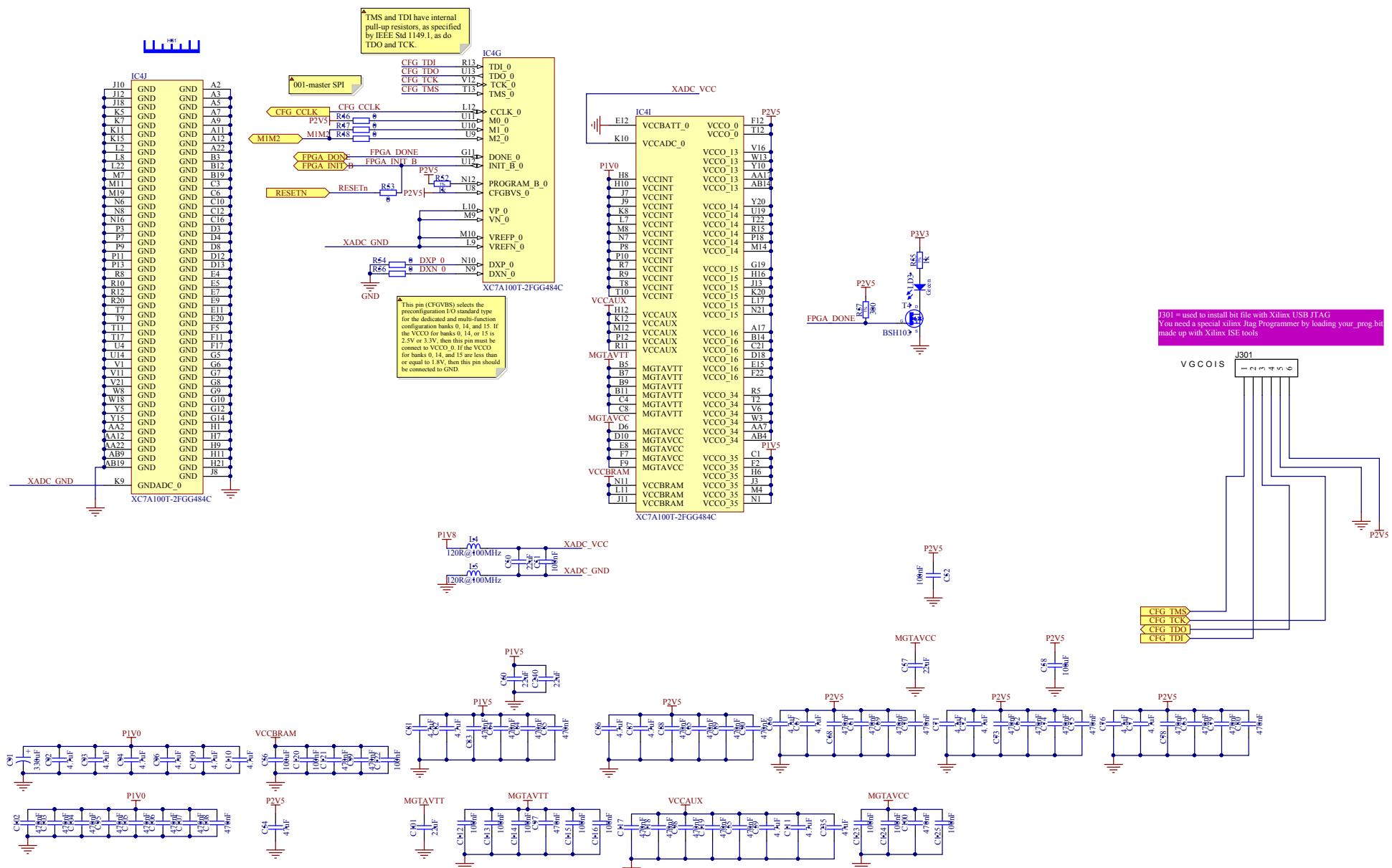
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D

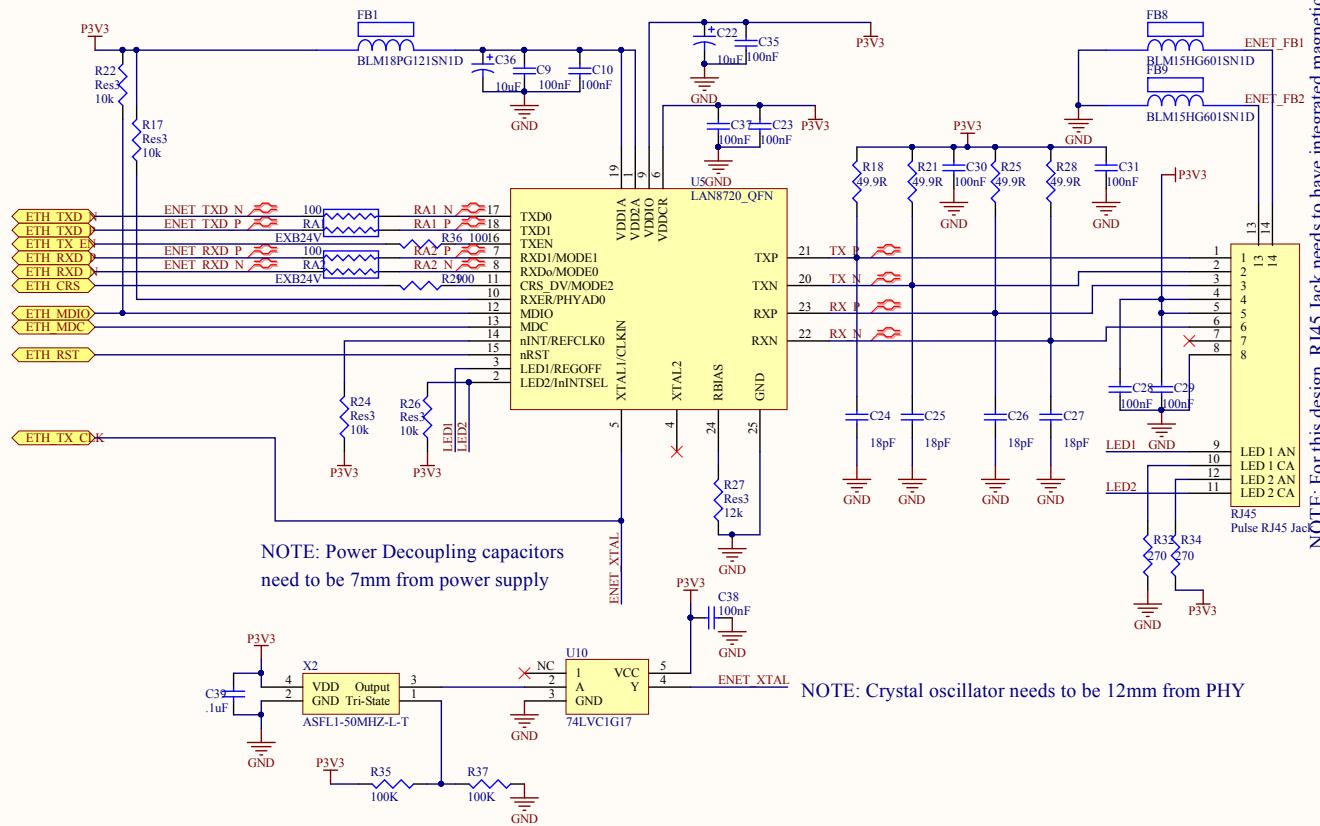
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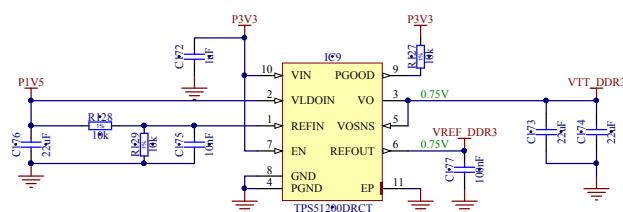
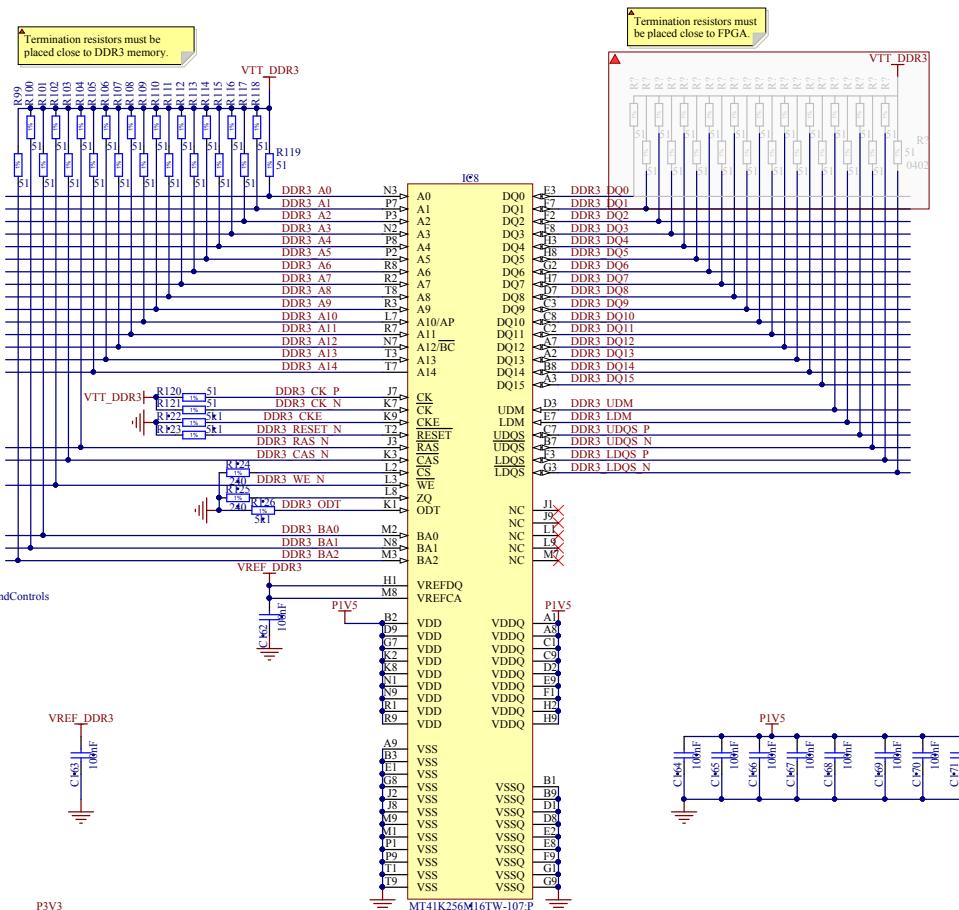
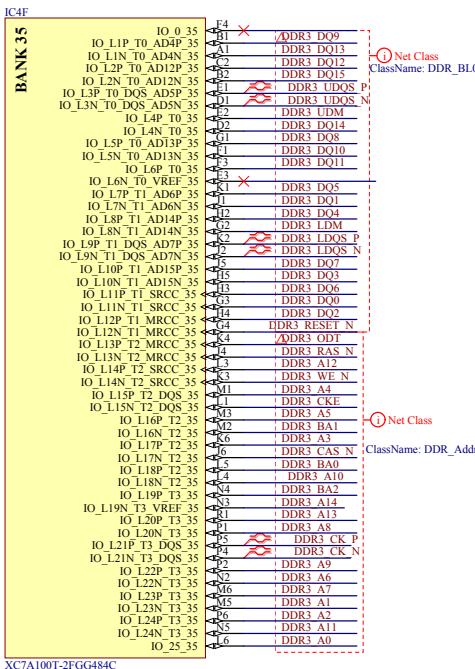


Ethernet Physical Layer (SMSC LAN8720)

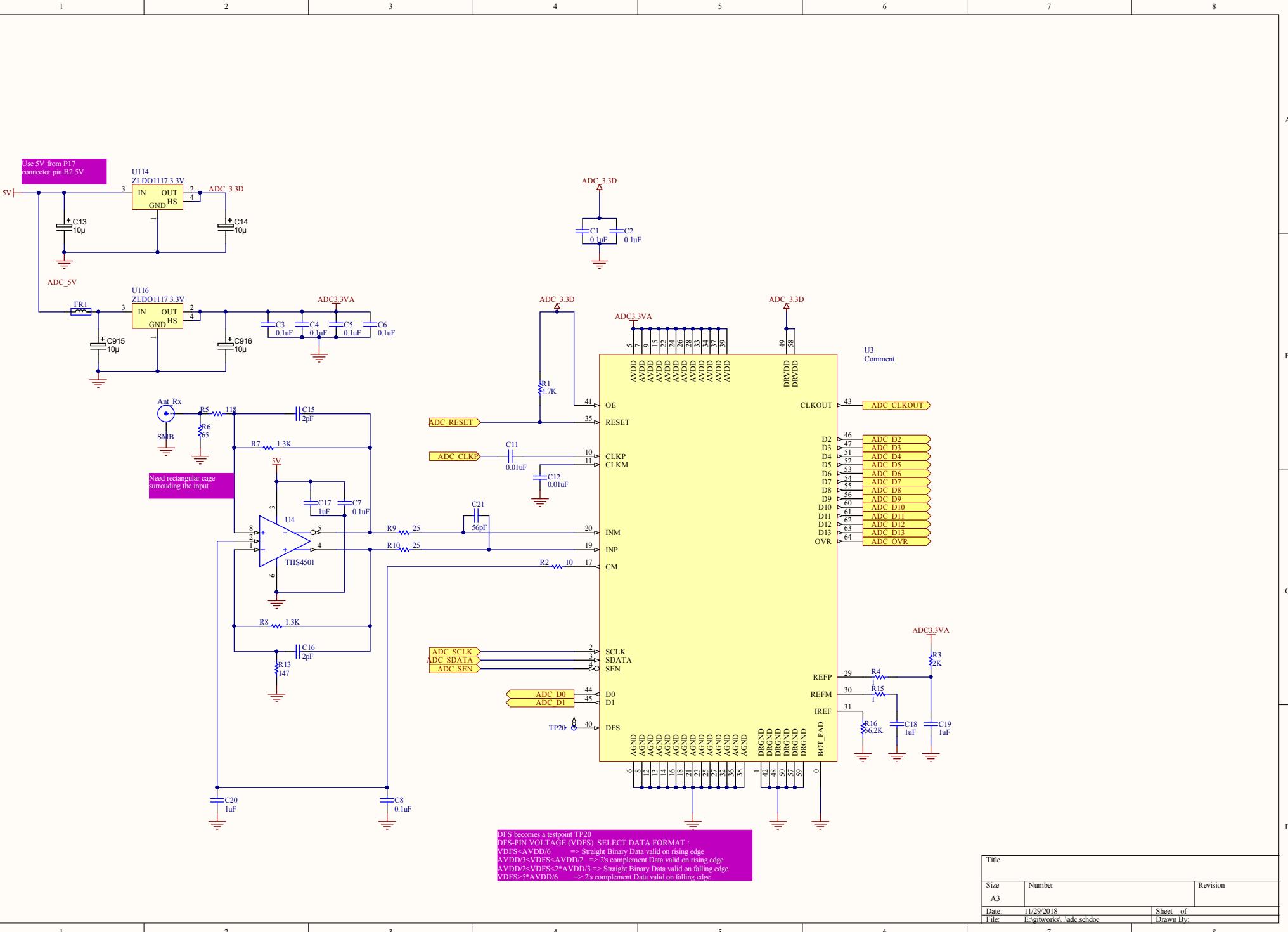


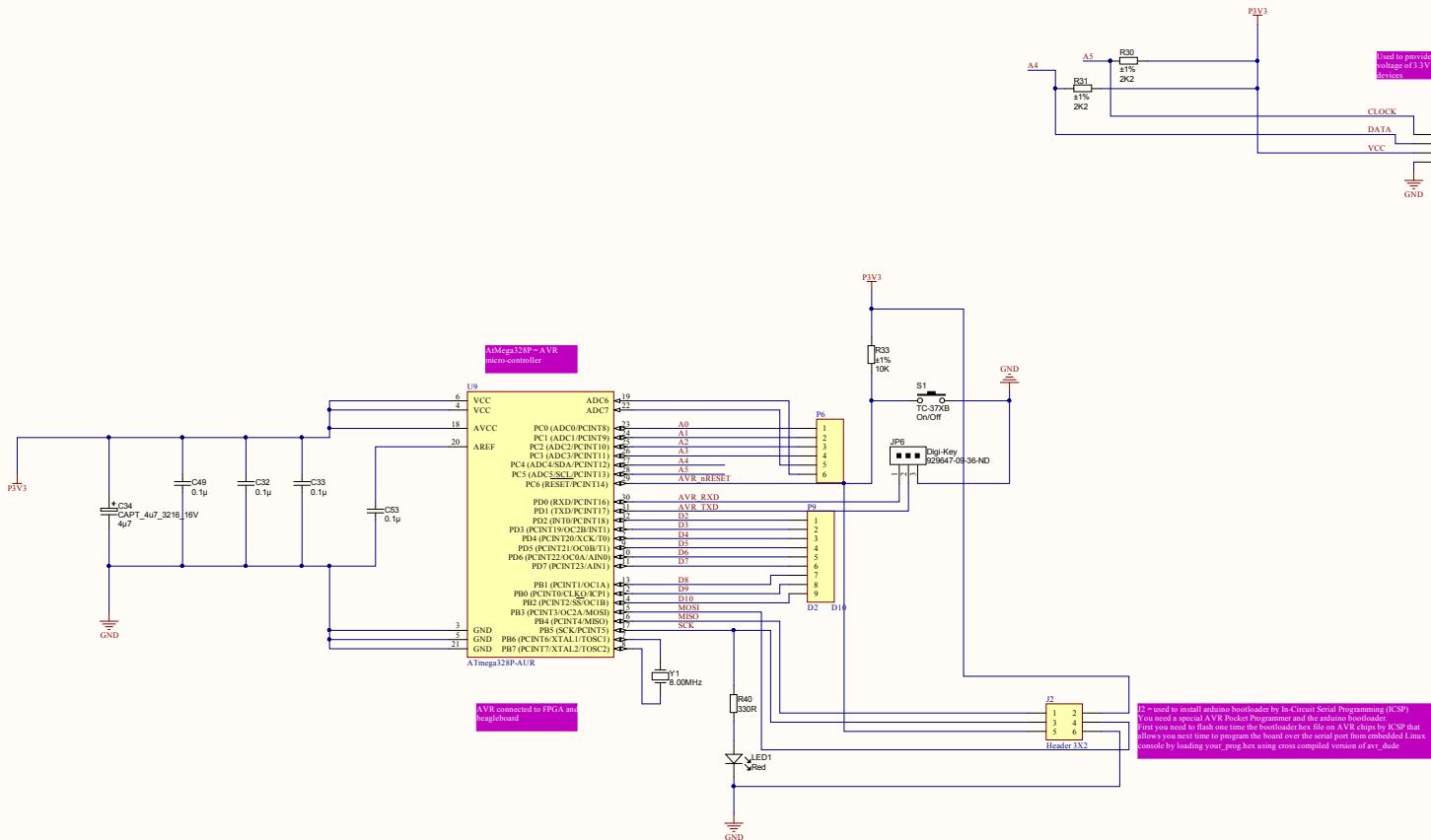
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Size B	Number	Revision 2.4b
Date: 11/29/2018	Sheet 5 of 9	
File: E:\git\works\lan\ethernet.SchDoc	Drawn By: Adam Vadala-Roth	

A



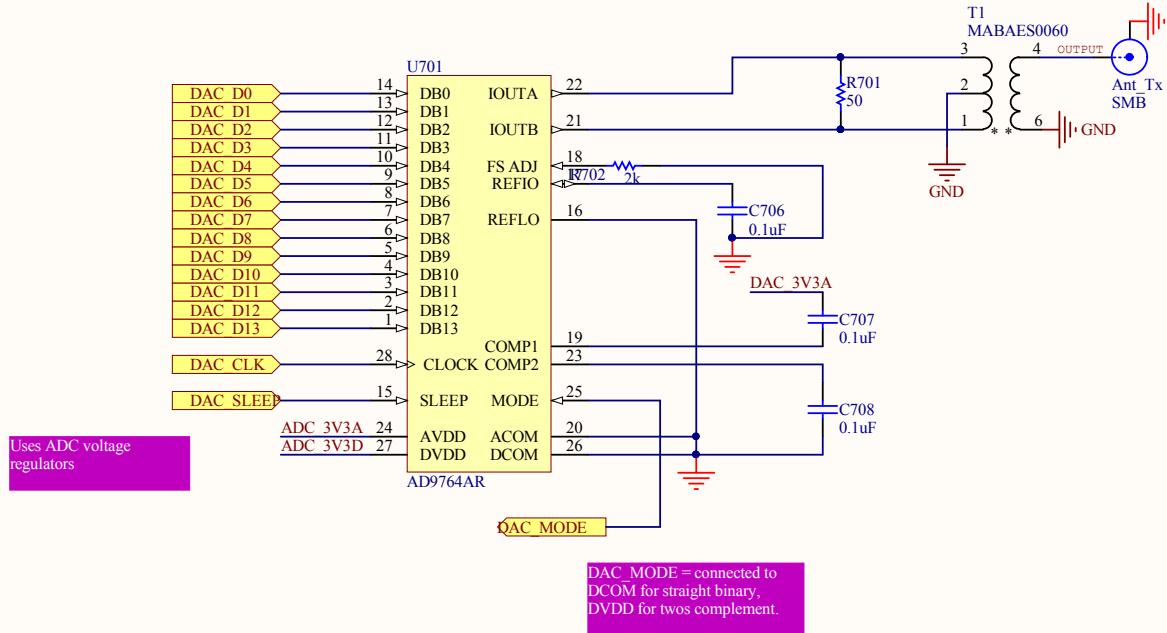
E





A

A



B

B

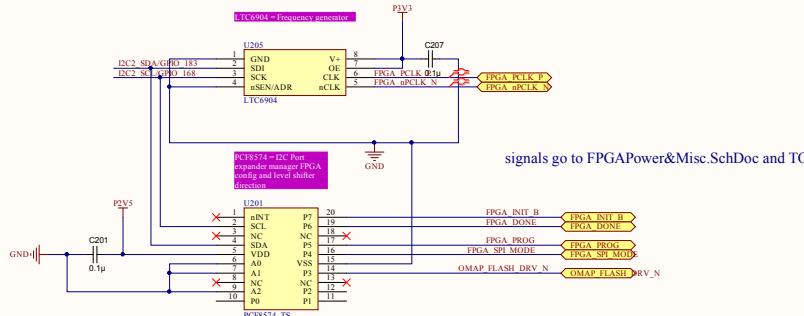
C

C

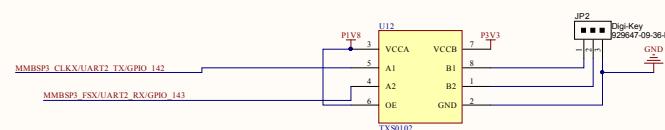
D

D

Title		
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A4		
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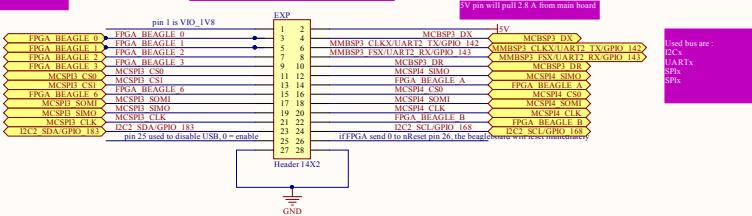


signals go to FPGAPower&Misc.SchDoc and TOP.SchDoc

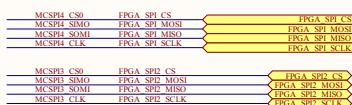


warning: over-voltage
(3.3V instead of 1.8V) may
harm beagleboard

Come back to Beagleboard xM
http://beagleboard.org/static/BBxMSRM_latest.pdf
page 108/164
7.24.2 - Table 22.



These res. R8xx must be placed on the PCB for probe



You can set the `FPGA_BEAGLE_x` to FPGA pins where you'd like to ease the pcb placement and routing

