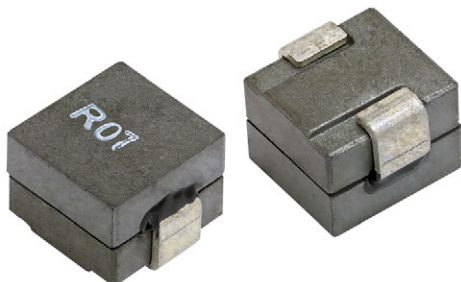


## Low Profile, High Current Inductors



### FEATURES

- Shielded construction
- Frequency range up to 2 MHz
- Material categorization:  
for definitions of compliance please see  
[www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)


**RoHS**  
COMPLIANT

### APPLICATIONS

- Notebook / desktop / server applications
- High current POL converters
- Low profile, high current power supplies
- DC/DC converters in distributed power systems

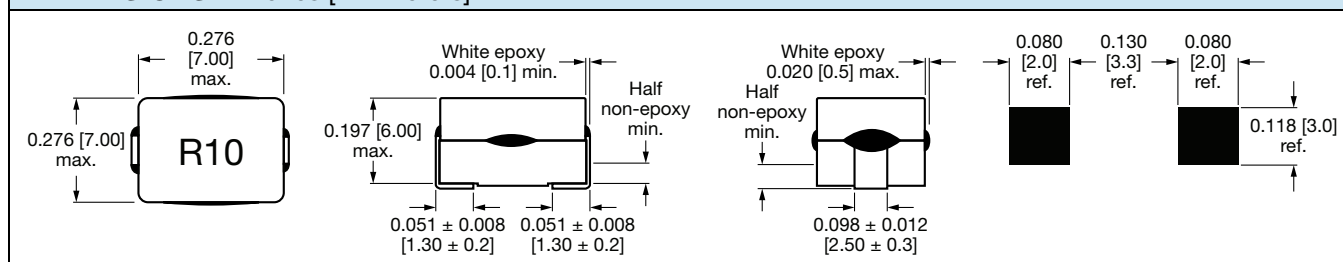
### STANDARD ELECTRICAL SPECIFICATIONS

PART NUMBER	$L_0$ INDUCTANCE $\pm 20\%$ AT 100 kHz, 1 V, 0 A ( $\mu$ H)	DCR TYP. 25 °C (m $\Omega$ )	DCR MAX. 25 °C (m $\Omega$ )	HEAT RATING CURRENT DC TYP. (A) <sup>(3)</sup>	SATURATION CURRENT DC TYP. (A) <sup>(4)</sup>
IFLR2727EZERR72NM01	0.072	0.32	0.35	45	58
IFLR2727EZERR10M01	0.105	0.32	0.35	45	46
IFLR2727EZERR15M01	0.150	0.32	0.35	45	30

#### Notes

- (1) All test data is referenced to 25 °C ambient.
- (2) Operating and Storage temperature range -40 °C to +125 °C.
- (3) DC current (A) that will cause an approximate  $\Delta T$  of 40 °C.
- (4) DC current (A) that will cause  $L_0$  to drop approximately 20 %.
- (5) The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

### DIMENSIONS in inches [millimeters]



### DESCRIPTION

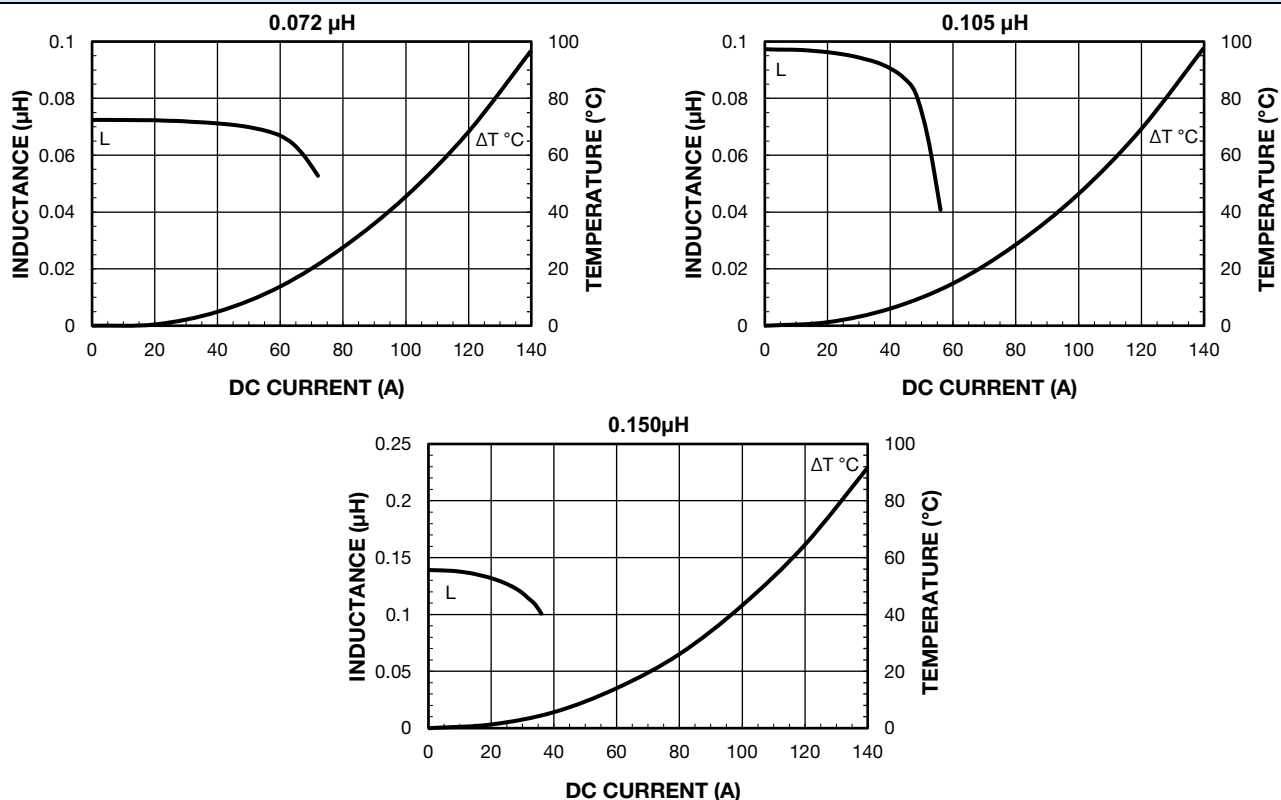
IFLR-2727EZ-01	0.15 $\mu$ H	$\pm 20\%$	TAPE AND REEL	e3
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD

### GLOBAL PART NUMBER

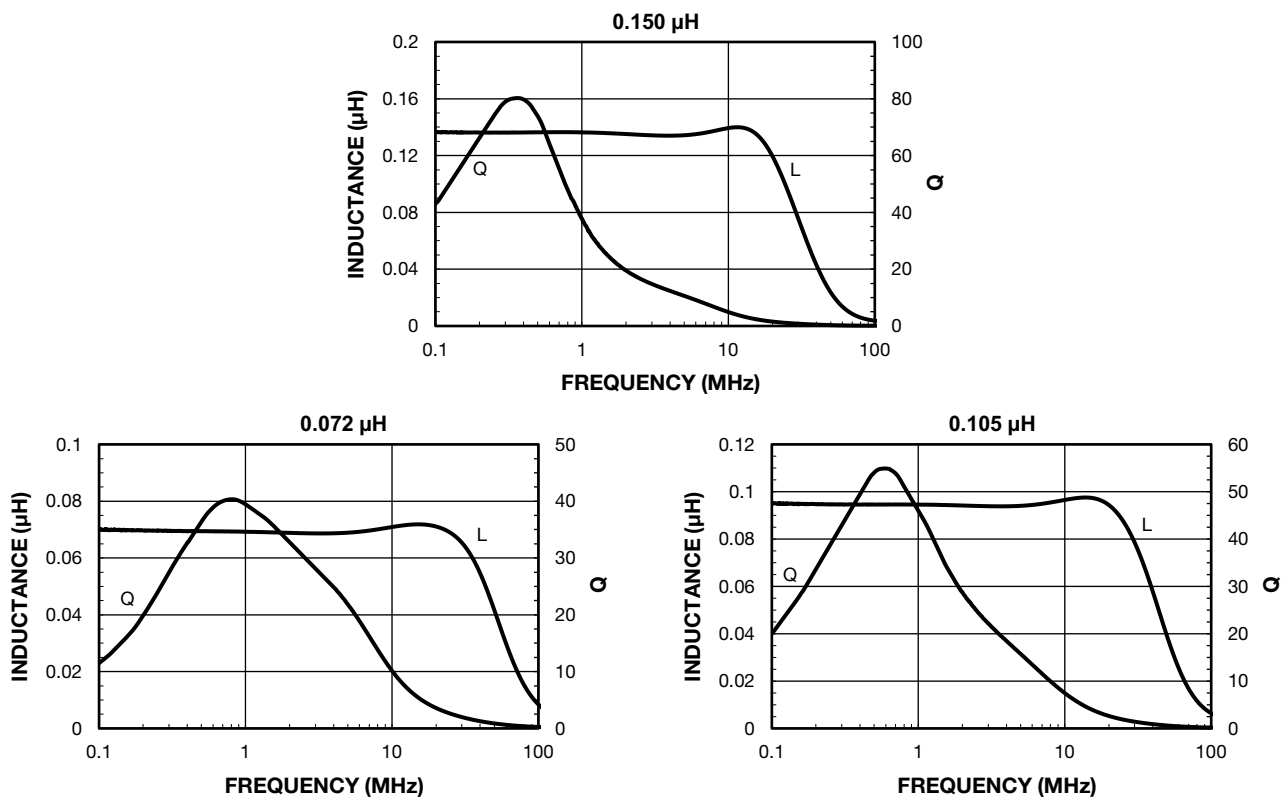
I	F	L	R	2	7	2	7	E	Z	E	R	R	1	5	M	0	1
PRODUCT FAMILY				SIZE						PACKAGE CODE		INDUCTANCE VALUE		TOL.	SERIES		



### PERFORMANCE GRAPHS



### PERFORMANCE GRAPHS: INDUCTANCE AND Q VS. FREQUENCY





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