



PMPB13XNE

30 V, single N-channel Trench MOSFET

26 November 2014

Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a leadless medium power DFN2020MD-6 (SOT1220) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- 1 kV ESD protection
- Small and leadless ultra thin SMD plastic package: 2 x 2 x 0.65 mm
- Exposed drain pad for excellent thermal conduction
- Tin-plated, 100% solderable side pads for optical solder inspection

3. Applications

- Charging switch for portable devices
- DC-to-DC converters
- Power management in battery-driven portables
- Hard disk and computing power management

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C	-	-	30	30	V
V _{GS}	gate-source voltage		-12	-	12	12	V
I _D	drain current	V _{GS} = 4.5 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	-	11.3	A
Static characteristics							
R _{DSon}	drain-source on-state resistance	V _{GS} = 4.5 V; I _D = 8 A; T _j = 25 °C		-	13	16	mΩ

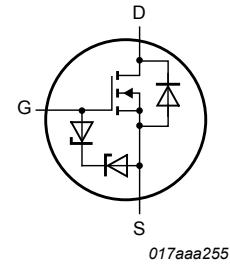
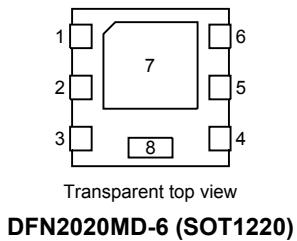
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

nexperia

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	D	drain		
2	D	drain		
3	G	gate		
4	S	source		
5	D	drain		
6	D	drain		
7	D	drain		
8	S	source		



6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMPB13XNE	DFN2020MD-6	DFN2020MD-6: plastic thermal enhanced ultra thin small outline package; no leads; 6 terminals	SOT1220

7. Marking

Table 4. Marking codes

Type number	Marking code
PMPB13XNE	1M

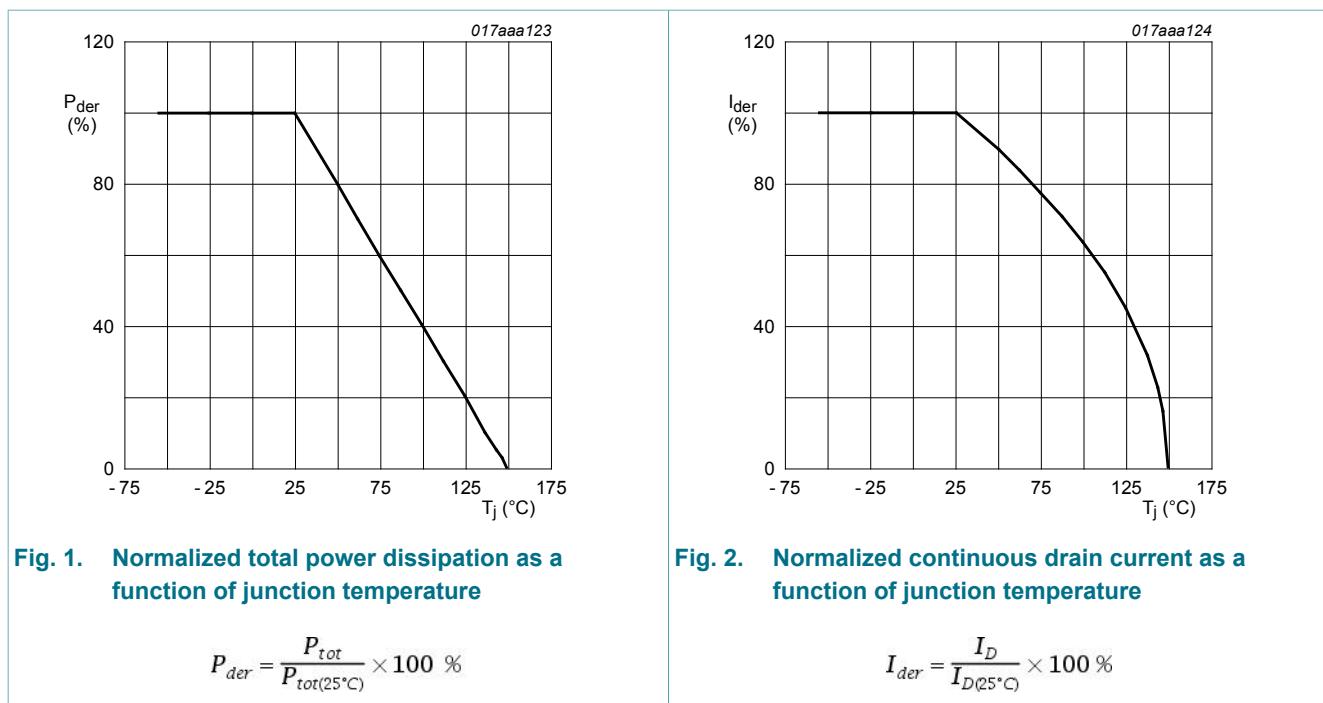
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C	-	-	30	V
V _{GS}	gate-source voltage		-12	12	V	
I _D	drain current	V _{GS} = 4.5 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	11.3	A
		V _{GS} = 4.5 V; T _{amb} = 25 °C	[1]	-	8	A
		V _{GS} = 4.5 V; T _{amb} = 100 °C	[1]	-	5	A
I _{DM}	peak drain current	T _{amb} = 25 °C; single pulse; t _p ≤ 10 µs	-	-	32	A
P _{tot}	total power dissipation	T _{amb} = 25 °C	[1]	-	1.7	W
		T _{amb} = 25 °C; t ≤ 5 s	[1]	-	3.5	W
		T _{sp} = 25 °C	-	-	12.5	W
T _j	junction temperature	-	-	-55	150	°C
T _{amb}	ambient temperature	-	-	-55	150	°C
T _{stg}	storage temperature	-	-	-65	150	°C
Source-drain diode						
I _S	source current	T _{amb} = 25 °C	[1]	-	2	A

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².



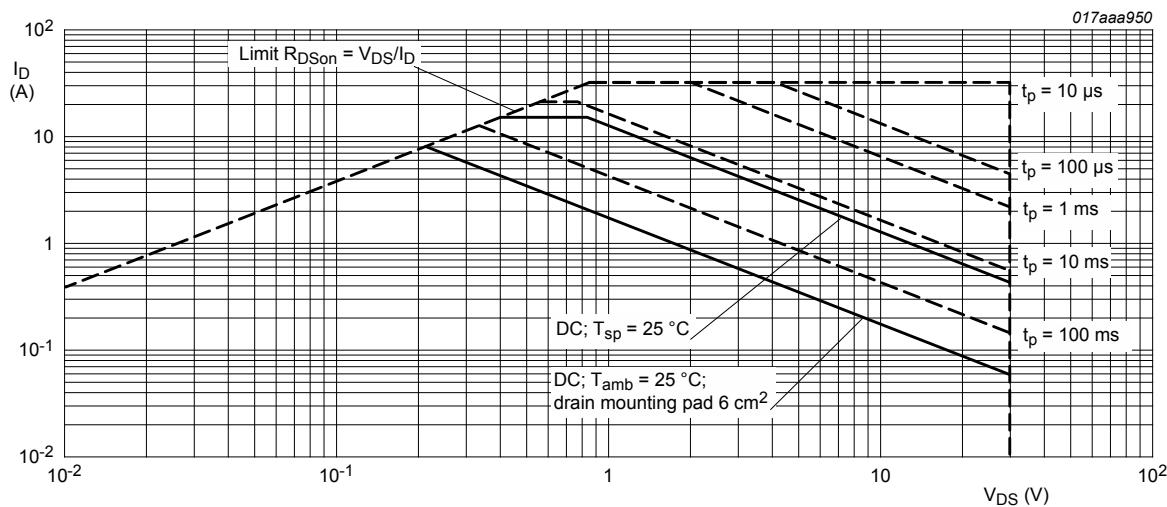


Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

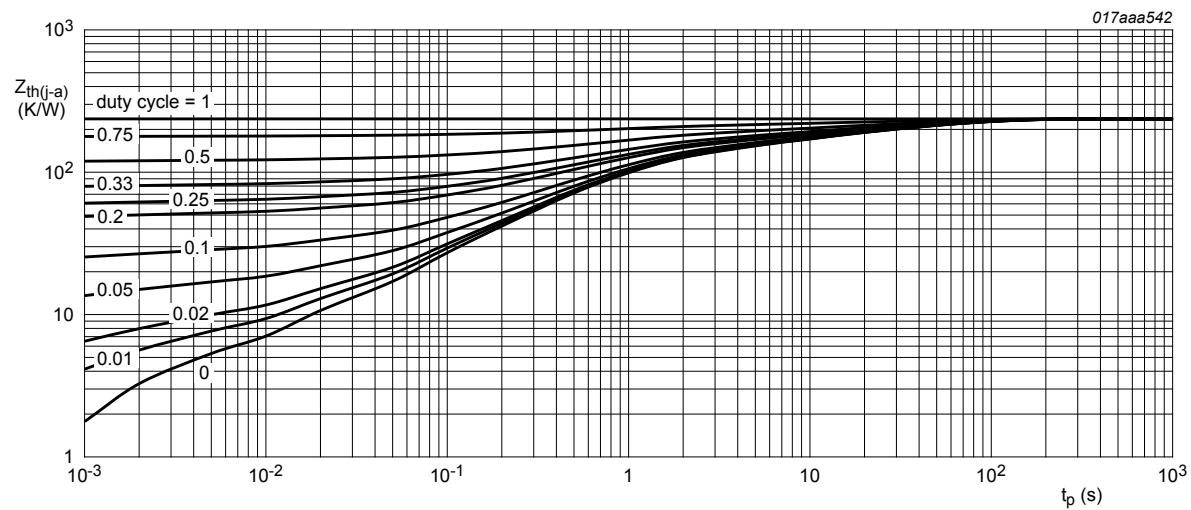
9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	235	270	K/W
			[2]	-	67	74	K/W
		in free air; $t \leq 5 \text{ s}$	[2]	-	33	36	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	5	10	K/W

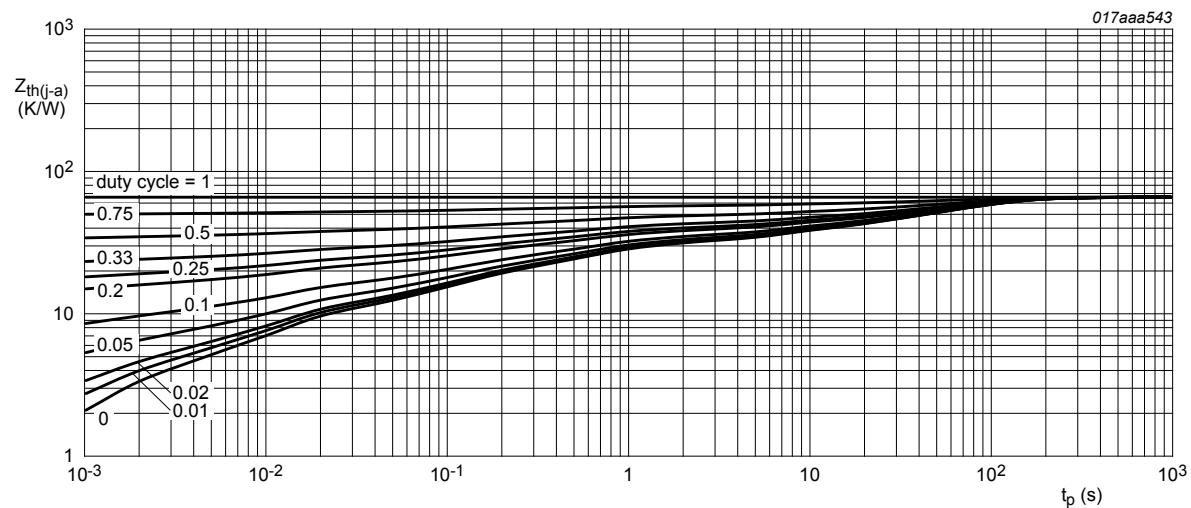
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm^2 .



FR4 PCB, standard footprint

Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for drain 6 cm^2

Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Static characteristics							
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25^\circ C$		30	-	-	V
V_{GSth}	gate-source threshold voltage	$I_D = 250 \mu A; V_{DS} = V_{GS}; T_j = 25^\circ C$		0.4	0.65	0.9	V
I_{DSS}	drain leakage current	$V_{DS} = 30 V; V_{GS} = 0 V; T_j = 25^\circ C$		-	-	1	μA
I_{GSS}	gate leakage current	$V_{GS} = 8 V; V_{DS} = 0 V; T_j = 25^\circ C$		-	-	10	μA
		$V_{GS} = -8 V; V_{DS} = 0 V; T_j = 25^\circ C$		-	-	-10	μA
R_{DSon}	drain-source on-state resistance	$V_{GS} = 4.5 V; I_D = 8 A; T_j = 25^\circ C$		-	13	16	$m\Omega$
		$V_{GS} = 4.5 V; I_D = 8 A; T_j = 150^\circ C$		-	21	27	$m\Omega$
		$V_{GS} = 2.5 V; I_D = 7.2 A; T_j = 25^\circ C$		-	14	20	$m\Omega$
		$V_{GS} = 1.8 V; I_D = 3.7 A; T_j = 25^\circ C$		-	17	24	$m\Omega$
g_{fs}	forward transconductance	$V_{DS} = 10 V; I_D = 8 A; T_j = 25^\circ C$		-	60	-	S
R_G	gate resistance	$f = 1 MHz; T_j = 25^\circ C$		-	2.1	-	Ω
Dynamic characteristics							
$Q_{G(tot)}$	total gate charge	$V_{DS} = 15 V; I_D = 6 A; V_{GS} = 4.5 V; T_j = 25^\circ C$		-	24	36	nC
Q_{GS}	gate-source charge			-	2.4	-	nC
Q_{GD}	gate-drain charge			-	4.6	-	nC
C_{iss}	input capacitance	$V_{DS} = 15 V; f = 1 MHz; V_{GS} = 0 V; T_j = 25^\circ C$		-	2195	-	pF
C_{oss}	output capacitance			-	155	-	pF
C_{rss}	reverse transfer capacitance			-	135	-	pF
$t_{d(on)}$	turn-on delay time	$V_{DS} = 15 V; I_D = 6 A; V_{GS} = 4.5 V; R_{G(ext)} = 6 \Omega; T_j = 25^\circ C$		-	12	-	ns
t_r	rise time			-	30	-	ns
$t_{d(off)}$	turn-off delay time			-	54	-	ns
t_f	fall time			-	49	-	ns
Source-drain diode							
V_{SD}	source-drain voltage	$I_S = 2 A; V_{GS} = 0 V; T_j = 25^\circ C$		-	0.6	1.2	V

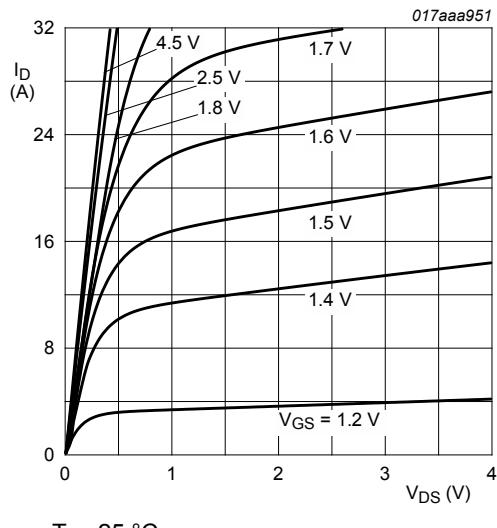


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

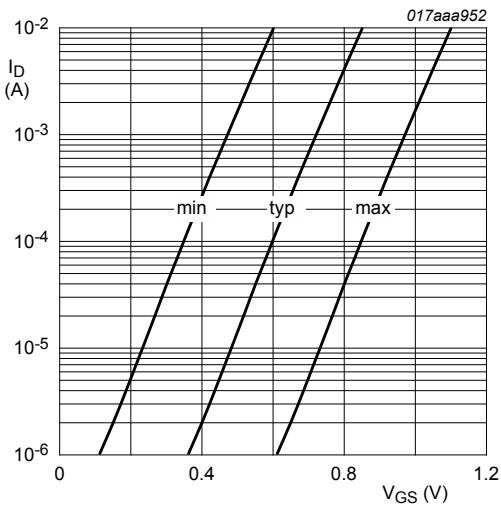


Fig. 7. Sub-threshold drain current as a function of gate-source voltage

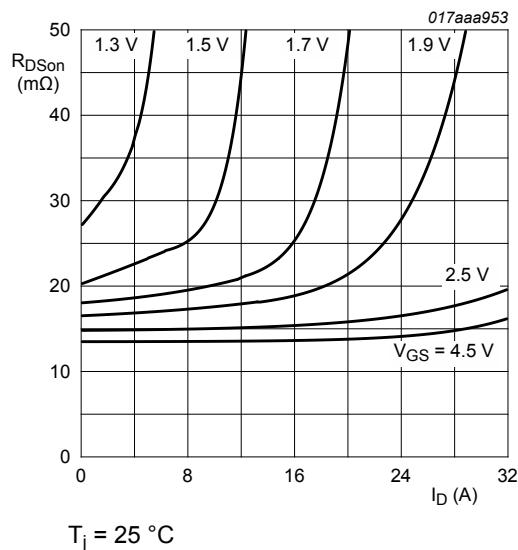


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

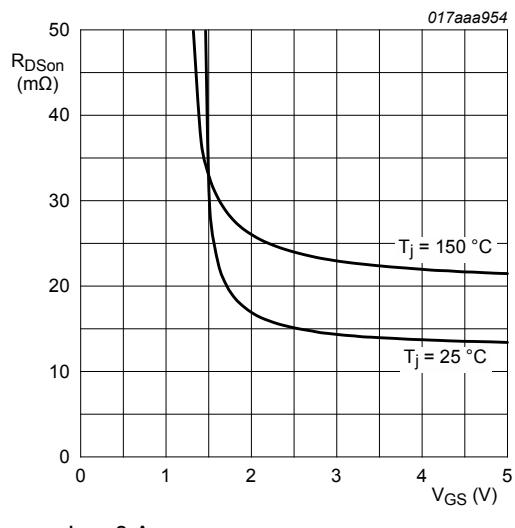
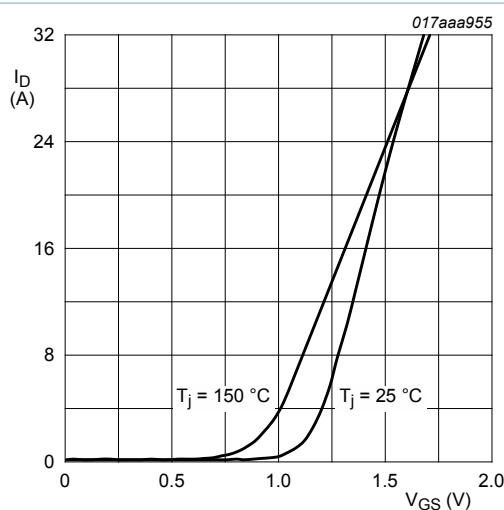


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values



$V_{DS} > I_D \times R_{DSon}$

Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

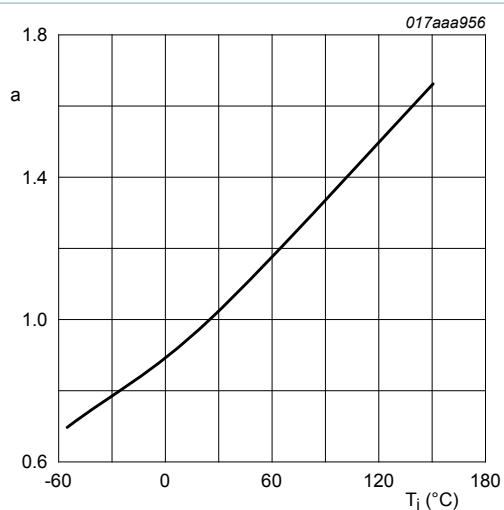
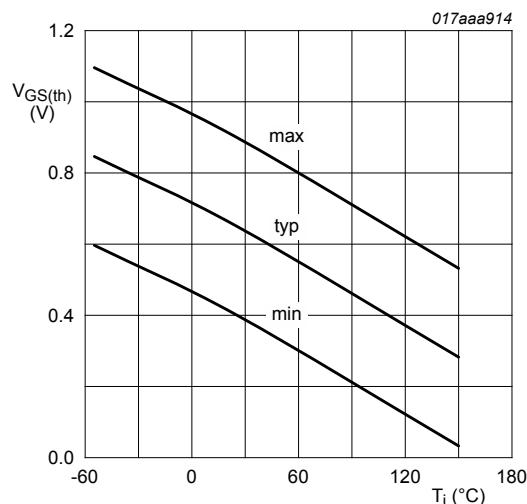


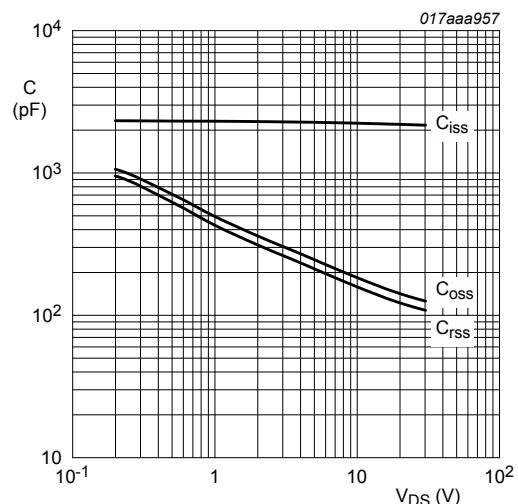
Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

$$a = \frac{R_{DSon}}{R_{DSon}(25^\circ\text{C})}$$



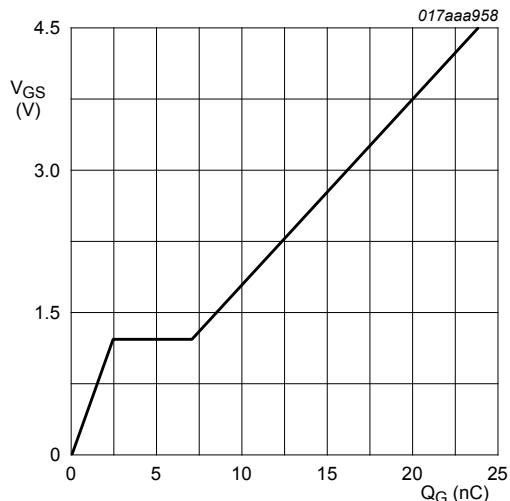
$I_D = 0.25 \text{ mA}; V_{DS} = V_{GS}$

Fig. 12. Gate-source threshold voltage as a function of junction temperature



$f = 1 \text{ MHz}; V_{GS} = 0 \text{ V}$

Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



$I_D = 8 \text{ A}$; $V_{DS} = 15 \text{ V}$; $T_{amb} = 25^\circ\text{C}$

Fig. 14. Gate-source voltage as a function of gate charge; typical values

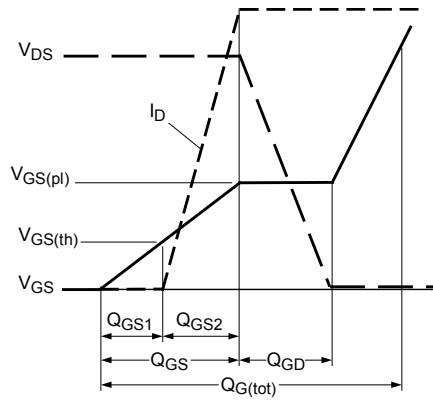
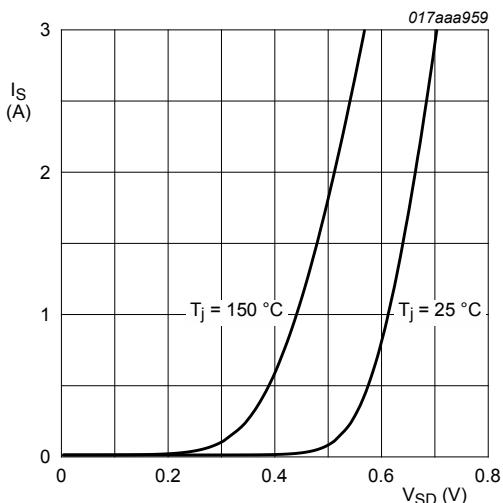


Fig. 15. MOSFET transistor: Gate charge waveform definitions



$V_{GS} = 0 \text{ V}$

Fig. 16. Source current as a function of source-drain voltage; typical values

11. Test information

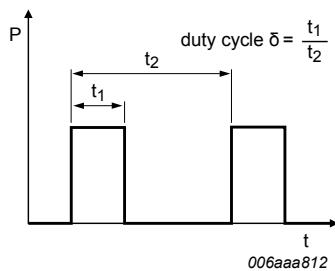


Fig. 17. Duty cycle definition

12. Package outline

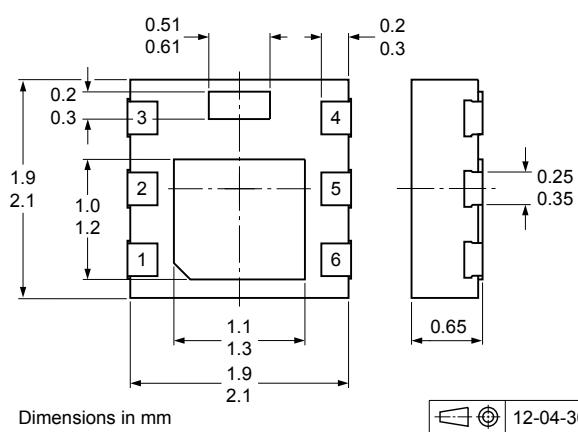
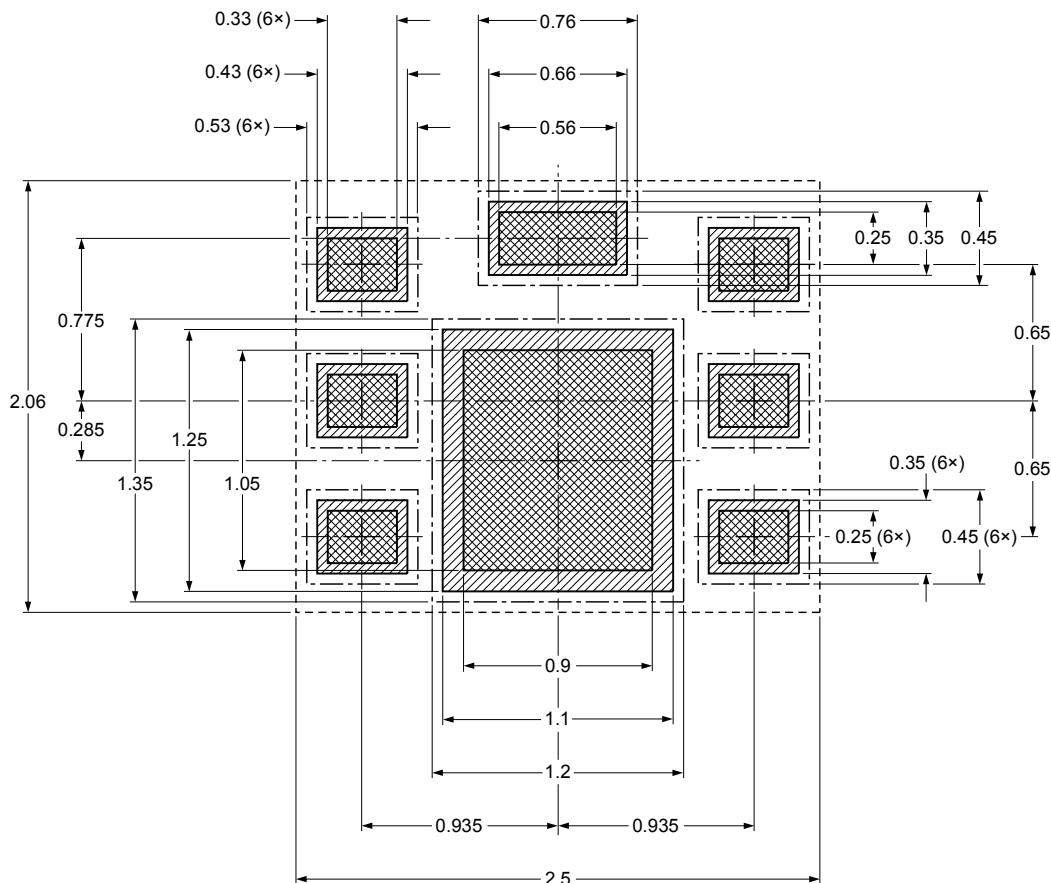


Fig. 18. Package outline DFN2020MD-6 (SOT1220)

13. Soldering

Footprint information for reflow soldering of DFN2020MD-6 package

SOT1220



solder land

solder land plus solder paste

solder paste deposit

solder resist

--- occupied area

Dimensions in mm

sot1220_fr

Fig. 19. Reflow soldering footprint for DFN2020MD-6 (SOT1220)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMPB13XNE v.2	20141126	Product data sheet	-	PMPB13XNE v.1
Modifications:		<ul style="list-style-type: none">• 3D package outline added• Features and benefits: corrected• Table 5: updated		
PMPB13XNE v.1	20121130	Product data sheet	-	-

15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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16. Contents

1	General description	1
2	Features and benefits	1
3	Applications	1
4	Quick reference data	1
5	Pinning information	2
6	Ordering information	2
7	Marking	2
8	Limiting values	3
9	Thermal characteristics	4
10	Characteristics	6
11	Test information	10
12	Package outline	10
13	Soldering	11
14	Revision history	12
15	Legal information	13
15.1	Data sheet status	13
15.2	Definitions	13
15.3	Disclaimers	13
15.4	Trademarks	14

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