

HJ-131IMH

Ultra-small chip level (4mm * 4mm, include ANT) ,ultra low power

Bluetooth 5.1 module DataSheet

DataSheet version: V2.0



Model : **HJ-131IMH**



Size: 4mm*4mm*1.3mm

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1 Overview

1.1 Characteristics

- BQB FCC CE SRRC RoHS REACH compliant
- Supply voltage range: High Voltage Mode 1.8V~3.6V;Low Voltage Mode 1.05V~1.7V
- It can be powered by a single AA size battery or a zinc air battery at 1.5V. After voltage regulation,it can also be powered by 2 pcs AA size batteries or lithium cells.
- GPIO max: 6
- 4mm * 4mm volume includes built-in high-performance antenna(External antenna can also be used)
- Memories
 - 32KB of OTP - 48KB of RAM
 - 4KB of EEPROM
- External 512Kb EEPROM or 1Mb EEPROM can be used to store user programs and OTA
- Functions
 - Support BLE 5.1, embedded low energy Bluetooth protocol stack and GATT service
 - Support BLE master-slave integration (Support 1 slave 1 master or 3 slaves, master and slave work at the same time without affecting each other)
 - Built-in standard firmware supports 1 slave, supports multiple instructions to configure BLE module(Refer to "HJ-131IMH Built-in Firmware Software Function Document")
 - Built-in standard firmware supports

transparent transmission, WeChat Mini Program, or no program module for customers to develop firmware by themselves

●RF Features

- Operating Frequency: 2.4GHz, Support ISM free Frequency band

- Transmitting Power: -19.5dBm ~ +2.5dBm

- Receiver sensitivity: -94dBm

- TX peak current < 3.5mA ; RX peak current < 2.2mA

- When using the built-in antenna in an open area, the transmission distance of wireless signal can spread more than 5 metres and less than 10 metres.

- When using an external antenna in an open area, the transmission distance of wireless signal can spread more than 40 metres and less than 80 metres.

●Low Power Dissipation

- Sleep current < 2μA

- One second broadcast current: 7μA(0dBm)

- Two second broadcast current:3.8μA(0dBm)

- 20ms connection gap current 65uA (0dBm)

- 1000ms connection gap current 6.8uA(0dBm)

- Package: LGA17, pad spacing: 0.8mm, pad size:0.5*0.5mm

●Size:

Common design:4mm*4mm*1.3mm(Built-in antenna inside)

Ultra thin: 4mm*4mm*1mm(Built-in antenna inside)

- Weight: 0.05g

- Operating temperature range: -40 ~ +85°C

1.2 Electrical Parameters

•Absolute Maximum Range

Table 1-1 Absolute maximum ratings

Parameter	MIN	MAX	Unit
Power Supply Voltage (VCC)	1.05	3.7	V
IO Supply Voltage	0	VCC	V
Operating Temperature	-40	+85	°C
Storage Temperature	-40	+85	°C

•Recommended Operating Conditions

Table 1-2 Recommended operating conditions

Parameter	MIN	TYP	MAX	Unit
Power Supply Voltage (VCC)	1.8(1.05)	3.3(1.5)	3.6(1.8)	V
IO Supply Voltage	0	3.3(1.8)	VCC	V
Dormant working current		<2		µA
Maximum Operating Current		0.4		mA
Operating Temperature	-40	+25	+85	°C

•I/O DC Characteristics

Table 1-3 I/O DC Characteristics

I/O Pin	Driving Capability	MIN	MAX	Unit
Input low voltage		0	0.4	V
Input high voltage		0.7	VCC	V
Output low voltage	5mA	0	0.6	V
Output high voltage	5mA	3.3	VCC	V

•Power Dissipation

Table 1-4 Power Dissipation

Test conditions	TYP	Unit
Dormancy mode	<2	µA
20ms Interval Broadcasting in Slave Mode	180	µA
1S Interval Broadcasting in Slave Mode	7.0	µA
20ms Connection Gap Holding Connection in Slave Mode	65	µA
7.5ms Connection Gap Holding Connection in Slave Mode	110	µA
Scanning in Host Mode	2.3	mA
20ms Connection Gap Holding Connection in Host Mode	78	µA

●RF Features

Table 1-5 RF Features

Attribute	Value	Remarks
Modulation	GFSK	
Frequency range	2.402 ~ 2.480GHz	Bandwidth: 2MHz
Number of channels	40	
Air speed	1Mbps	
RF Port Impedance	50Ω	
Transmit Power	MAX: +2.5dBm	
TX Current consumption	TYP: 3.5mA@0dBm	
RX Current consumption	TYP: 2.2mA	
Receive sensitivity	TYP: -94dBm, MAX: -95dBm	
Antenna	Built-in Antenna	External antenna can be used

2 Hardware specification

2.1 Package and dimensions

The package of HJ-131IMH is LGA17, welding pad spacing is 0.8 mm. Detailed dimensions are shown in the figure 2-1, 2-2, 2-3, 2-4.

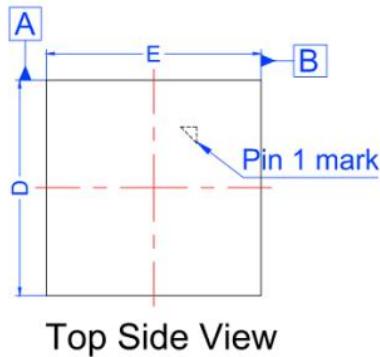


Figure 2-1 Top view

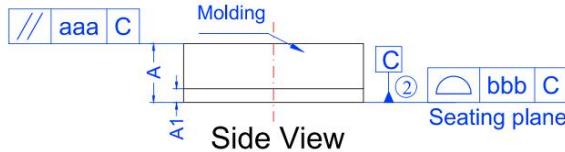
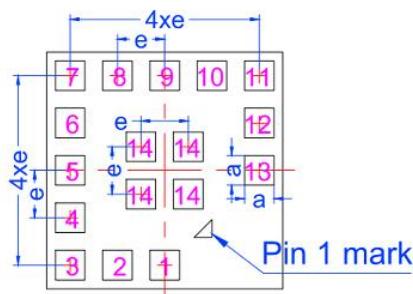


Figure 2-2 Side view



Bottom Side View

Figure 2-3 Bottom view

DIMENSIONAL REFERENCES Units:mm

SYMBOL	DIMENSIONAL REQMTS			SYMBOL	Tolerance of Form & Position
	MIN	NOM	MAX		
A	1.26	1.30	1.34	aaa	0.10
A1	0.27	0.30	0.33	bbb	0.10
D	3.90	4.00	4.10		
E	3.90	4.00	4.10		
a	0.45	0.50	0.55		
e		0.80 REF			

Note:

1. All dimensions are in mm
- ② Datum 'C' is the mounting surface, with which the package is in contact

Figure 2-4 Dimensions picture

Note: The normal value of ultra-thin model A is 1mm, and other parameters are the same as those of conventional model

2.2 Pin Definition

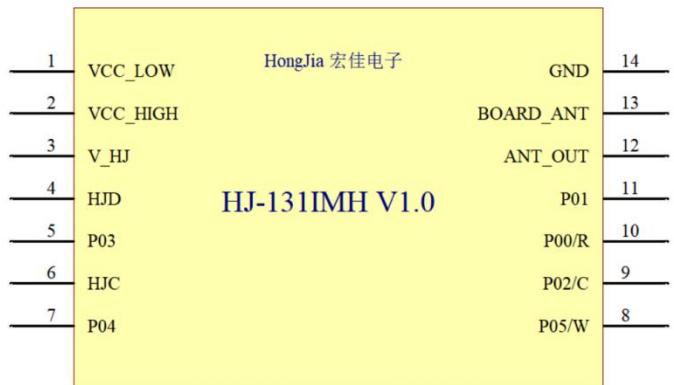


Figure 2-5 Pin definition graph

Table 2-1 Pin definition table

Pin	Name	Type	Description	(customized / transparent transmission) function
1	VCC_LOW	Low Power In	Low voltage power input port	Low Voltage Mode input : 1.05-1.7V
2	VCC_HIGH	High Power In	High voltage power input port	High Voltage Mode input : 1.8-3.6V
3	<i>V_HJ</i>	<i>HongJia Reserve</i>	<i>Internal EEPROM power supply pin</i>	<i>Power supply range 1.7-3.6V, please connect with VCC_HIGH if needed</i>
4	<i>HJD</i>	<i>HongJia Reserve</i>	<i>Internal EEPROM SDA pin</i>	<i>If needed, please connect with P03</i>
5	P03	IO	General purposed GPIO	If you need to use internal EEPROM, please connect with HJD
6	<i>HJC</i>	<i>HongJia Reserve</i>	<i>Internal EEPROM SCL pin</i>	<i>If needed, please connect with P04</i>
7	P04	IO	General purposed GPIO	If you need to use internal EEPROM, please connect with HJC
8	P05/W	UART TX	General purposed GPIO	BLE serial prot's TX pin
9	P02/C	WAKEUP/UART RX	Wake-up Pin / UART Pin	Wake-up function and BLE serial RX (Under low power consumption, the pin defaults to the wake-up pin. If the external high level is increased for more than 1ms, BLE can be woken up. After waking up, this pin becomes the UART RX function, which can perform normal data transmission and reception;)
10	P00/R	I/RST	Reset Pin	High-level reset; floating or low-level normal operation
11	P01	O	Connection Status Pin	Connection status (connection = 1; disconnect = 0)
12	ANT_OUT	RF OUT	RF Output	RF output, can be used with external antenna
13	Built-in_ANT	Built-in ANT	Built-in antenna input port	Internal integrated matching circuit. If an internal antenna is used, short circuit the 12 and 13 pins directly.
14	GND	Ground	Power Input GND	Power Negative GND

2.3 Internal Structure

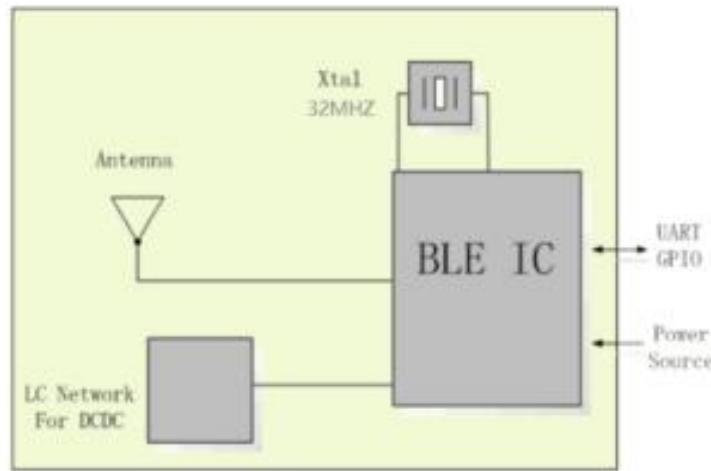


Figure 2-6 HJ-131IMH internal structure frame

2.4 Reference Design

2.4.1 Low Voltage Mode (Can be powered by one single AA size battery, voltage range: 1.05-1.7V)

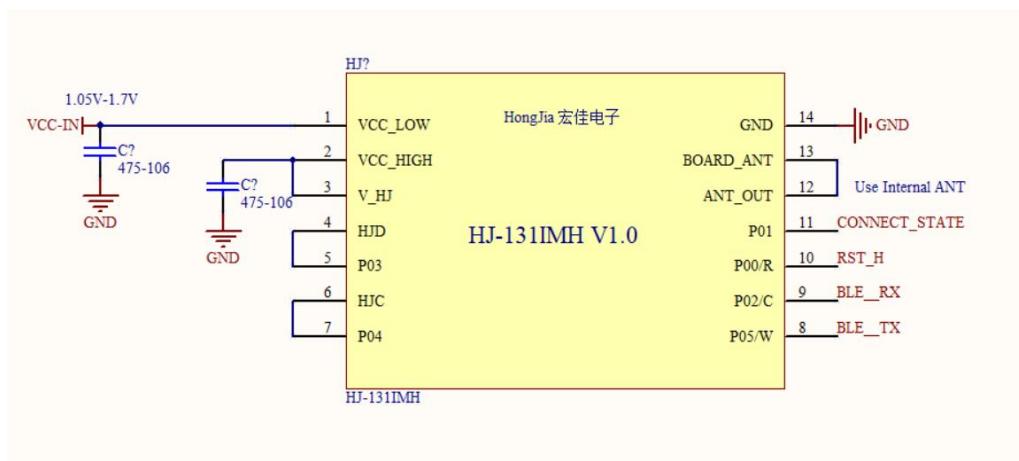


Figure 2-7 Low Voltage Supply Mode

2.4.2 HighVoltage Mode (Voltage range: 1.8-3.6V)



Figure 2-8 High Voltage Supply Mode

2.4.3 Using the built-in antenna of the module

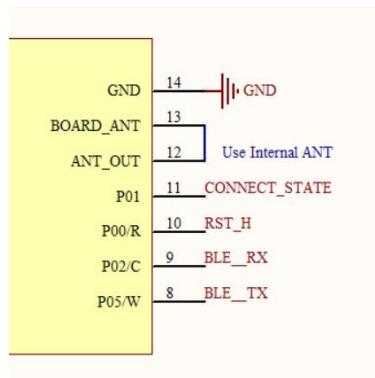


Figure 2-9 Using the built-in antenna of the module

2.4.4 Using an external antenna

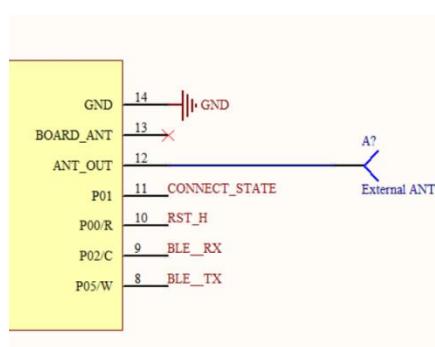


Figure 2-10 Using an external antenna

2.5 The external antenna part is designed for reference

2.5.1 Use internal high performance antennas

Simply short connect PIN12 with PIN13 to enable the internal high-performance antenna, as shown in Figure 2-11 below, with an open communication distance of 5 to 10 meters.

It should be noted that no devices or wires can be placed near the antenna, no devices can be placed on the back of the module, the copper cladding should avoid the internal antenna area, and the module GND copper cladding is large enough.

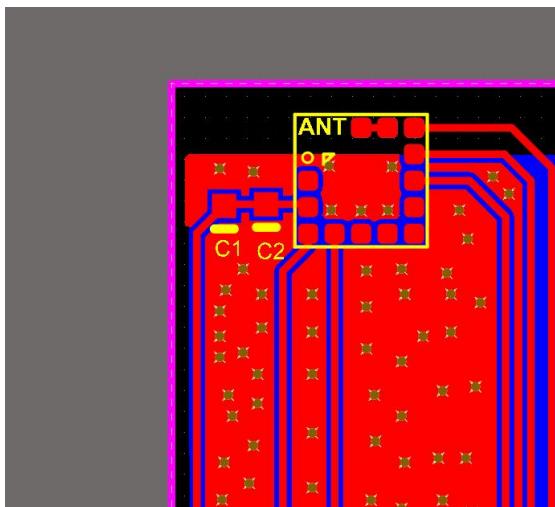


Figure 2-11 Internal antenna circuit design

2.5.2 An external PCB antenna is used

The pin of PIN13 is suspended, and the pin of PIN12 is connected to the PCB antenna

through a π -shaped filter circuit, as shown in Figure 2-12 below. The communication distance in the open field can reach 40~80 meters.

It should be noted that no devices or wires can be placed near the antenna, no devices can be placed on the back of the module, and coppers should be wrapped around the module and PI filter circuit to avoid the PCB antenna.

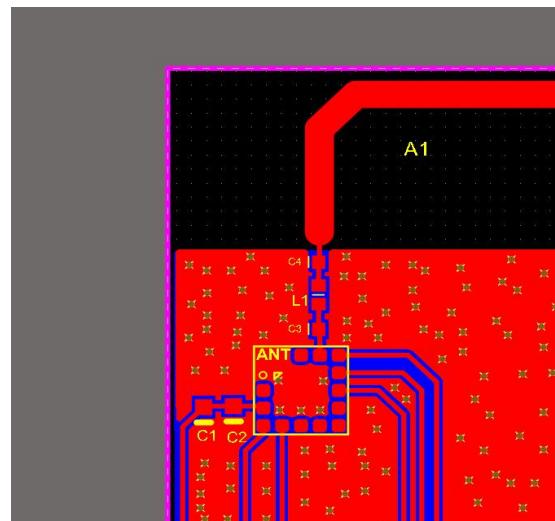


Figure 2-12 External PCB antenna circuit design

3 Announcements

3.1 Notices for Hardware Design

1. All IO ports can be used. Please pay attention to the pin diagram of all pins, and pay attention to the IO mode and status of the connected IO.
2. Filter capacitors C1 and C2 should be placed as close to the power input pin of the module as possible.
3. When using external antenna, be sure to contact our company, let us confirm whether your external PCB antenna or IPEX lead antenna PCB design is reasonable.
4. The module should not be placed in a metal shell. If a metal shell must be used, the antenna must be led out.
5. In the products that need to install the wireless module, some metal parts, such as screws and inductors, should be kept away from the RF antenna part of the wireless module as far as possible.
6. Near the antenna of the Bluetooth module and on the back, as far as possible do not place other components, and can not be wired. If the device or trace is placed, the Bluetooth performance will be affected.
7. The module antenna should be placed around the edge of the circuit board. The antenna part is close to the edge or corner of the motherboard.
8. Each layer of the circuit board is copper-clad to GND as a whole, and it is necessary to ensure

that the copper-clad area of the module, especially the antenna part, is large enough and well grounded.

9. Through holes should be punched in the copper-clad area of the entire circuit board, especially in the copper-clad area near the module and antenna, and as many as possible should be punched.
10. If there are high power devices or high voltage conversion circuits on the circuit board, the GND copper cladding of the module should be isolated from the GND copper cladding of other parts, connected by single point grounding, and perforated as many as possible to reduce the interference to the RF signal.
11. Do not need to use the pin can be suspended processing.

3.2 Notices for Ultrasound Welding

Warning: Please carefully consider using ultrasonic welding technology. If it is necessary to use ultrasonic welding technology, please use 40KHz high frequency ultrasound welding technology. Keep the module away from the ultrasonic soldering line and the fixing column during the design method to prevent damage to the module!

For specific ultrasonic welding matters, please contact our company for technical consultation.

4 Soldering Recommendations

Reflow soldering is recommended for welding.

HJ-131IMH module use high temperature resistant materials, manufacturing by Lead-free Process. The maximum temperature resistance is 265°C. Ten continuous reflow soldering has no effect on properties and strength. Specific parameters as shown in Table 4-1.

Table 4-1 Reflow soldering parameters

Parameter	Value
Features	Lead-free process
Average ramp up rate(T_{SMAX} to T_p)	3°C/sec. max
Temperature Min(T_{Smin})	150°C
Temperature Max(T_{Smax})	200°C
Preheat time (Min to Max) (tS)	80~100sec
Peak Temperature (T_p)	250±5°C
Ramp-down Rate	6°C/sec. max
Time 25°C to Peak Temp (T_p)	8 min. max

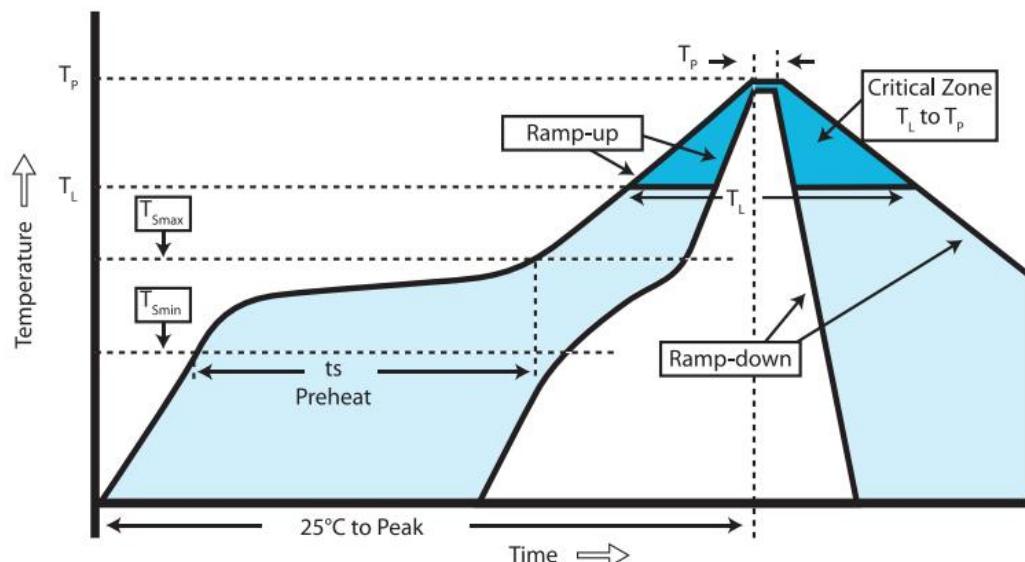


Figure 4-1 Temperature Curve of Reflow Welding

5 Supply Information

5.1 Model Definition

Table 5-1 Model Definition

Type	Model	Description
Standard Edition of uart transparent transmission	HJ-131IMH	Include UART port transparent transmission firmware, the firmware module is a bridge between the Bluetooth device or the mobile phone and the MCU. The Customer does not need to understand the BLE protocol stack, and control the UART port command operation and the UART port data, and the operation is simple, short Development cycle to speed up product launch.
Custom version	HJ-131IMH_CUS	This version supports custom firmware, the customer proposes functions according to the product requirements, and we will customize the module with the special version firmware to supply the customer.
Customer development Version	HJ-131IMH_EMP	This version of the module has no built-in firmware, customer can develop their own firmware according to the Dialog official chip datasheet and support documents, and only need to provide firmware for us to burn.

5.2 Development Kit Board

We have specially prepared an Kit board for developers. The Kit board leads all GPIOs, and onboard USB TO TTL is convenient for debugging and testing with the computer. At the same time we also provide test documents, please contact our sales for details.



5.3 Packaging method

Packaging with tapes and reel. Sealed with chip-level anti-static aluminum foil bag, each bag contains desiccant, use industrial grade vacuum machine to ensure airtight, moisture-proof, waterproof and dustproof (IP65). The actual packing effect is shown in Figure 5-1.



Figure 5-1 External Packing Image

All packages will be labeled with goods information. All packages will be marked with the cargo information, including ROHS and anti-static signs. The production batch information in the item number is 15 bits.



Remarks: P16a I15b S17c001 represents PCB production in January 2016, IC production in February 2015, and SMT patch in the first time in March 2017.

Figure 5-2 Label Sample Diagram

6 Version History

Table 6-1 Revision History

No.	Version Number	Release Time	Reviser	Checker	Description
1	V1.2	20191109	LMY	LJH	First edition
2	V1.31	20200101	LMY	ZYP	Upadate Paremeters
3	V1.5	20200206	LMY	LJH	Redefine pin functions
4	V1.7	20200512	LMY	ZYP	Add Development Kit board
5	V2.0	20230901	FJW	LMY	Format adjustment, Add hardware considerations,Fix the reference circuit diagram icon