

74CBTLV3245-Q100

8-bit bus switch with output enable

Rev. 5 — 24 June 2024

Product data sheet

1. General description

The 74CBTLV3245-Q100 is an 8-pole, single-throw bus switch. The device features a single output enable input (\overline{OE}) that controls eight switch channels. The switches are disabled when \overline{OE} is HIGH. Schmitt-trigger action at control inputs makes the circuit tolerant of slower input rise and fall times. This device is fully specified for partial power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

2. Features and benefits

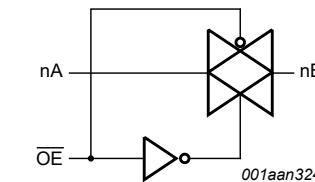
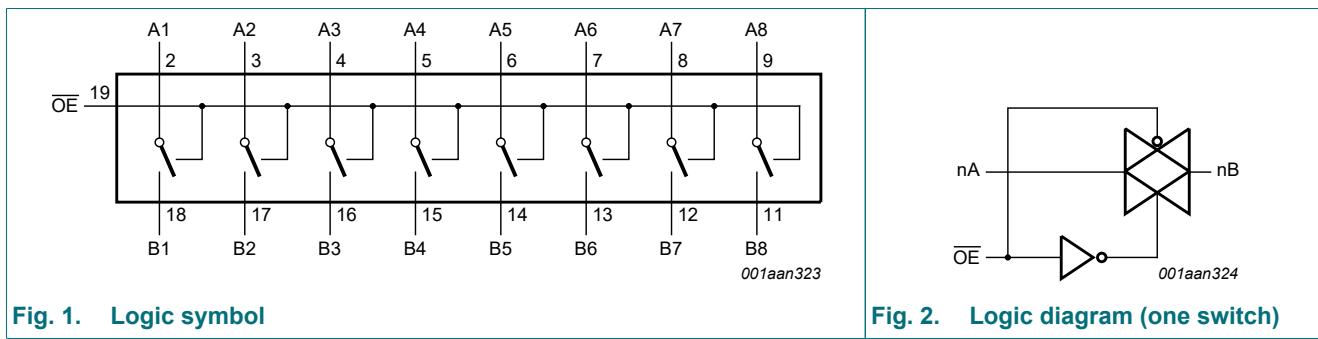
- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
 - Specified from -40 °C to +85 °C and from -40 °C to +125 °C
- Supply voltage range from 2.3 V to 3.6 V
- High noise immunity
- Complies with JEDEC standard:
 - JESD8-5 (2.3 V to 2.7 V)
 - JESD8-B/JESD36 (2.7 V to 3.6 V)
- 5 Ω switch connection between two ports
- Rail to rail switching on data I/O ports
- CMOS low power consumption
- Latch-up performance exceeds 250 mA per JESD78B Class I level A
- I_{OFF} circuitry provides partial Power-down mode operation
- DHVQFN package with Side-Wettable Flanks enabling Automated Optical Inspection (AOI) of solder joints
- ESD protection:
 - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 2000 V
 - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V

3. Ordering information

Table 1. Ordering information

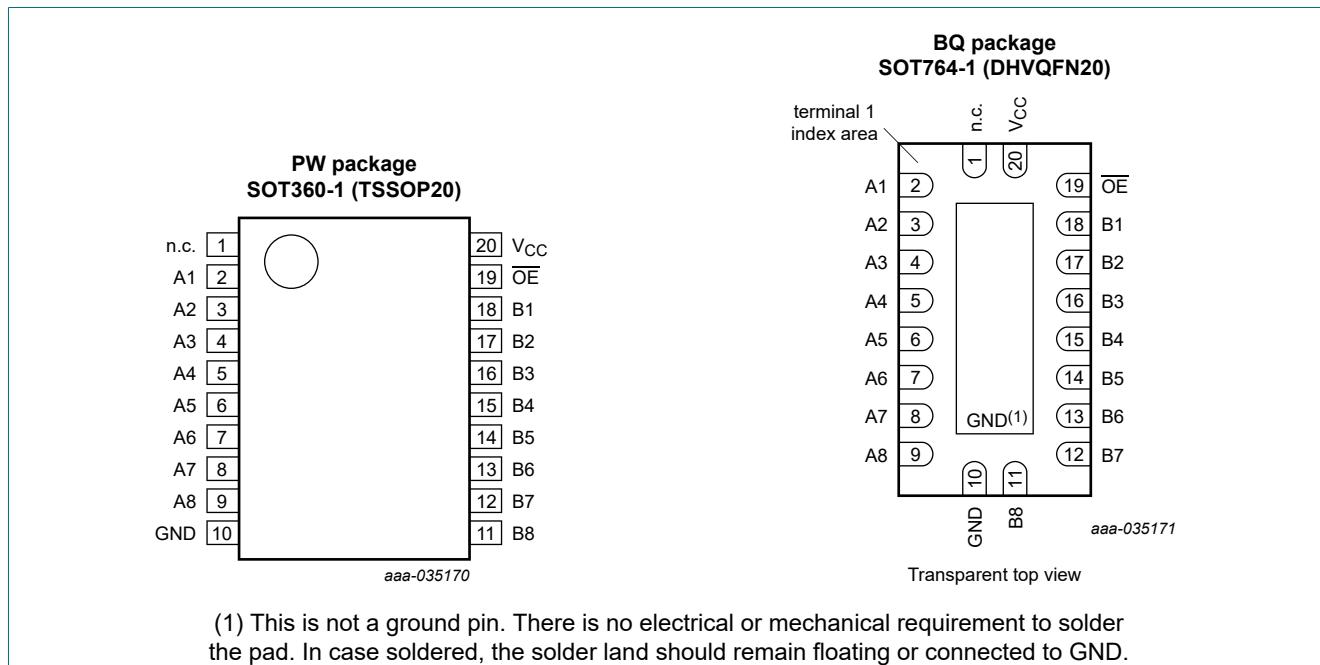
Type number	Package			Version
	Temperature range	Name	Description	
74CBTLV3245PW-Q100	-40 °C to +125 °C	TSSOP20	plastic thin shrink small outline package; 20 leads; body width 4.4 mm	SOT360-1
74CBTLV3245BQ-Q100	-40 °C to +125 °C	DHVQFN20	plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads; 20 terminals; body 2.5 × 4.5 × 0.85 mm	SOT764-1

4. Functional diagram



5. Pinning information

5.1. Pinning



5.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
n.c.	1	not connected
A1, A2, A3, A4, A5, A6, A7, A8	2, 3, 4, 5, 6, 7, 8, 9	data input/output (A port)
GND	10	ground (0 V)
B1, B2, B3, B4, B5, B6, B7, B8	18, 17, 16, 15, 14, 13, 12, 11	data input/output (B port)
OE	19	output enable input (active LOW)
V _{CC}	20	positive supply voltage

6. Functional description

Table 3. Function selection

H = HIGH voltage level; L = LOW voltage level; Z = high-impedance OFF-state.

Input	Input/output
OE	An, Bn
L	An = Bn
H	Z

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+4.6	V
V _I	input voltage	[1]	-0.5	+4.6	V
V _{SW}	switch voltage	enable and disable mode	[1]	-0.5	V _{CC} + 0.5
I _{IK}	input clamping current	V _I < -0.5 V	-50	-	mA
I _{SK}	switch clamping current	V _I < -0.5 V	-50	-	mA
I _{SW}	switch current	V _{SW} = 0 V to V _{CC}	-	±128	mA
I _{CC}	supply current		-	+100	mA
I _{GND}	ground current		-100	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C	[2]	-	500 mW

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT360-1 (TSSOP20) package: P_{tot} derates linearly with 10.0 mW/K above 100 °C.

For SOT764-1 (DHVQFN20) package: P_{tot} derates linearly with 12.9 mW/K above 111 °C.

8. Recommended operating conditions

Table 5. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		2.3	3.6	V
V _I	input voltage		0	3.6	V
V _{SW}	switch voltage	enable and disable mode	0	V _{CC}	V
T _{amb}	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 2.3 V to 3.6 V	[1]	-	200 ns/V

[1] Applies to control signal levels.

9. Static characteristics

Table 6. Static characteristics

At recommended operating conditions voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	$T_{amb} = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$			$T_{amb} = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$		Unit
			Min	Typ [1]	Max	Min	Max	
V_{IH}	HIGH-level input voltage	$V_{CC} = 2.3\text{ V}$ to 2.7 V	1.7	-	-	1.7	-	V
		$V_{CC} = 3.0\text{ V}$ to 3.6 V	2.0	-	-	2.0	-	V
V_{IL}	LOW-level input voltage	$V_{CC} = 2.3\text{ V}$ to 2.7 V	-	-	0.7	-	0.7	V
		$V_{CC} = 3.0\text{ V}$ to 3.6 V	-	-	0.9	-	0.9	V
I_I	input leakage current	pin \overline{OE} ; $V_I = \text{GND}$ to V_{CC} ; $V_{CC} = 3.6\text{ V}$	-	-	± 1	-	± 20	μA
$I_{S(OFF)}$	OFF-state leakage current	$V_{CC} = 3.6\text{ V}$; see Fig. 3	-	-	± 1	-	± 20	μA
$I_{S(ON)}$	ON-state leakage current	$V_{CC} = 3.6\text{ V}$; see Fig. 4	-	-	± 1	-	± 20	μA
I_{OFF}	power-off leakage current	V_I or $V_O = 0\text{ V}$ to 3.6 V ; $V_{CC} = 0\text{ V}$	-	-	± 10	-	± 50	μA
I_{CC}	supply current	$V_I = \text{GND}$ or V_{CC} ; $I_O = 0\text{ A}$; $V_{SW} = \text{GND}$ or V_{CC} ; $V_{CC} = 3.6\text{ V}$	-	-	10	-	50	μA
ΔI_{CC}	additional supply current	pin \overline{OE} ; $V_I = V_{CC} - 0.6\text{ V}$; [2] $V_{SW} = \text{GND}$ or V_{CC} ; $V_{CC} = 3.6\text{ V}$	-	-	300	-	2000	μA
C_I	input capacitance	pin \overline{OE} ; $V_{CC} = 3.3\text{ V}$; $V_I = 0\text{ V}$ to 3.3 V	-	0.9	-	-	-	pF
$C_{S(OFF)}$	OFF-state capacitance	$V_{CC} = 3.3\text{ V}$; $V_I = 0\text{ V}$ to 3.3 V	-	5.2	-	-	-	pF
$C_{S(ON)}$	ON-state capacitance	$V_{CC} = 3.3\text{ V}$; $V_I = 0\text{ V}$ to 3.3 V	-	14.3	-	-	-	pF

[1] All typical values are measured at $T_{amb} = 25^{\circ}\text{C}$.

[2] One input at 3 V, other inputs at V_{CC} or GND.

9.1. Test circuits

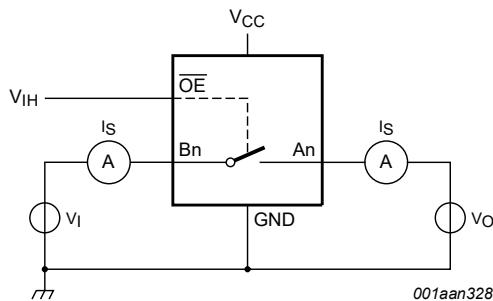


Fig. 3. Test circuit for measuring OFF-state leakage current (one switch)

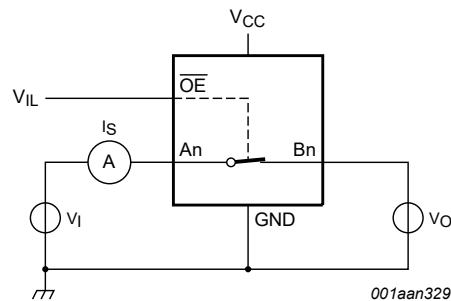


Fig. 4. Test circuit for measuring ON-state leakage current (one switch)

9.2. ON resistance

Table 7. Resistance R_{ON}

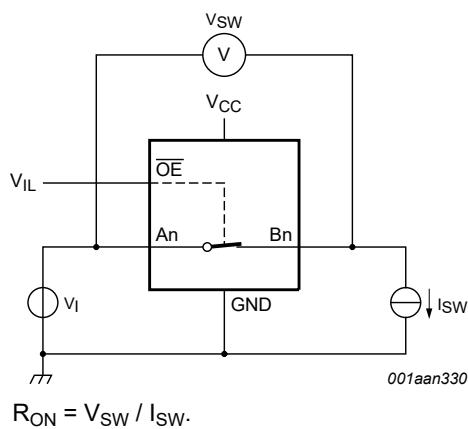
At recommended operating conditions; voltages are referenced to GND (ground = 0 V); for test circuit see [Fig. 5](#).

Symbol	Parameter	Conditions	$T_{amb} = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$		$T_{amb} = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$		Unit
			Min	Typ [1]	Max	Min	
R_{ON}	ON resistance	$V_{CC} = 2.3\text{ V}$ to 2.7 V ; see Fig. 6 to Fig. 8	[2]				
		$I_{SW} = 64\text{ mA}$; $V_I = 0\text{ V}$	-	4.2	8.0	-	Ω
		$I_{SW} = 24\text{ mA}$; $V_I = 0\text{ V}$	-	4.2	8.0	-	Ω
		$I_{SW} = 15\text{ mA}$; $V_I = 1.7\text{ V}$	-	8.4	40	-	Ω
		$V_{CC} = 3.0\text{ V}$ to 3.6 V ; see Fig. 9 to Fig. 11					
		$I_{SW} = 64\text{ mA}$; $V_I = 0\text{ V}$	-	4.0	7.0	-	Ω
		$I_{SW} = 24\text{ mA}$; $V_I = 0\text{ V}$	-	4.0	7.0	-	Ω
		$I_{SW} = 15\text{ mA}$; $V_I = 2.4\text{ V}$	-	6.2	15	-	Ω

[1] Typical values are measured at $T_{amb} = 25^{\circ}\text{C}$ and nominal V_{CC} .

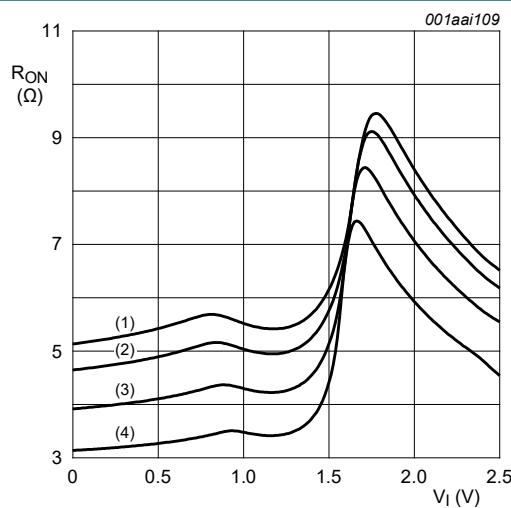
[2] Measured by the voltage drop between the A and B terminals at the indicated current through the switch. ON-state resistance is determined by the lower of the voltages of the two (A or B) terminals.

9.3. ON resistance test circuit and graphs



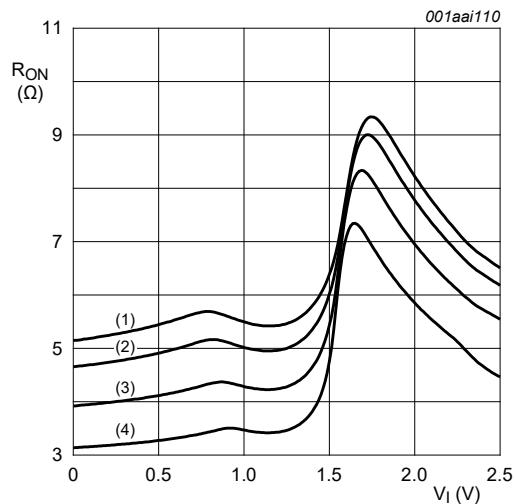
$$R_{ON} = V_{SW} / I_{SW}.$$

Fig. 5. Test circuit for measuring ON resistance (one switch)



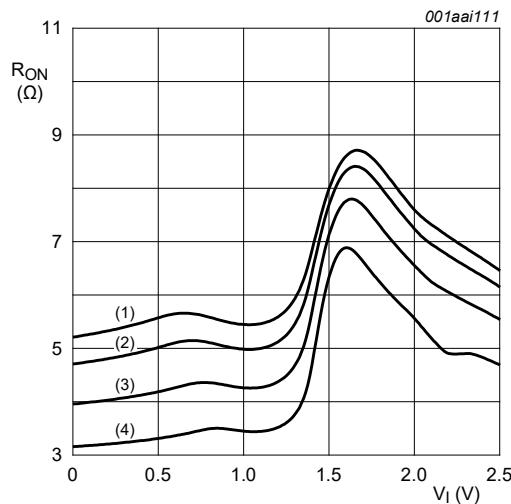
- (1) $T_{amb} = 125 \text{ }^{\circ}\text{C}.$
- (2) $T_{amb} = 85 \text{ }^{\circ}\text{C}.$
- (3) $T_{amb} = 25 \text{ }^{\circ}\text{C}.$
- (4) $T_{amb} = -40 \text{ }^{\circ}\text{C}.$

Fig. 6. ON resistance as a function of input voltage; $V_{CC} = 2.5 \text{ V}$; $I_{SW} = 15 \text{ mA}$



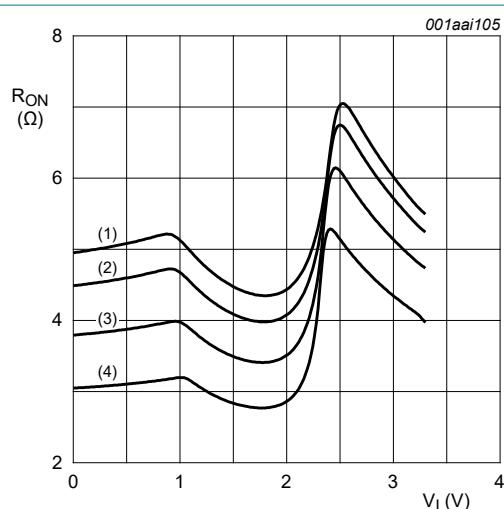
- (1) $T_{amb} = 125 \text{ }^{\circ}\text{C}.$
- (2) $T_{amb} = 85 \text{ }^{\circ}\text{C}.$
- (3) $T_{amb} = 25 \text{ }^{\circ}\text{C}.$
- (4) $T_{amb} = -40 \text{ }^{\circ}\text{C}.$

Fig. 7. ON resistance as a function of input voltage; $V_{CC} = 2.5 \text{ V}$; $I_{SW} = 24 \text{ mA}$



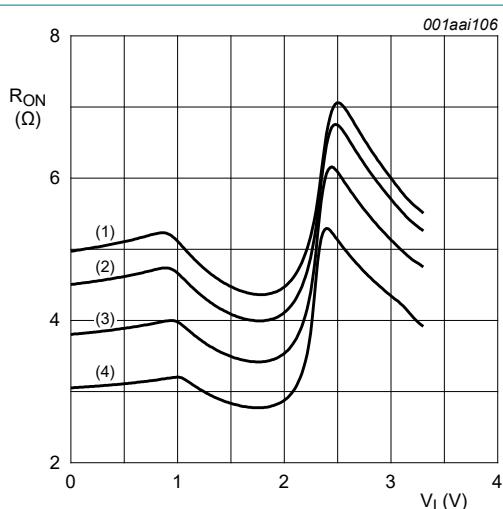
- (1) $T_{amb} = 125 \text{ }^{\circ}\text{C}.$
- (2) $T_{amb} = 85 \text{ }^{\circ}\text{C}.$
- (3) $T_{amb} = 25 \text{ }^{\circ}\text{C}.$
- (4) $T_{amb} = -40 \text{ }^{\circ}\text{C}.$

Fig. 8. ON resistance as a function of input voltage; $V_{CC} = 2.5 \text{ V}$; $I_{SW} = 64 \text{ mA}$



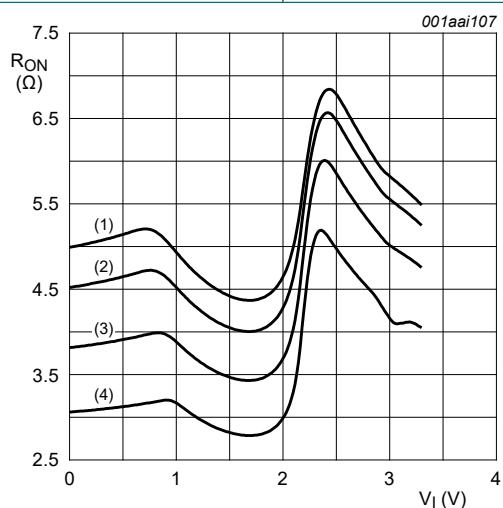
- (1) $T_{amb} = 125$ °C.
- (2) $T_{amb} = 85$ °C.
- (3) $T_{amb} = 25$ °C.
- (4) $T_{amb} = -40$ °C.

Fig. 9. ON resistance as a function of input voltage; $V_{CC} = 3.3$ V; $I_{SW} = 15$ mA



- (1) $T_{amb} = 125$ °C.
- (2) $T_{amb} = 85$ °C.
- (3) $T_{amb} = 25$ °C.
- (4) $T_{amb} = -40$ °C.

Fig. 10. ON resistance as a function of input voltage; $V_{CC} = 3.3$ V; $I_{SW} = 24$ mA



- (1) $T_{amb} = 125$ °C.
- (2) $T_{amb} = 85$ °C.
- (3) $T_{amb} = 25$ °C.
- (4) $T_{amb} = -40$ °C.

Fig. 11. ON resistance as a function of input voltage; $V_{CC} = 3.3$ V; $I_{SW} = 64$ mA

10. Dynamic characteristics

Table 8. Dynamic characteristics

GND = 0 V; for test circuit see [Fig. 14](#)

Symbol	Parameter	Conditions	$T_{amb} = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$			$T_{amb} = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$		Unit
			Min	Typ [1]	Max	Min	Max	
t_{pd}	propagation delay	An to Bn or Bn to An; [2] [3] see Fig. 12						
		$V_{CC} = 2.3\text{ V}$ to 2.7 V	-	-	0.13	-	0.20	ns
		$V_{CC} = 3.0\text{ V}$ to 3.6 V	-	-	0.20	-	0.31	ns
t_{en}	enable time	\overline{OE} to An or Bn; see Fig. 13 [4]						
		$V_{CC} = 2.3\text{ V}$ to 2.7 V	1.0	3.4	5.5	1.0	8.0	ns
		$V_{CC} = 3.0\text{ V}$ to 3.6 V	1.0	3.0	4.9	1.0	7.0	ns
t_{dis}	disable time	\overline{OE} to An or Bn; see Fig. 13 [5]						
		$V_{CC} = 2.3\text{ V}$ to 2.7 V	1.0	3.0	5.5	1.0	8.0	ns
		$V_{CC} = 3.0\text{ V}$ to 3.6 V	1.0	3.4	5.8	1.0	8.5	ns

[1] All typical values are measured at $T_{amb} = 25^{\circ}\text{C}$ and at nominal V_{CC} .

[2] The propagation delay is the calculated RC time constant of the typical on-state resistance of the switch and the load capacitance, when driven by an ideal voltage source (zero output impedance).

[3] t_{pd} is the same as t_{PLH} and t_{PHL} .

[4] t_{en} is the same as t_{PZH} and t_{PZL} .

[5] t_{dis} is the same as t_{PHZ} and t_{PLZ} .

10.1. Waveforms and test circuit

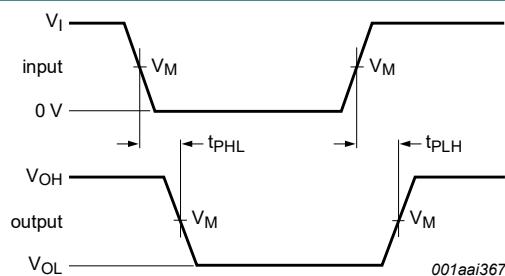


Fig. 12. The data input (A_n, B_n) to output (B_n, A_n) propagation delay times

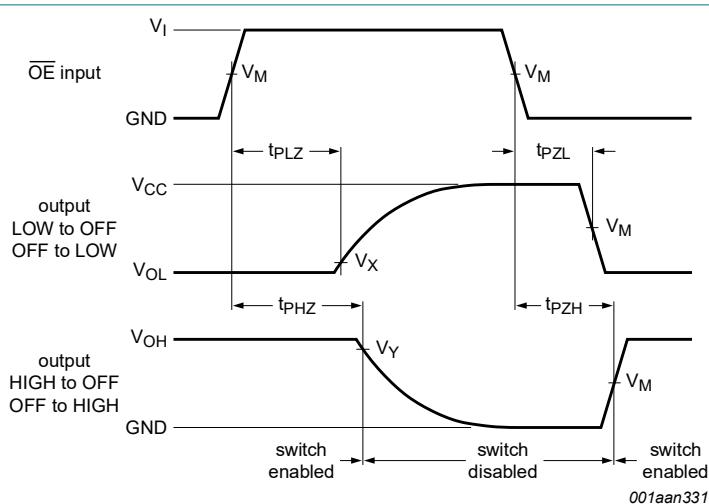
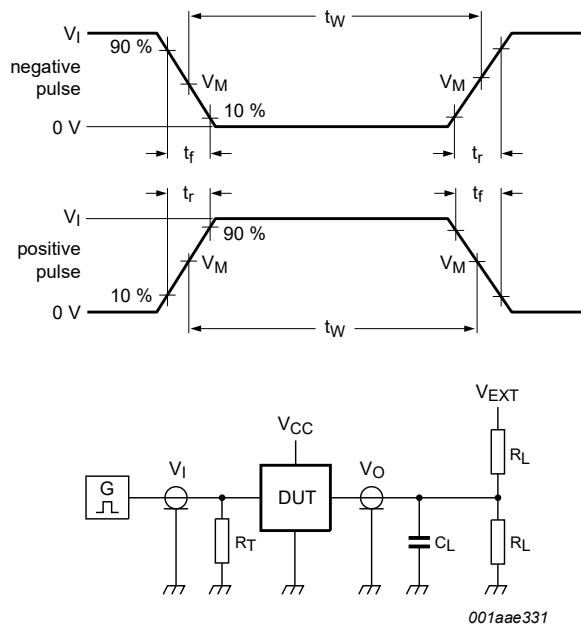


Fig. 13. Enable and disable times

Table 9. Measurement points

Supply voltage	Input				Output		
V_{CC}	V_M	V_I	$t_r = t_f$		V_M	V_X	V_Y
2.3 V to 2.7 V	$0.5 \times V_{CC}$	V_{CC}	≤ 2.0 ns		$0.5 \times V_{CC}$	$V_{OL} + 0.15$ V	$V_{OH} - 0.15$ V
3.0 V to 3.6 V	$0.5 \times V_{CC}$	V_{CC}	≤ 2.0 ns		$0.5 \times V_{CC}$	$V_{OL} + 0.3$ V	$V_{OH} - 0.3$ V



Test data is given in [Table 10](#).

Definitions for test circuit:

R_L = Load resistance;

C_L = Load capacitance including jig and probe capacitance;

R_T = Termination resistance should be equal to the output impedance Z_o of the pulse generator;

V_{EXT} = External voltage for measuring switching times.

Fig. 14. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Load		V_{EXT}		
V_{CC}	C_L	R_L	t_{PLH}, t_{PHL}	t_{PZH}, t_{PHZ}	t_{PZL}, t_{PLZ}
2.3 V to 2.7 V	30 pF	500 Ω	open	GND	$2 \times V_{CC}$
3.0 V to 3.6 V	50 pF	500 Ω	open	GND	$2 \times V_{CC}$

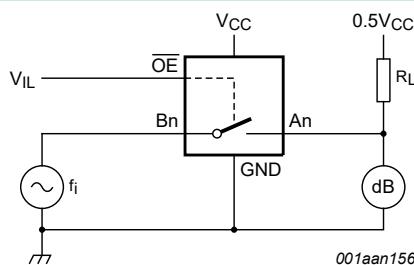
10.2. Additional dynamic characteristics

Table 11. Additional dynamic characteristics

GND = 0 V.

Symbol	Parameter	Conditions	$T_{amb} = 25 \text{ }^{\circ}\text{C}$			Unit
			Min	Typ	Max	
$f_{(-3\text{dB})}$	-3 dB frequency response	$V_{CC} = 3.3 \text{ V}$; $R_L = 50 \Omega$; see Fig. 15	[1]	-	406	- MHz

[1] f_i is biased at $0.5V_{CC}$.



Adjust f_i voltage to obtain 0 dBm level at output. Increase f_i frequency until dB meter reads -3 dB.

Fig. 15. Test circuit for measuring the frequency response when channel is in ON-state

11. Package outline

TSSOP20: plastic thin shrink small outline package; 20 leads; body width 4.4 mm

SOT360-1

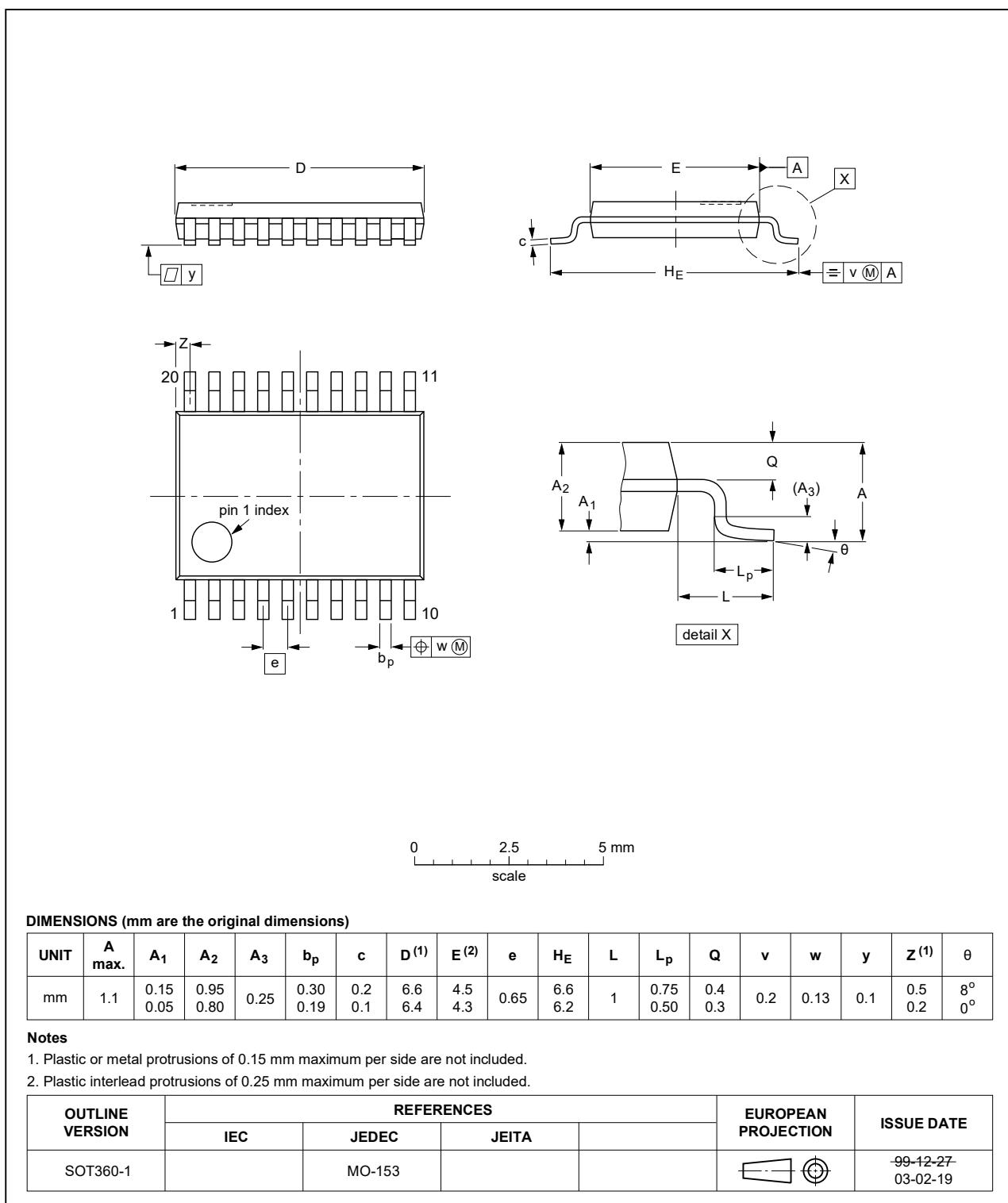


Fig. 16. Package outline SOT360-1 (TSSOP20)

DHVQFN20: plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads;
20 terminals; body 2.5 x 4.5 x 0.85 mm

SOT764-1

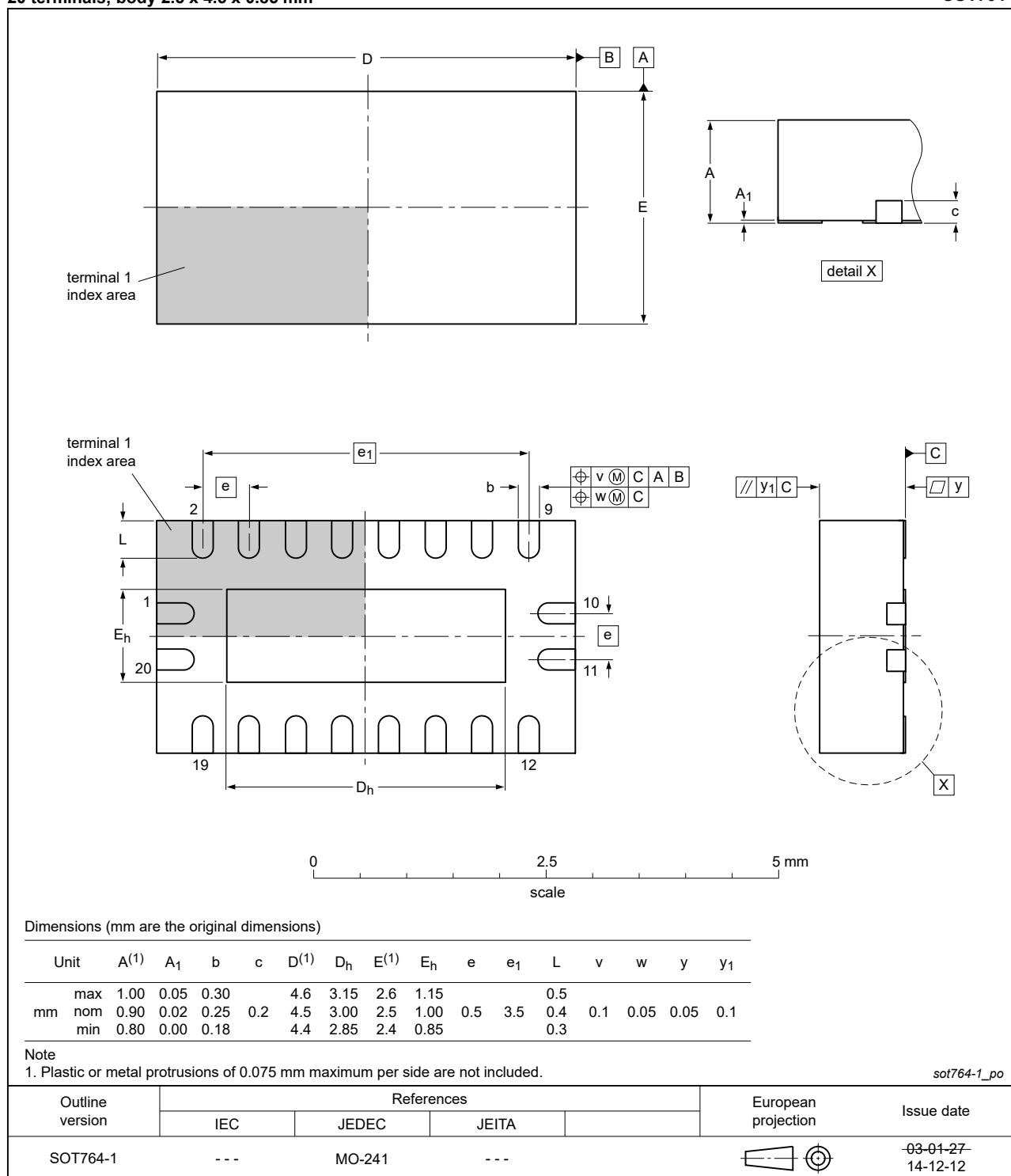


Fig. 17. Package outline SOT764-1 (DHVQFN20)

12. Abbreviations

Table 12. Abbreviations

Acronym	Description
ANSI	American National Standards Institute
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
ESDA	ElectroStatic Discharge Association
HBM	Human Body Model
JEDEC	Joint Electron Device Engineering Council

13. Revision history

Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74CBTLV3245_Q100 v.5	20240624	Product data sheet	-	74CBTLV3245_Q100 v.4
Modifications:				<ul style="list-style-type: none"> • Section 2: ESD specification updated according to the latest JEDEC standard.
74CBTLV3245_Q100 v.4	20200507	Product data sheet	-	74CBTLV3245_Q100 v.3
Modifications:				<ul style="list-style-type: none"> • Section 2 updated. • Table 4: Derating values for P_{tot} total power dissipation updated.
74CBTLV3245_Q100 v.3	20190412	Product data sheet	-	74CBTLV3245_Q100 v.2
Modifications:				<ul style="list-style-type: none"> • The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. • Legal texts have been adapted to the new company name where appropriate.
74CBTLV3245_Q100 v.2	20161110	Product data sheet	-	74CBTLV3245_Q100 v.1
Modifications:				<ul style="list-style-type: none"> • Additional dynamic characteristics added.
74CBTLV3245_Q100 v.1	20160414	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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Contents

1. General description.....	1
2. Features and benefits.....	1
3. Ordering information.....	1
4. Functional diagram.....	2
5. Pinning information.....	2
5.1. Pinning.....	2
5.2. Pin description.....	2
6. Functional description.....	3
7. Limiting values.....	3
8. Recommended operating conditions.....	3
9. Static characteristics.....	4
9.1. Test circuits.....	5
9.2. ON resistance.....	5
9.3. ON resistance test circuit and graphs.....	6
10. Dynamic characteristics.....	8
10.1. Waveforms and test circuit.....	9
10.2. Additional dynamic characteristics.....	11
11. Package outline.....	12
12. Abbreviations.....	14
13. Revision history.....	14
14. Legal information.....	15

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For sales office addresses, please send an email to: salesaddresses@nexperia.com

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