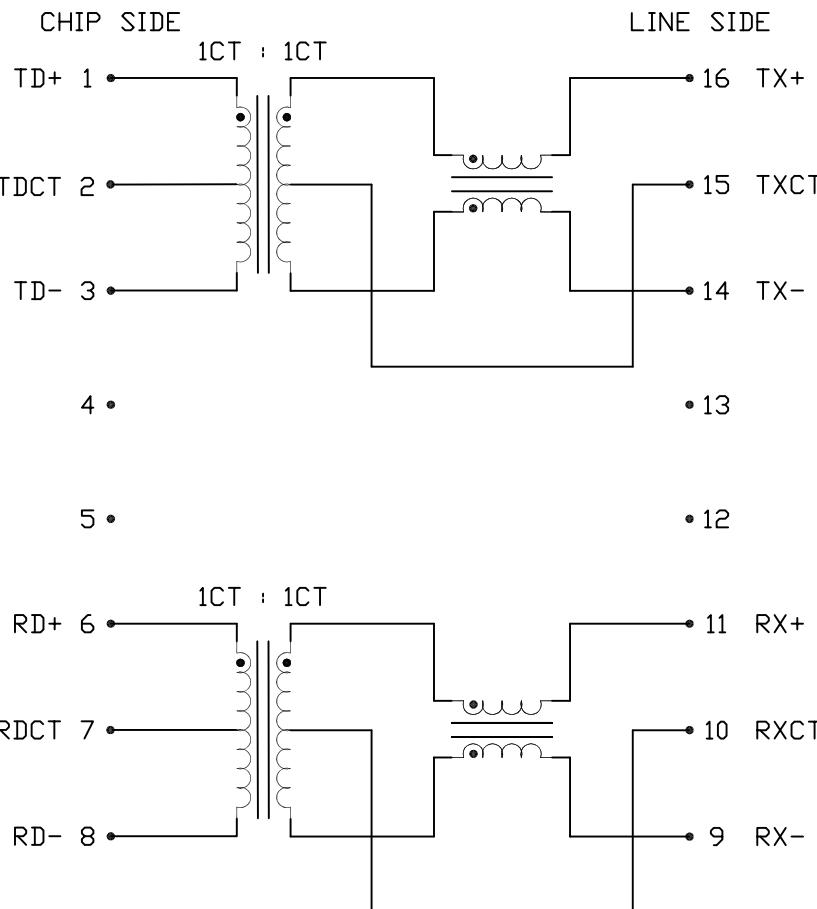


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SCHEMATICELECTRICAL CHARACTERISTICS @25°C

TURNS RATIO 1-2-3 : 16-15-14 6-7-8 : 11-10-9	1CT : 1CT 1CT : 1CT
POLARITY □CL Tx PIN 1-3 Rx PIN 6-8	PER DOT CONVENTION
LEAKAGE INDUCTANCE, Le CHIP SIDE WITH LINE SIDE SHORT	350µH MIN @100kHz, 100mVrms w/8mA DC BIAS
INTERWINDING CAPACITANCE, Cw/w CHIP SIDE TO LINE SIDE	0.3µH MAX @1MHz, 20mVrms
DC RESISTANCE PIN (1-3) = (6-8)	30pF MAX @1MHz, 20mVrms
INSERTION LOSS 300kHz - 80MHz	1.1Ωhm MAX
RETURN LOSS 300kHz - 30MHz 30MHz - 60MHz 60MHz - 80MHz	1.0dB MAX
COMMON TO COMMON MODE REJ 300kHz - 100MHz	18dB MIN
COMMON TO DIFFERENTIAL MODE REJ 300kHz - 100MHz	18-20LOG(F/30MHz) dB MIN 12dB MIN
CROSSTALK 300kHz - 100MHz	40dB TYP
HIPOT	LEAKAGE CURRENT LESS THAN 1mA AT 1500VAC
OPERATING TEMPERATURE -40°C TO +85°C	

RoHS

NOTES :

THIS PRODUCT HAS BEEN SUPPLYING TO JAPAN CUSTOMERS.
PCN PROCESS NEEDS TO COMPLY WITH 4M CRITERIA.

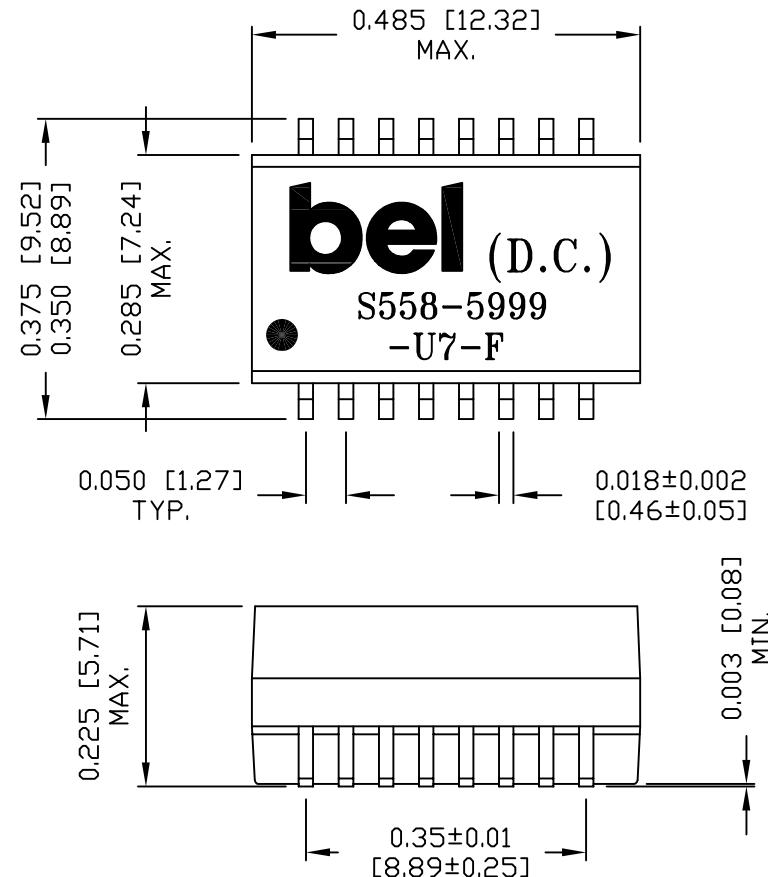
REV. : A	PAGE : 2
ORIGINATED BY	DATE
Alice Pang	2021-01-28
DRAWN BY	DATE
ZC Guo	2021-01-28
TITLE	
10/100BT MAGNETICS MODULE	
S558-5999-U7-F	
PART NO. / DRAWING NO.	STANDARD DIM.
S558-5999-U7-F	[] METRIC DIM. AS REF.
FILE NAME	TOL. IN INCH
S5585999U7FA.DWG	.X
	.XX
	.XXX
	UNIT : INCH [mm]
	SCALE : N/A
	SIZE : A4



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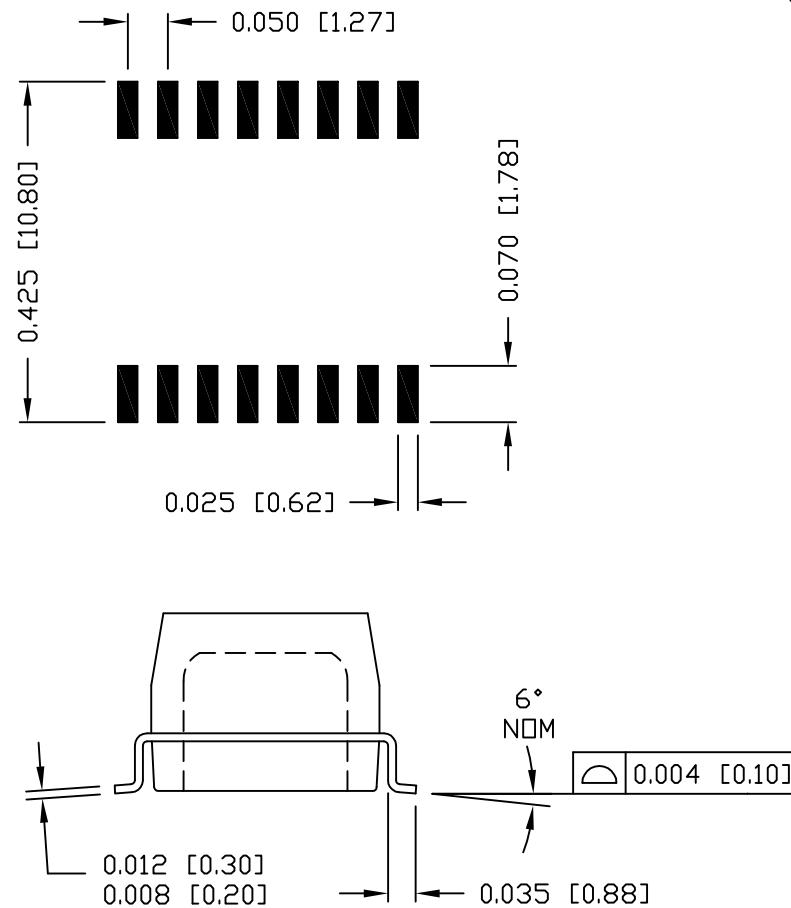
RoHS



NOTES :

1. STANDARD MARKING REFER TO DOC. HAND-WORK-04.
2. PACKAGE CODE: "QBS001".
3. THIS PRODUCT HAS BEEN SUPPLYING TO JAPAN CUSTOMERS.
PCN PROCESS NEEDS TO COMPLY WITH 4M CRITERIA.

SUGGESTED PCB PAD LAYOUT



ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM.	[] METRIC DIM. AS REF.	REV. : A	PAGE : 3
Lawrence Tsang	2021-01-28		S558-5999-U7-F	TOL. IN INCH	UNIT : INCH [mm]		
DRAWN BY	DATE		FILE NAME	.X	.XX ±0.01	SCALE : N/A	
ZC Guo	2021-01-28		S5585999U7FA.DWG	.XXX	±0.005	SIZE : A4	

