

PT-PWR-EPS-002_v1

v1

23/07/2019

RELEASED 12-JUN-2019

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2	BLOCK DIAGRAM	2	BLOCK DIAGRAM
3	CONNECTORS	3	CONNECTORS
4	SOLAR BATTERY CHARGERS	4	SOLAR BATTERY CHARGERS

DESIGN CONSIDERATIONS:

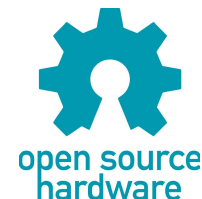
DESIGN NOTE:
Example text for informational
design notes.

DESIGN NOTE:
Example text for debug notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.

DESIGN NOTE:
Example text for cautionary
design notes.

DESIGN NOTE:
Example text for critical
design notes.



Project: **PT-PWR-EPS-002_v1.PrjPcb**

Page Contents: **[01] - COVER PAGE.SchDoc**

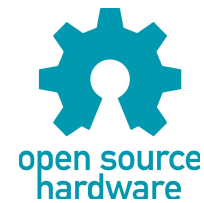
Revision: **v1** Design: **JABN** Date: **23/07/2019**

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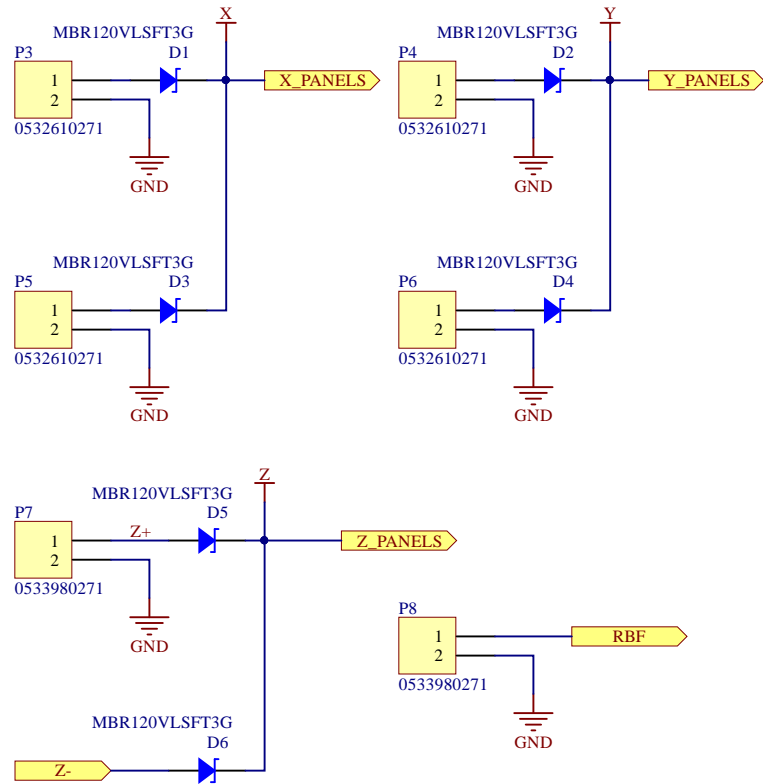
(Block Diagram)



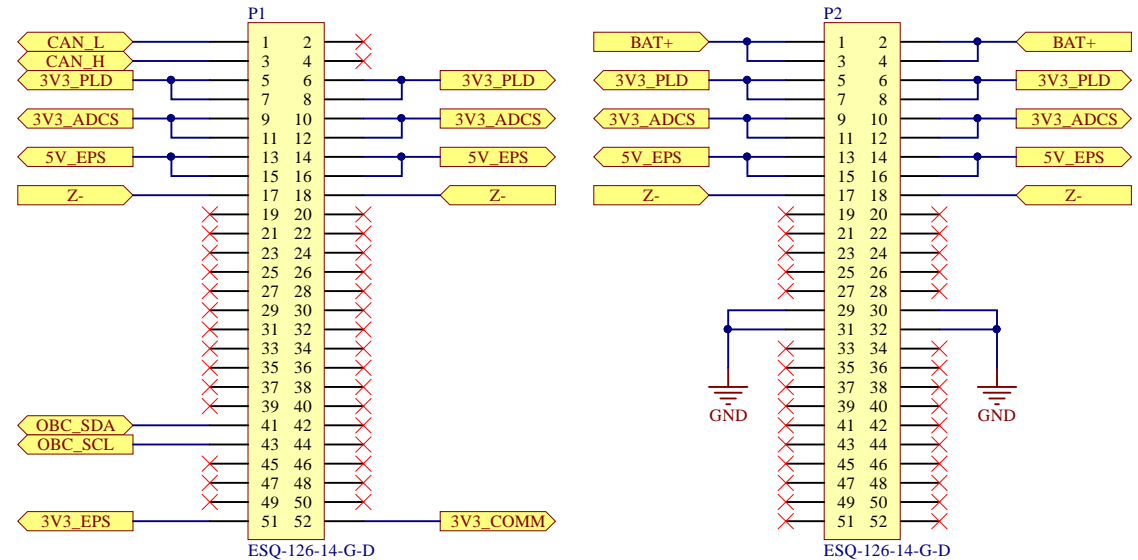
Project: PT-PWR-EPS-002_v1.PrjPcb			
Page Contents: [02] - BLOCK DIAGRAM.SchDoc			UNIVERSIDAD DEL VALLE DE GUATEMALA
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CONNECTORS

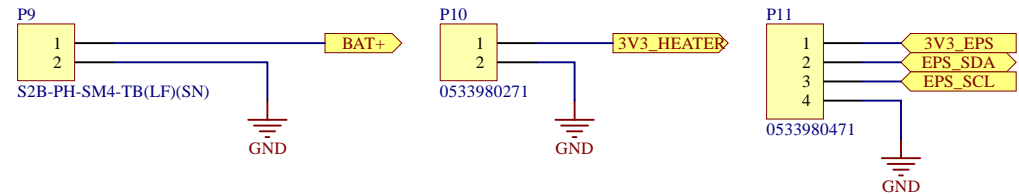
SOLAR PANELS



PC/104-GomSpace NanoDock



BATTERY, HEATER AND TEMPERATURE SENSOR



Project: **PT-PWR-EPS-002_v1.PrjPcb**

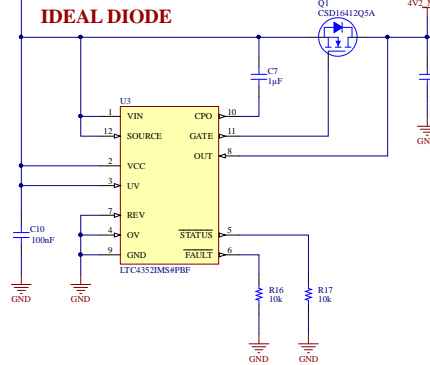
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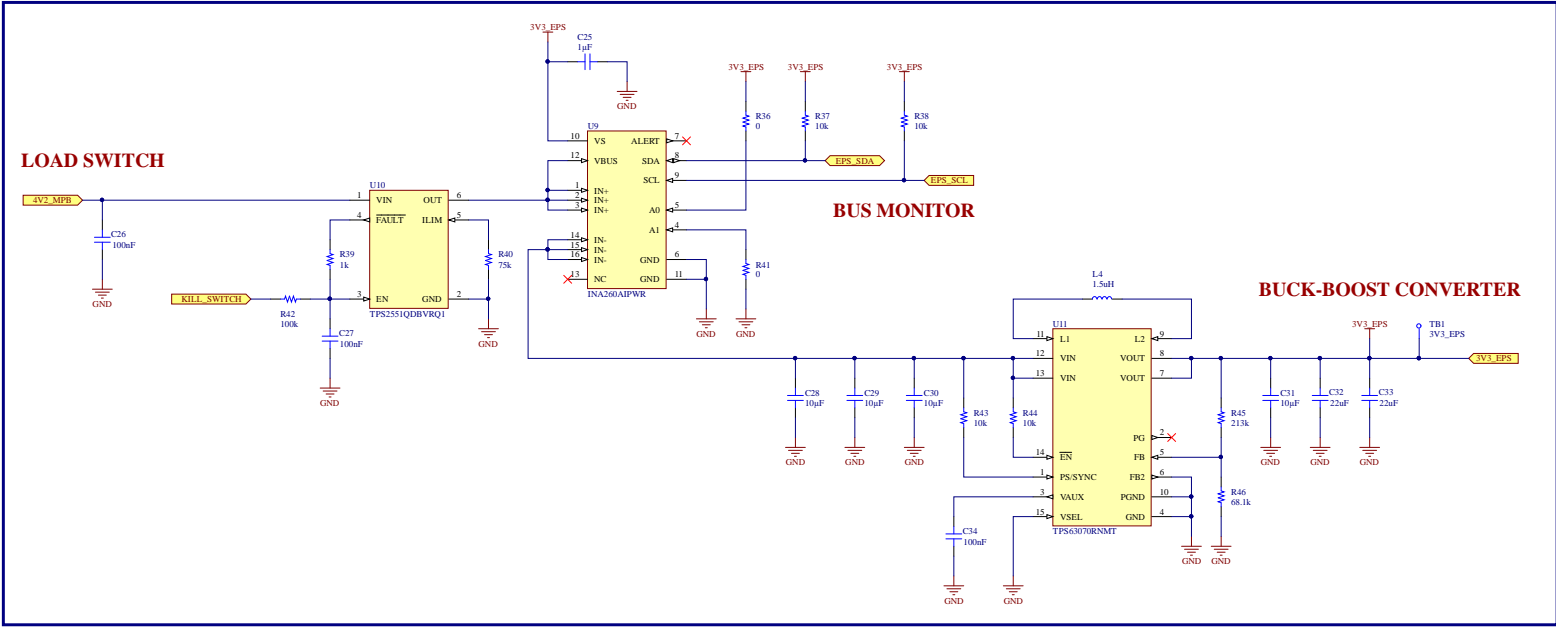
Size: **A4** Document Number: **3** Sheet **3** of **12**

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SOLAR BATTERY CHARGER (X - PANELS)



3V3 VOLTAGE REGULATOR



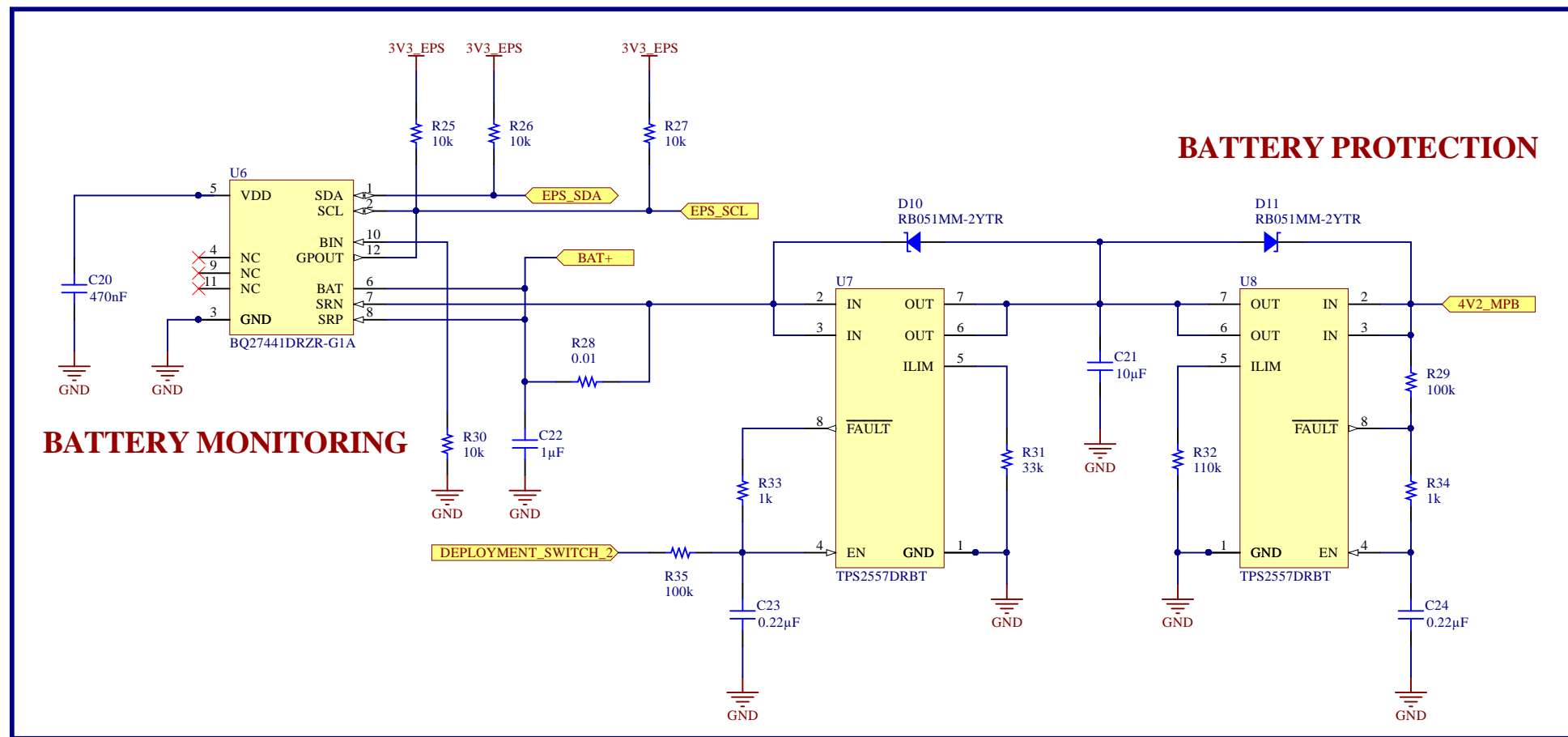
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Page Contents: **[06] - 3V3 VOLTAGE REGULATOR.SchDoc**

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ENERGY STORAGE AND PROTECTION



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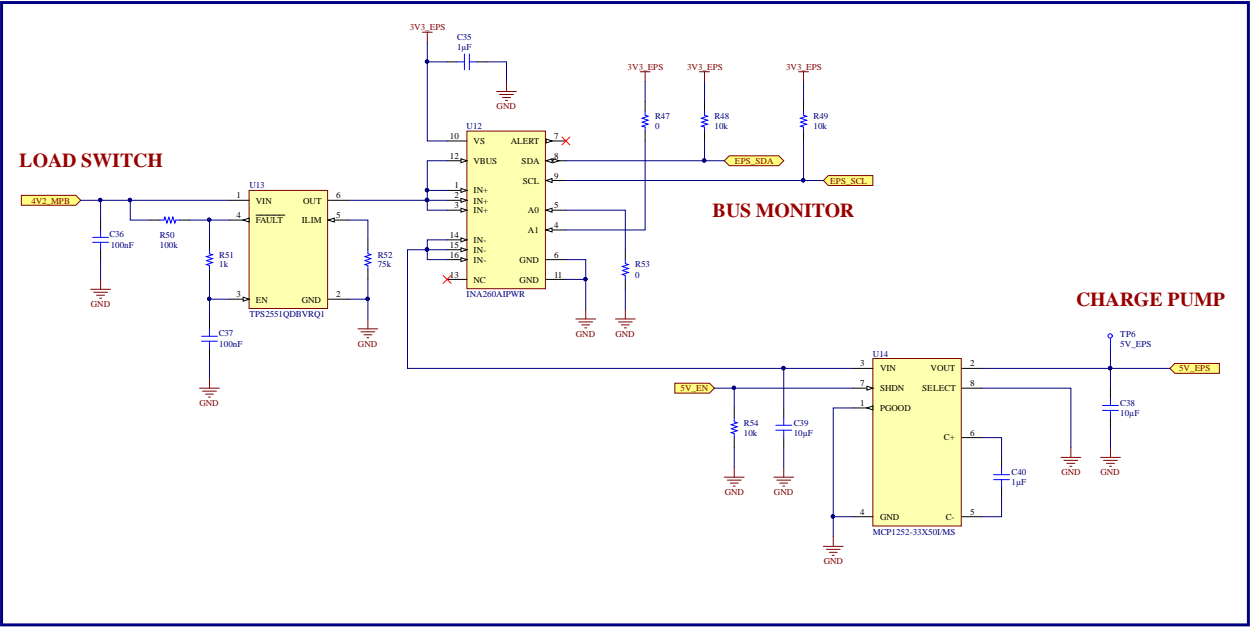
Page Contents: **[05] - ENERGY STORAGE AND PROTECTION.SchDoc**

Revision: **v1** Design: **JABN** Date: **23/07/2019**

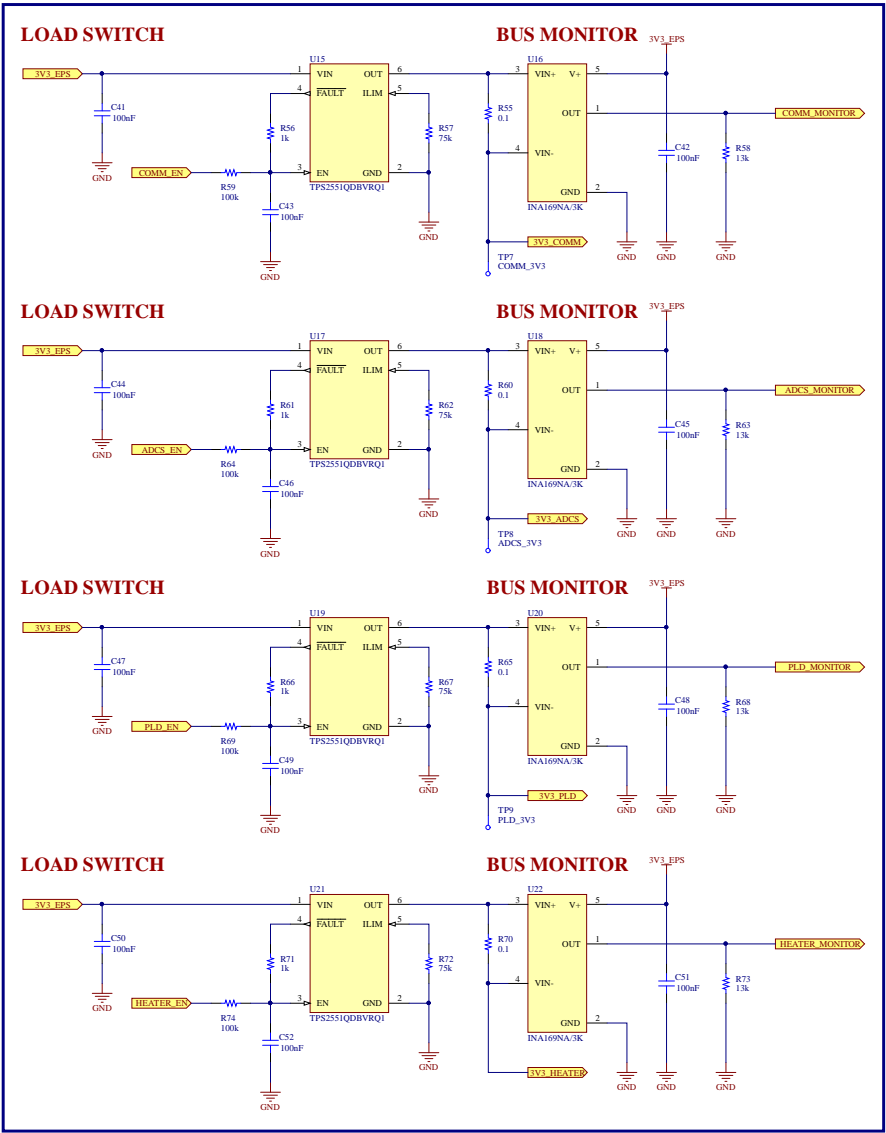
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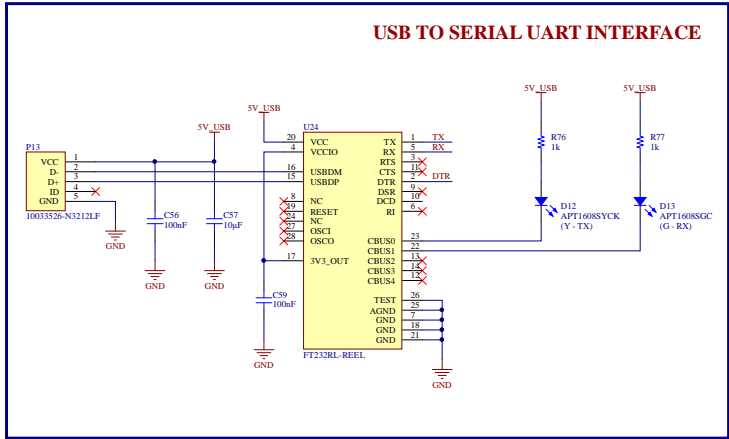
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5V VOLTAGE REGULATOR



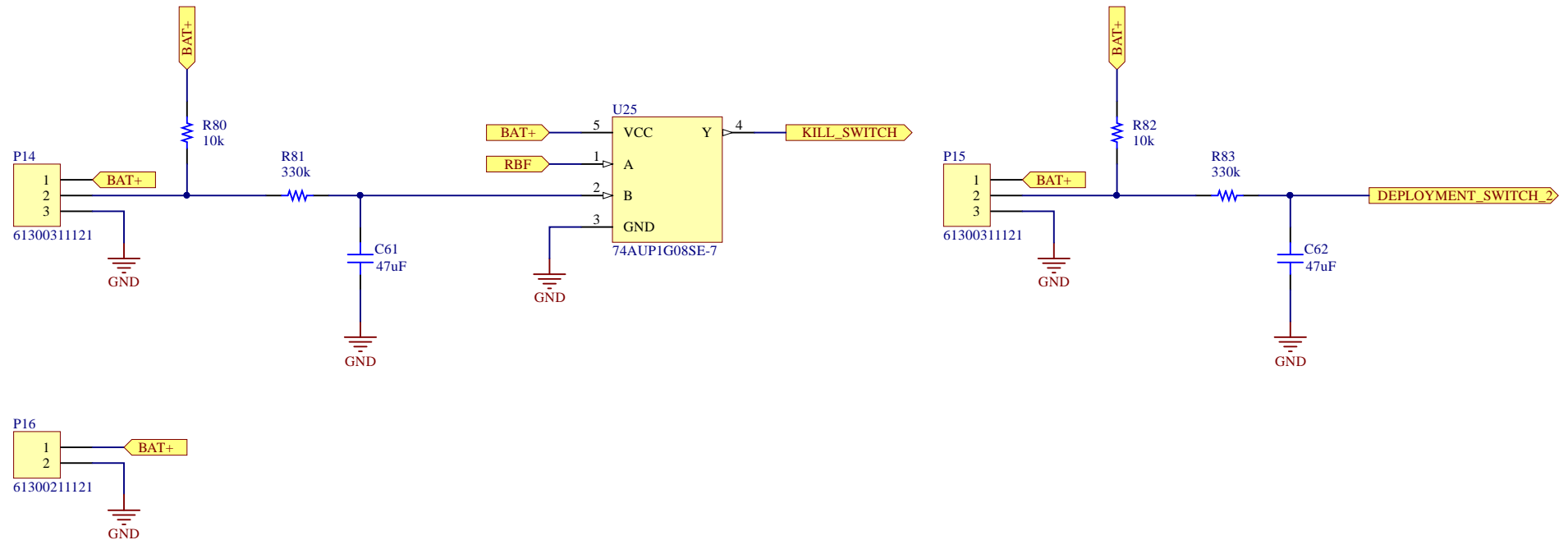
POWER CONTROL AND DISTRIBUTION



[illegible]

DEPLOYMENT SWITCHES, RBF AND FPP

DEPLOYMENT SWITCHES AND RBF INTERFACE



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REVISION HISTORY

12/06/2019 (JABN) - Initial release. Board



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<div>Designator [01] - COVER PAGE.SchDoc</div> <div></div>	<div>Designator [02] - BLOCK DIAGRAM.SchDoc</div> <div></div>	<div>Designator [03] - CONNECTORS</div> <div></div>	<div>Designator [04] - POWER HARVESTING SYSTEM.SchDoc</div> <div></div>
<div>Designator [05] - ENERGY STORAGE AND PROTECTION.SchDoc</div> <div></div>	<div>Designator [06] - 5V VOLTAGE REGULATOR.SchDoc</div> <div></div>		
<div>Designator [07] - 5V VOLTAGE REGULATOR.SchDoc</div> <div></div>	<div>Designator [08] - POWER CONTROL AND DISTRIBUTION.SchDoc</div> <div></div>	<div>Designator [09] - MICROCONTROLLER UNIT.SchDoc</div> <div></div>	<div>Designator [10] - DEPLOYMENT SWITCHES AND RBF.SchDoc</div> <div></div>
<div>Designator [11] - REVISION HISTORY.SchDoc</div> <div></div>			
<div>NOTES</div> <div>Mark not fitted components as NF</div> <div>DRAFT - Very early stage of schematic.</div> <div>PRELIMINARY - Close to final schematic.</div> <div>CHECKED - There should not be any mistakes.</div> <div>RELEASED - A board with this schematic has been sent to production.</div>			
		<div>Project: PT-PWR-EPS-002_v1.PrjPcb</div> <div>Page Contents: PT-PWR-EPS-002_v1 Project.SchDoc</div> <div>Revision: v1Design: JABNDate: 23/07/2019</div> <div>Size: A4Document Number: 12Sheet 12 of 12</div>	<div>UVG UNIVERSIDAD DEL VALLE DE GUATEMALA</div>
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