

PT-MIS-PCB-004
v1

21/03/2019

RELEASED 21-MAR-2019

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DESIGN CONSIDERATIONS:

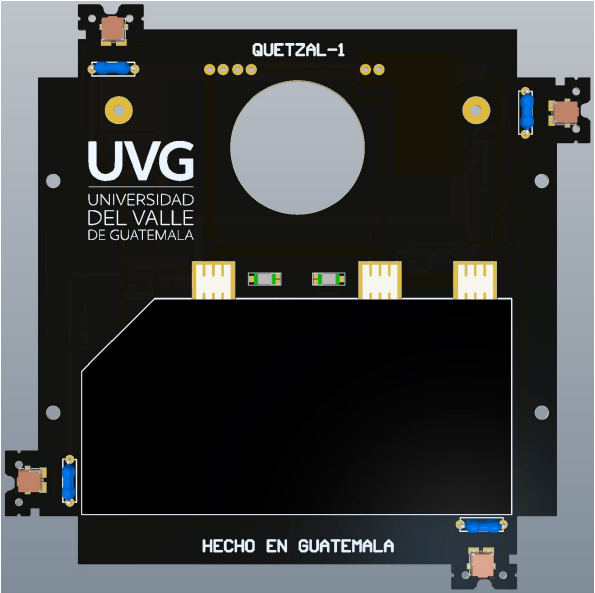
DESIGN NOTE:
Example text for informational
design notes.


DESIGN NOTE:
Example text for debug notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.

DESIGN NOTE:
Example text for cautionary
design notes.

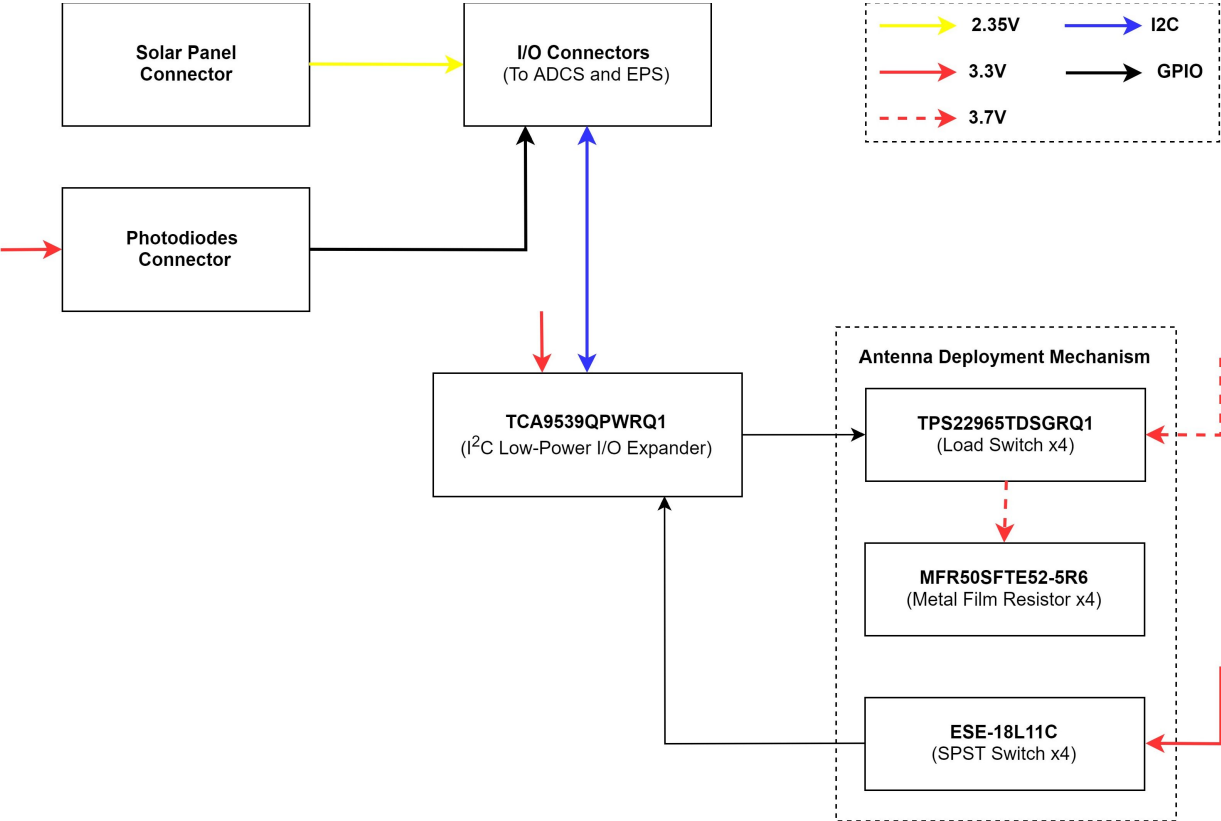
DESIGN NOTE:
Example text for critical
design notes.



Project: <i>PT-MIS-PCB-004_v1.PrfPcb</i>			 UNIVERSIDAD DEL VALLE DE GUATEMALA
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Revision: v1	Design: JABN	Date: 21/03/2019	
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PT-MIS-PCB-004


(Block Diagram)



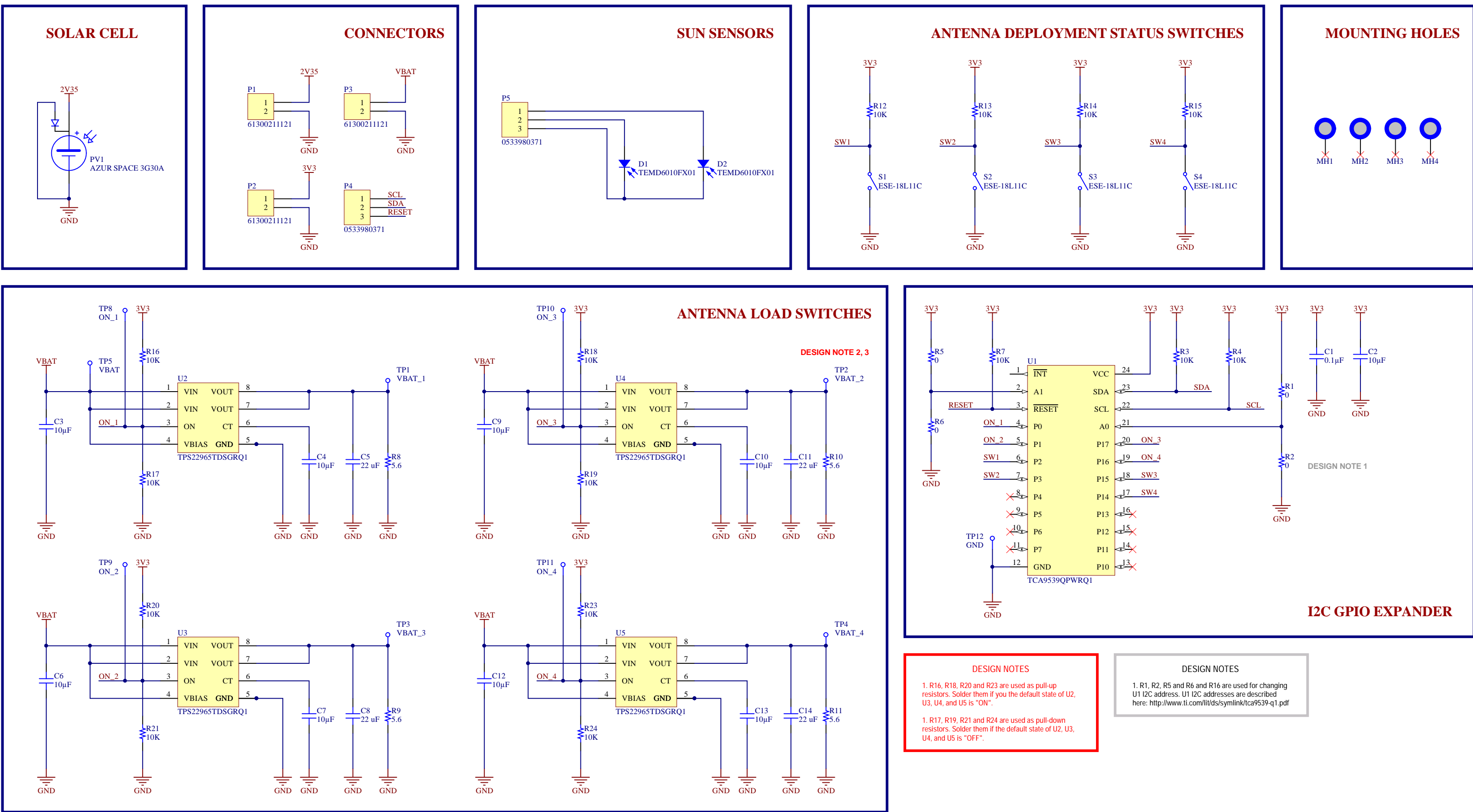
Project: ***PT-MIS-PCB-004_v1.PrjPcb***

Page Contents: ***[02] - BLOCK DIAGRAM.SchDoc***

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PT-MIS-PCB-004 SCHEMATIC



Project: **PT-MIS-PCB-004_v1.PrjPcb**

Page Contents: **[03] - SCHEMATIC.SchDoc**


Revision: **v1** Design: **JABN** Date: **21/03/2019**

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REVISION HISTORY

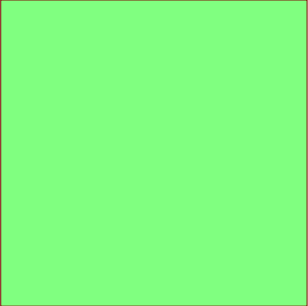
29/11/2018 (JABN) - Initial release.

08/02/2019 (JABN) - R10 GND routing change.

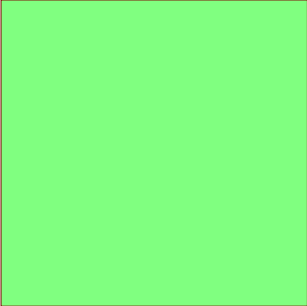
Project: <i>PT-MIS-PCB-004_v1.PrjPcb</i>			 UNIVERSIDAD DEL VALLE DE GUATEMALA
Page Contents: <i>[04] - REVISION HISTORY.SchDoc</i>			
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A	<div>Designator [01] - COVER PAGE.SchDoc</div> <div></div>		<div>Designator [02] - BLOCK DIAGRAM.SchDoc</div> <div></div>		<div>Designator [03] - SCHEMATIC.SchDoc</div> <div></div>		<div>Designator [04] - REVISION HISTORY.SchDoc</div> <div></div>												
B																			
C	<div>NOTES</div> <div>Mark not fitted components as NF</div> <div>DRAFT - Very early stage of schematic.</div> <div>PRELIMINARY - Close to final schematic.</div> <div>CHECKED - There should not be any mistakes.</div> <div>RELEASED - A board with this schematic has been sent to production.</div>																		
D					<table><tr><td colspan="3">Project: PT-MIS-PCB-004_v1.PrfPcb</td></tr><tr><td colspan="3">Page Contents: PT-MIS-PCB-004_v1 Project.SchDoc</td></tr><tr><td>Revision: v1</td><td>Design: JABN</td><td>Date: 21/03/2019</td></tr><tr><td>Size: A4</td><td>Document Number: 5</td><td>Sheet 5 of 5</td></tr></table>			Project: PT-MIS-PCB-004_v1.PrfPcb			Page Contents: PT-MIS-PCB-004_v1 Project.SchDoc			Revision: v1	Design: JABN	Date: 21/03/2019	Size: A4	Document Number: 5	Sheet 5 of 5
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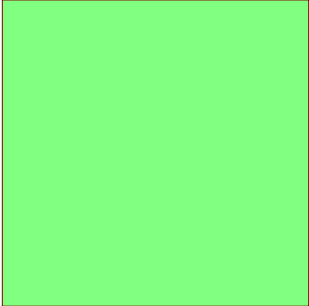
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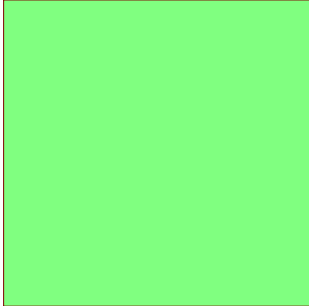
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[02] - BLOCK DIAGRAM.SchDoc



Designator
[03] - SCHEMATIC.SchDoc



Designator
[04] - REVISION HISTORY.SchDoc



NOTES

Mark not fitted components as **NF**

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