

(Power and reference left as an exercise)
(Do use a dual supply)

What would David Gilmour do:

- buy two graphic EQ's
- buy a pan pedal

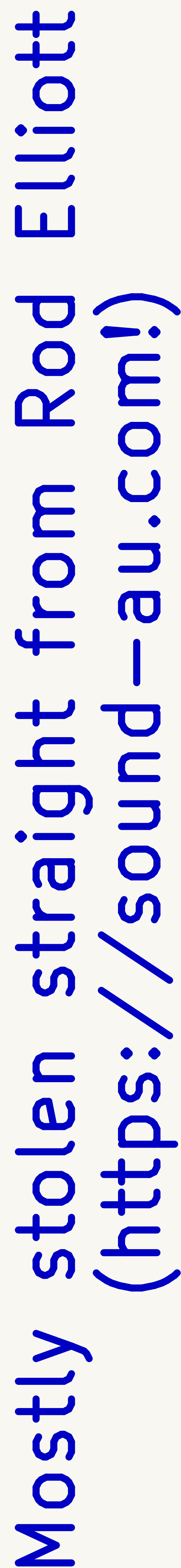
The diagram illustrates a 3-bit digital-to-analog converter (DAC) circuit. It consists of three operational amplifiers (NE5532) and various resistors. The circuit is powered by a reference voltage (VREF) and includes feedback loops labeled "Cuts", "Boosts", and "Edu". The output is labeled "Buffer".

Components and Connections:

- Op-Amp 1 (NE5532 U11A):**
 - Non-inverting input (+): Connected to VREF via a 10k resistor (R82).
 - Inverting input (-): Connected to the output via a 10k resistor (R83) and to the output of Op-Amp 2 via a 10k resistor (R84).
 - Output: Connected to the output of Op-Amp 2 via a 10k resistor (R85) and to the output of Op-Amp 3 via a 10k resistor (R86).
 - Feedback: Connected to the output via a 10k resistor (R87).
- Op-Amp 2 (NE5532 U11B):**
 - Non-inverting input (+): Connected to VREF via a 10k resistor (R82).
 - Inverting input (-): Connected to the output via a 10k resistor (R83) and to the output of Op-Amp 1 via a 10k resistor (R84).
 - Output: Connected to the output of Op-Amp 1 via a 10k resistor (R85) and to the output of Op-Amp 3 via a 10k resistor (R86).
 - Feedback: Connected to the output via a 10k resistor (R87).
- Op-Amp 3 (NE5532 U13B):**
 - Non-inverting input (+): Connected to VREF via a 10k resistor (R82).
 - Inverting input (-): Connected to the output via a 10k resistor (R83) and to the output of Op-Amp 1 via a 10k resistor (R84).
 - Output: Connected to the output of Op-Amp 1 via a 10k resistor (R85) and to the output of Op-Amp 2 via a 10k resistor (R86).
 - Feedback: Connected to the output via a 10k resistor (R87).

Labels and Annotations:

- Cuts:** A label indicating a cut in the feedback loop of Op-Amp 1.
- Boosts:** A label indicating a boost in the feedback loop of Op-Amp 2.
- Edu:** A label indicating the output of the circuit.
- Buffer:** A label indicating the output of the circuit.



The schematic diagram illustrates a 10-channel differential amplifier circuit. It features 10 identical stages, each utilizing an RC4580 operational amplifier. The non-inverting input (pin 1) of each op-amp is connected to a voltage divider consisting of resistors R1 and R2, which is referenced to VREF. The feedback network includes a capacitor C1 and a resistor R3. The differential input network consists of capacitors C2 and C4, and resistors R5 and R6. The output of each stage is connected to a load resistor (RV1 to RV10) and a common output line. The circuit is powered by a 5V supply and ground.