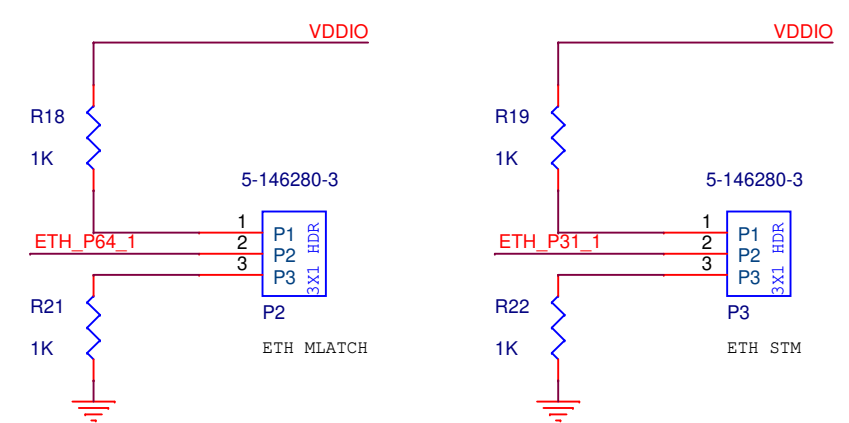
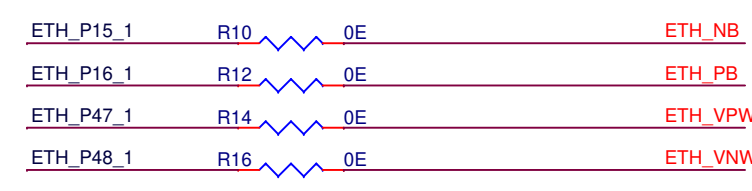
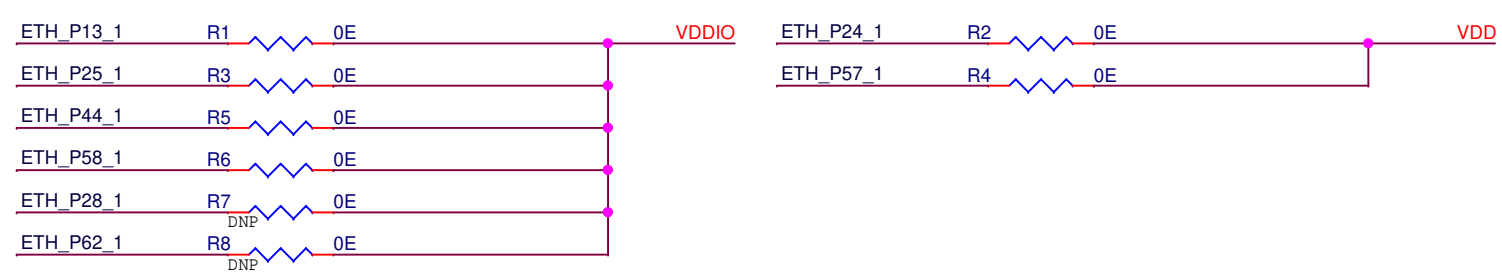
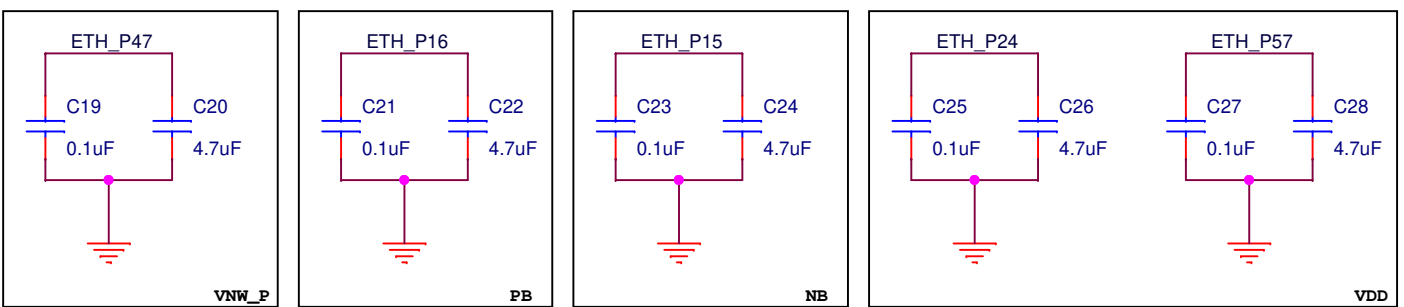
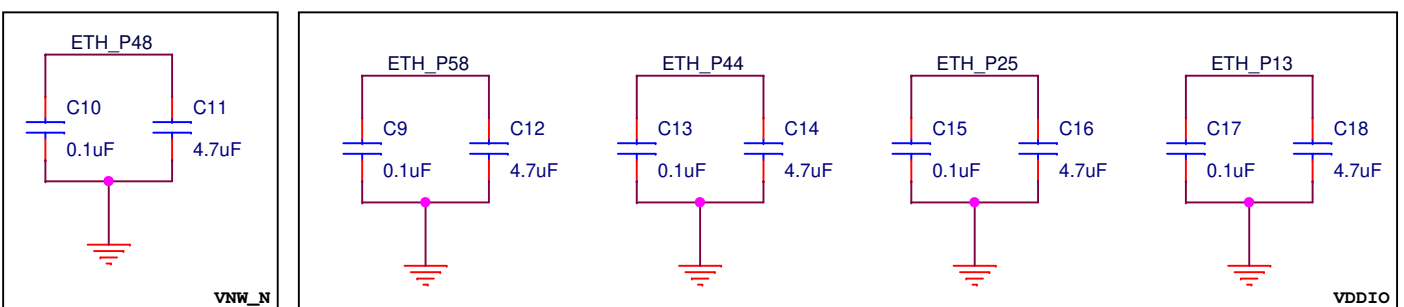
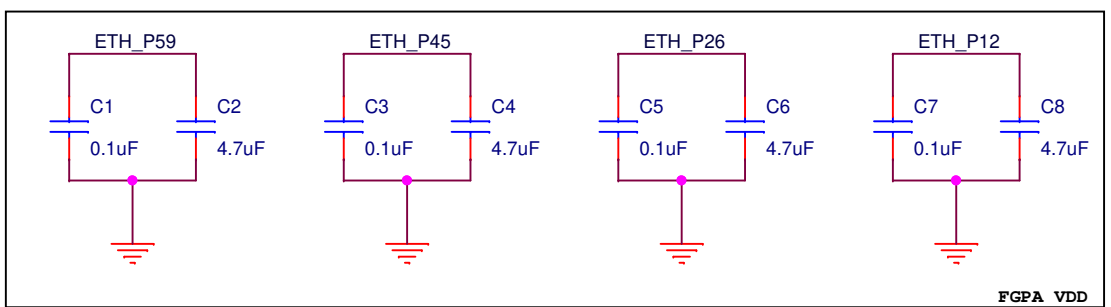
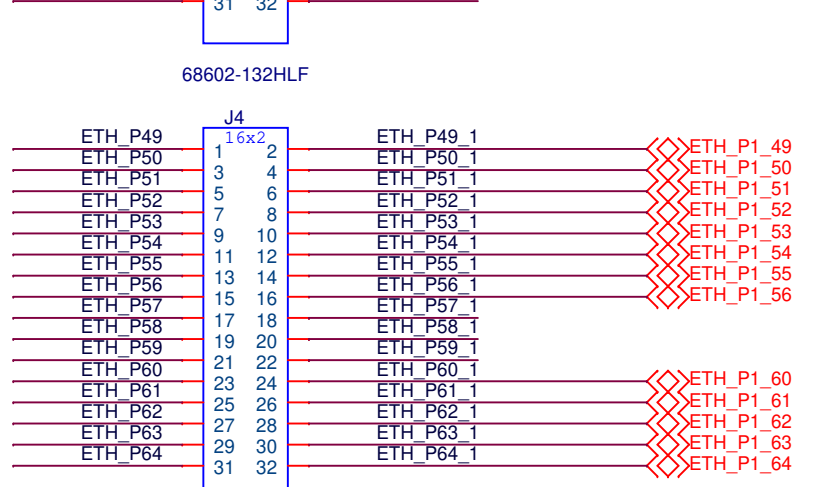
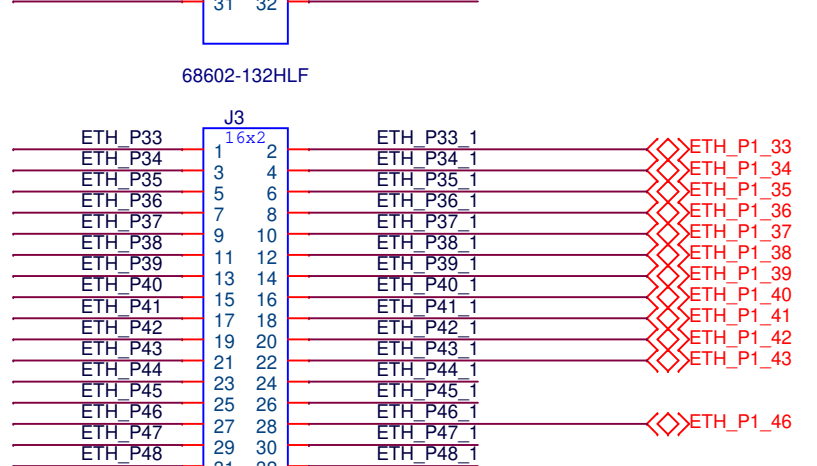
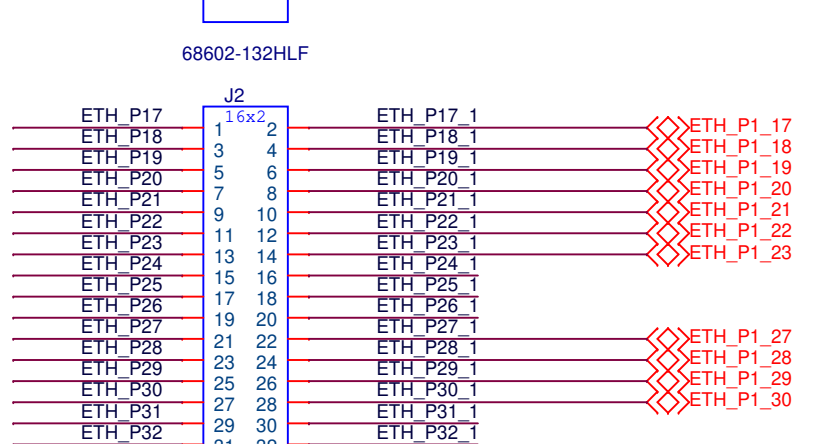
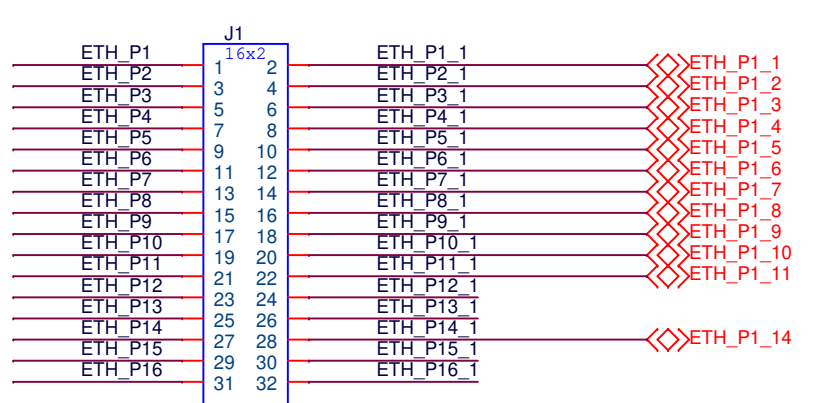
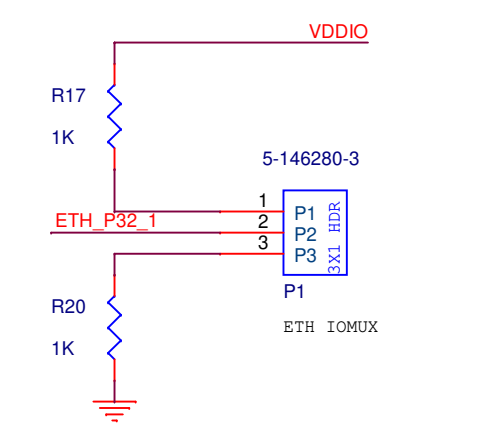
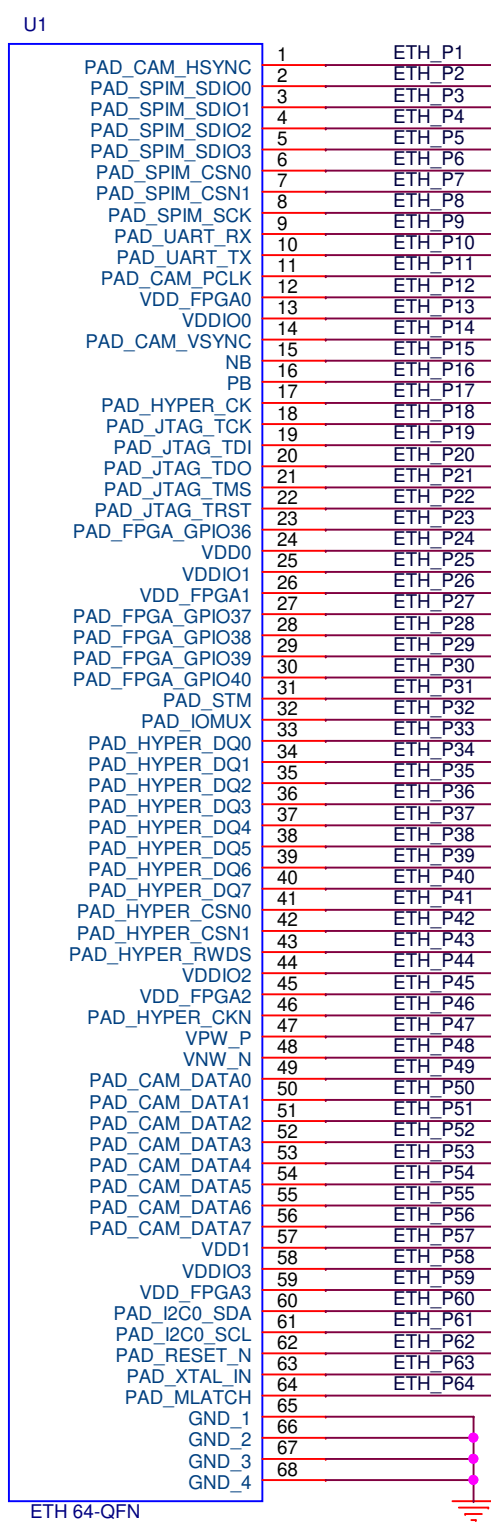


ETH IC Evaluation Board (Salinas)

Rev 1.1



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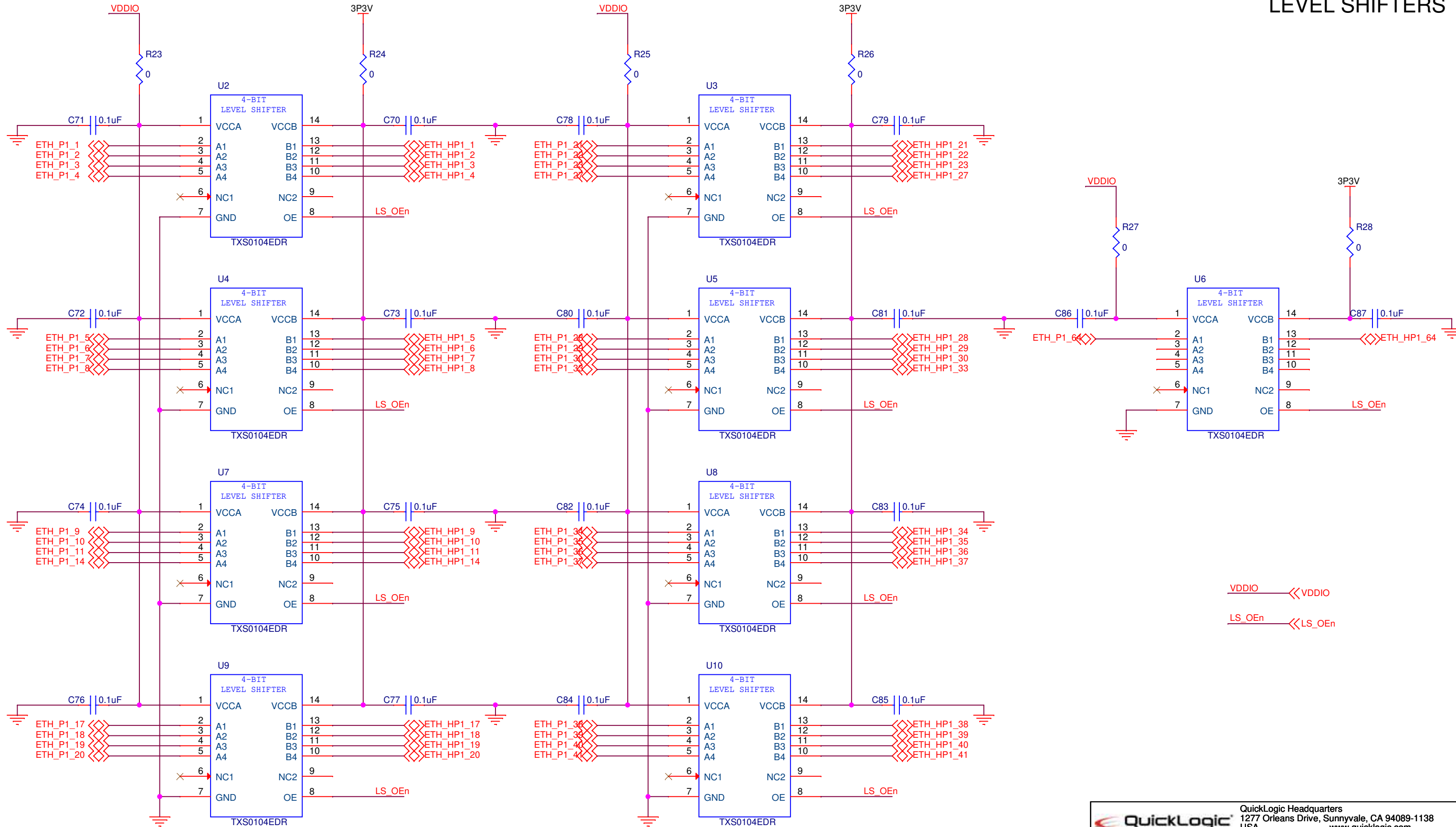
QuickLogic

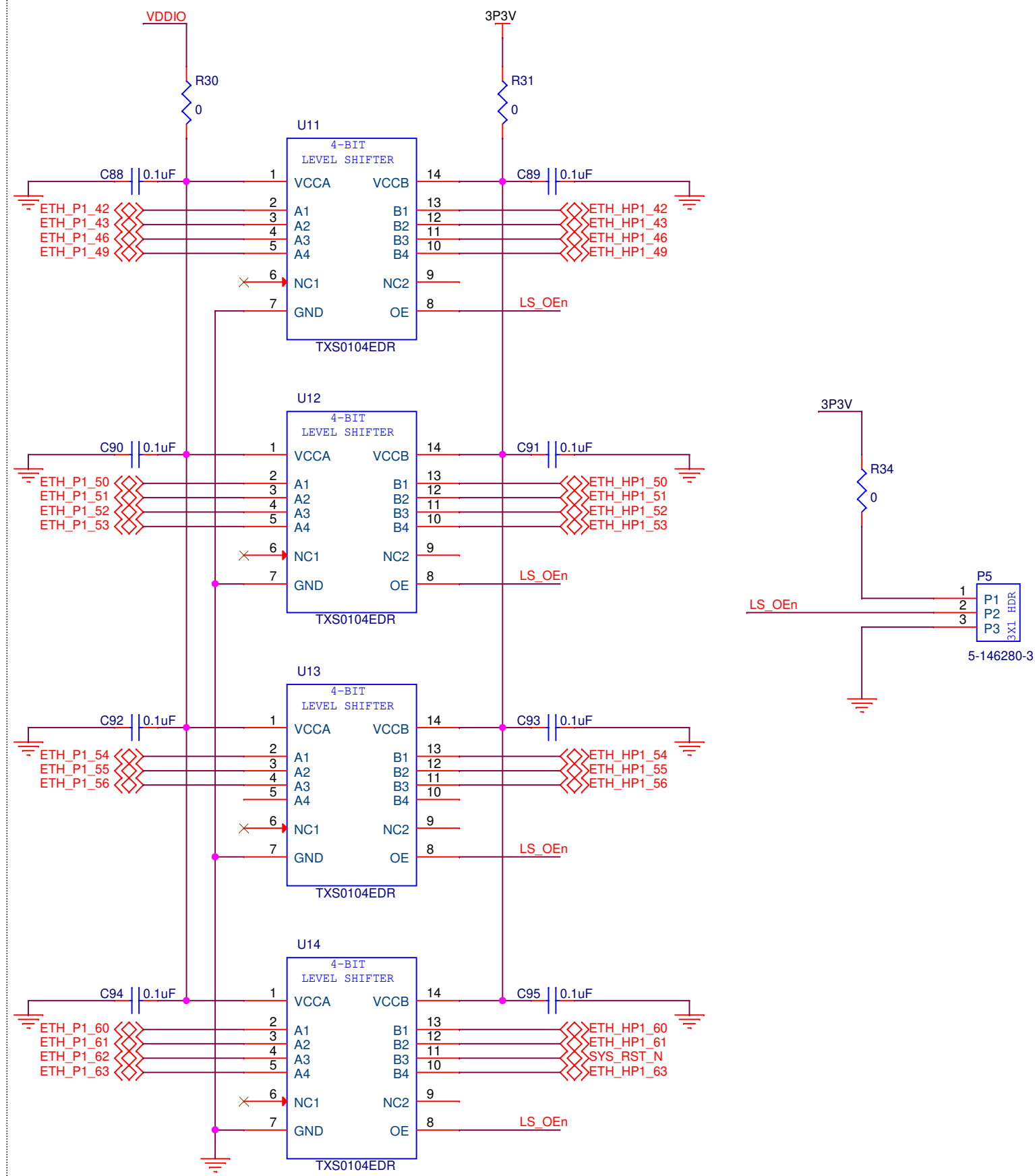
ETH IC Evaluation Board

Size Document Number Rev 1.1

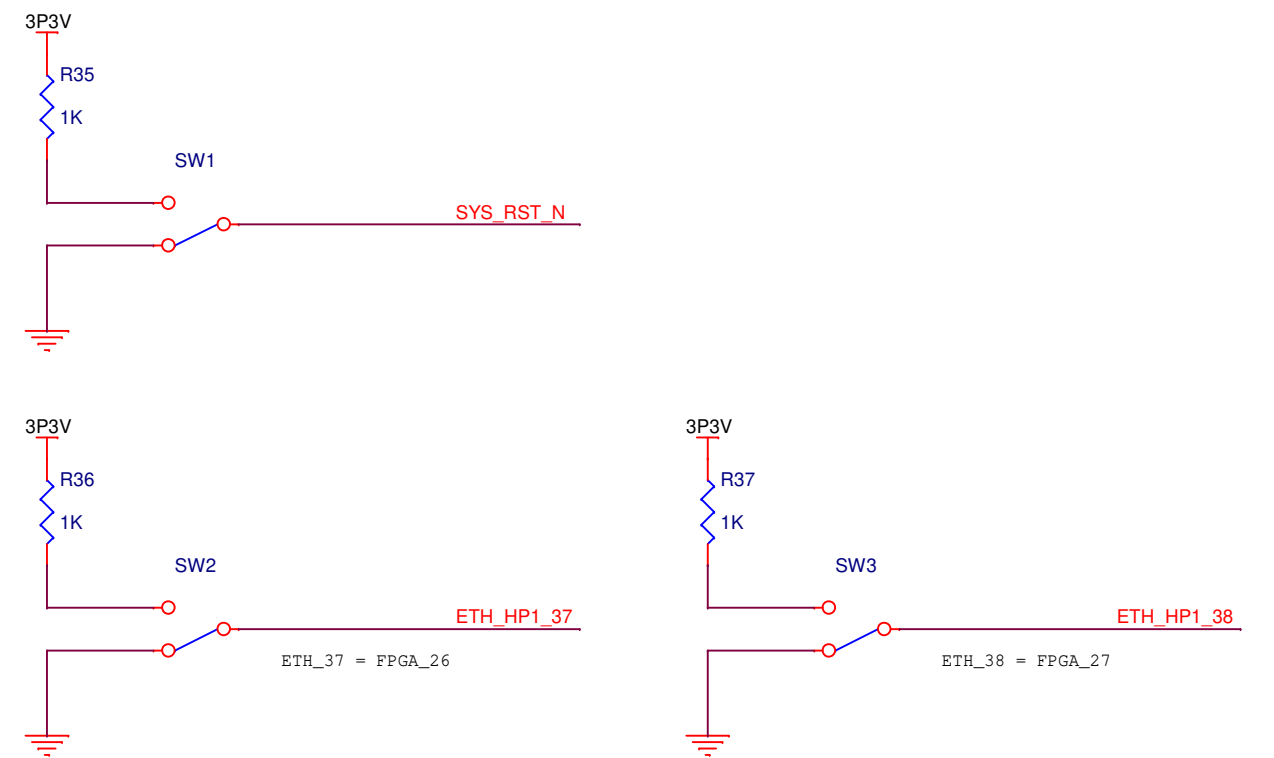
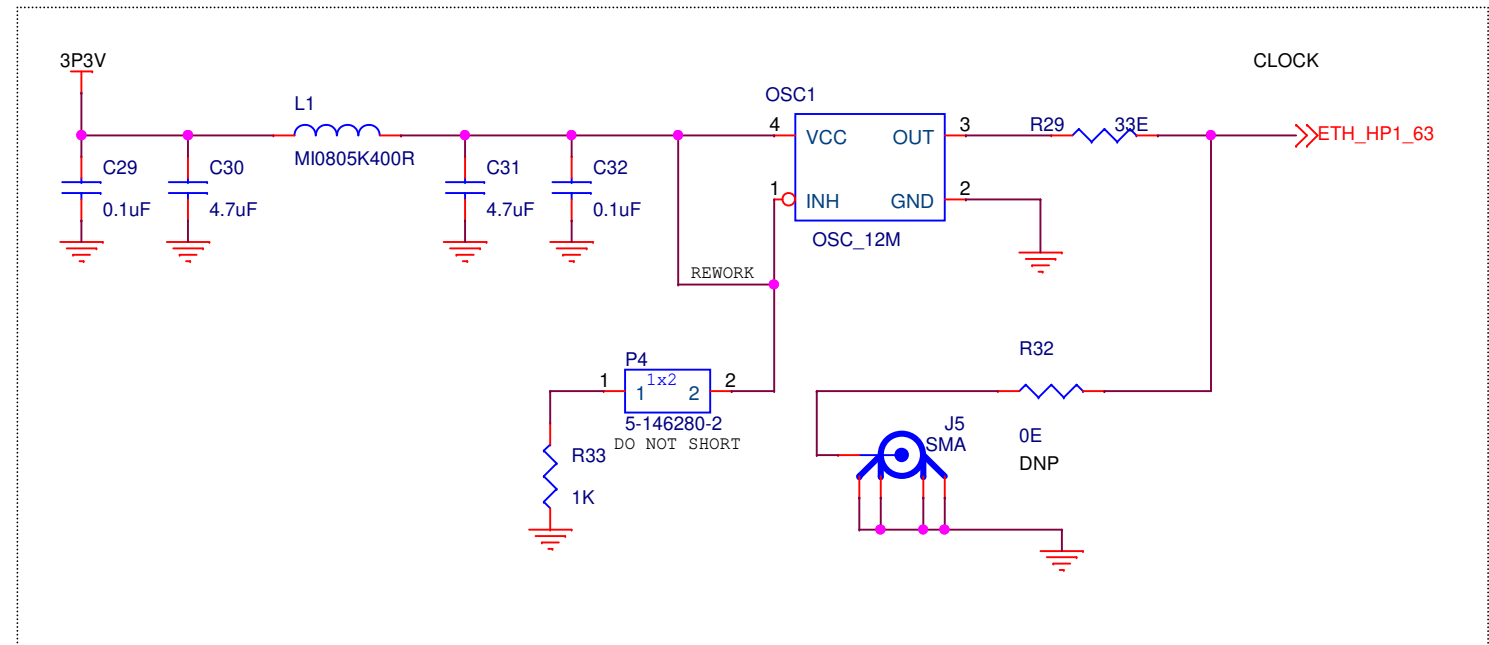
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LEVEL SHIFTERS





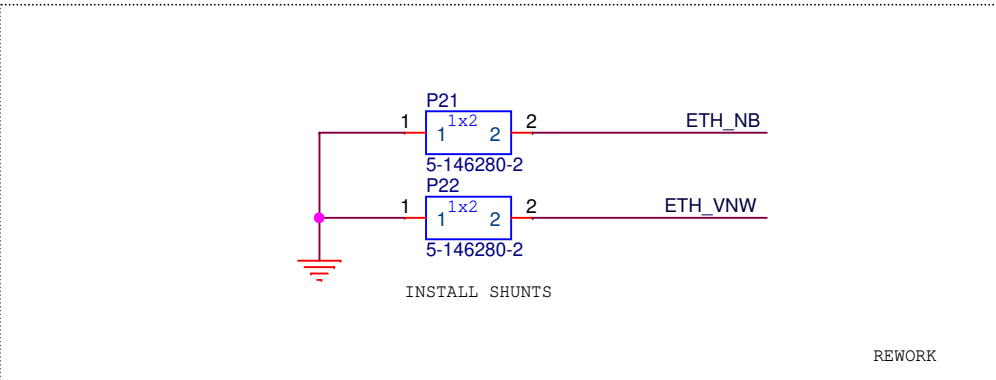
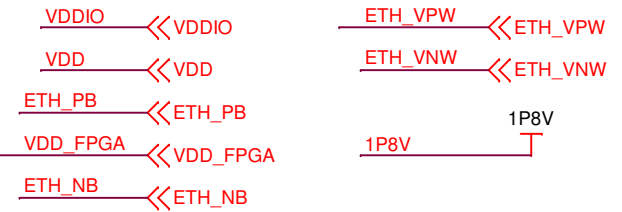
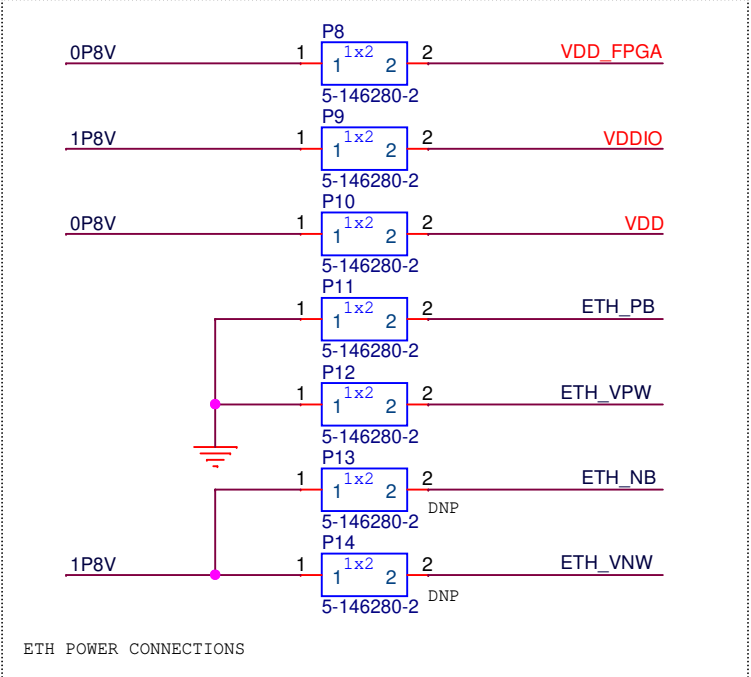
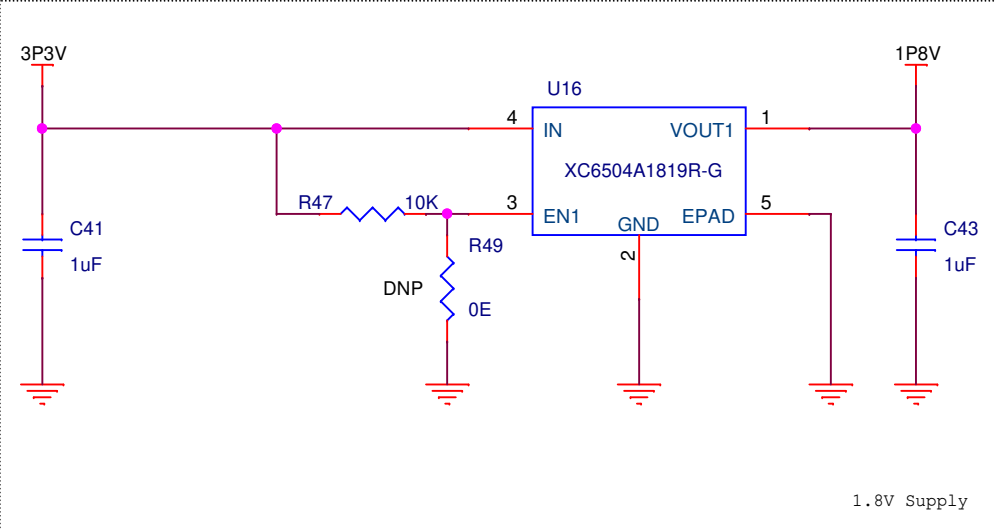
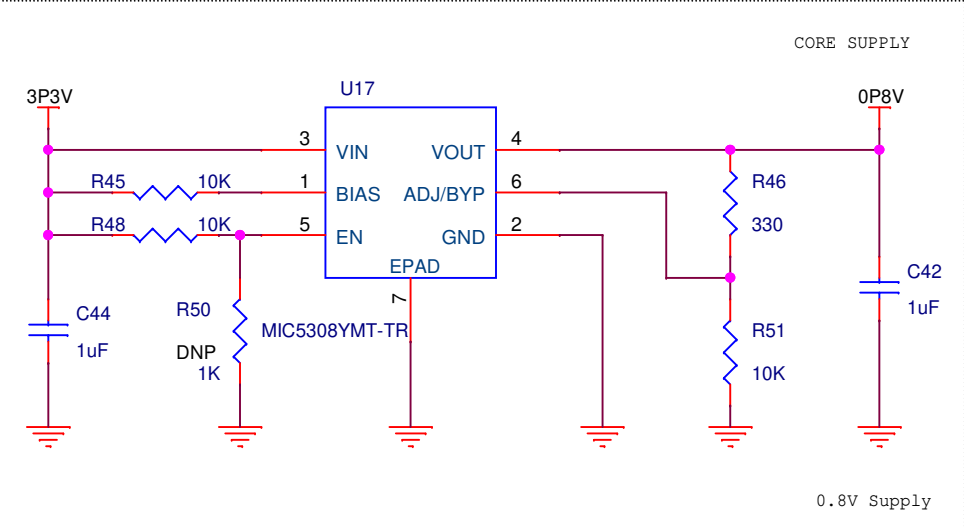
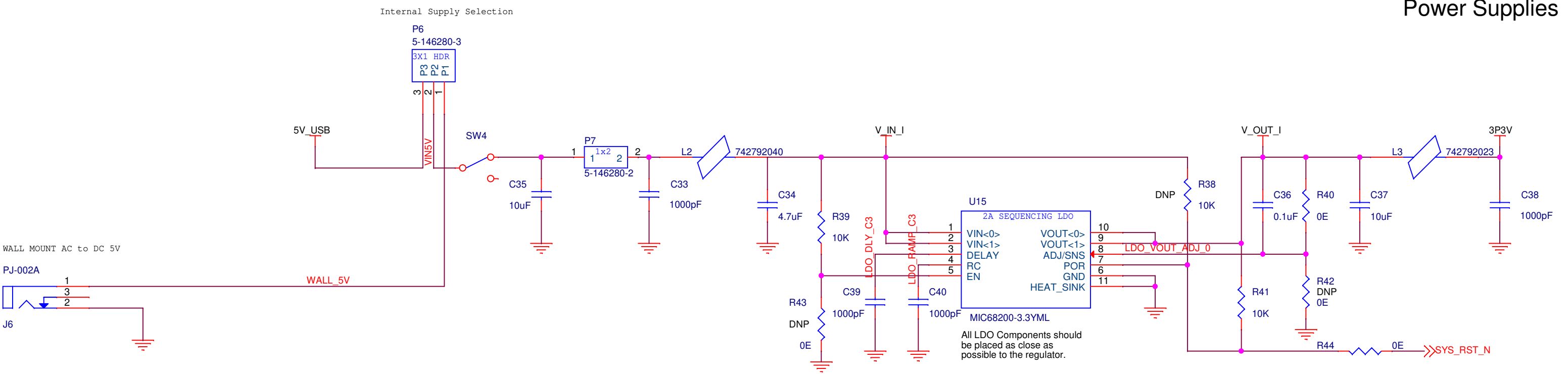
LEVEL SHIFTERS




ETH_HP1_38 << ETH_HP1_38
VDDIO << VDDIO
ETH_HP1_37 << ETH_HP1_37
LS_OEn << LS_OEn

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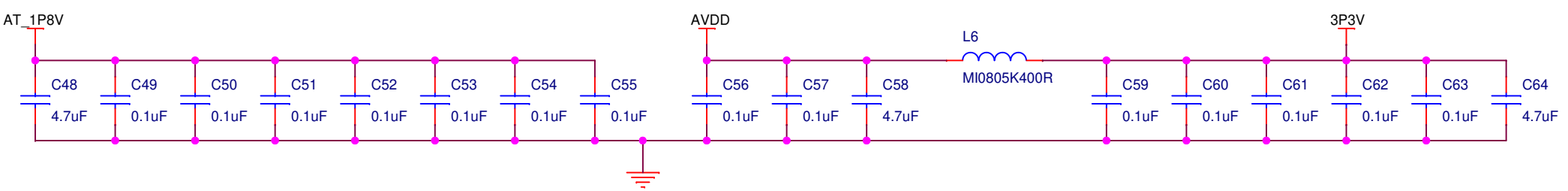
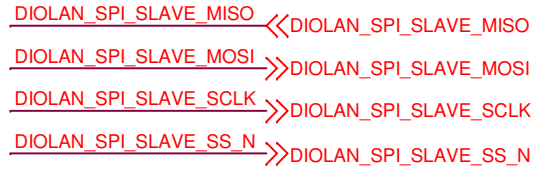
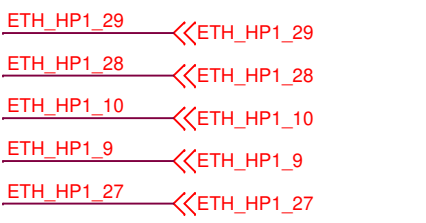
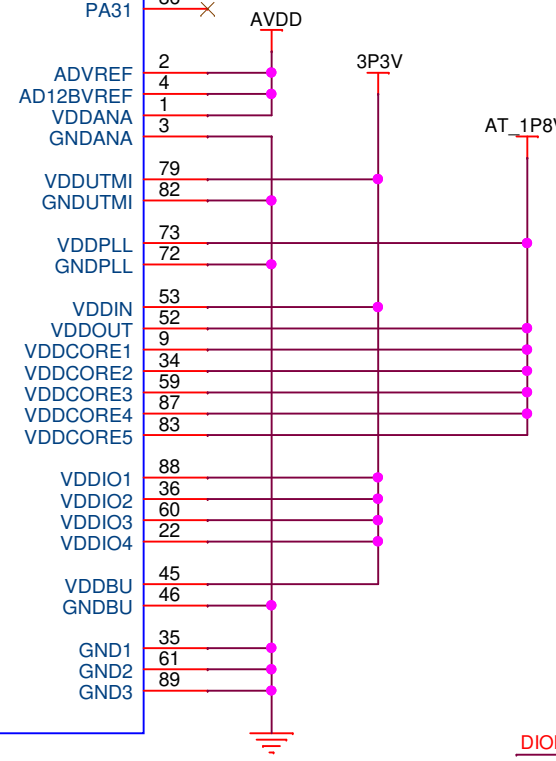
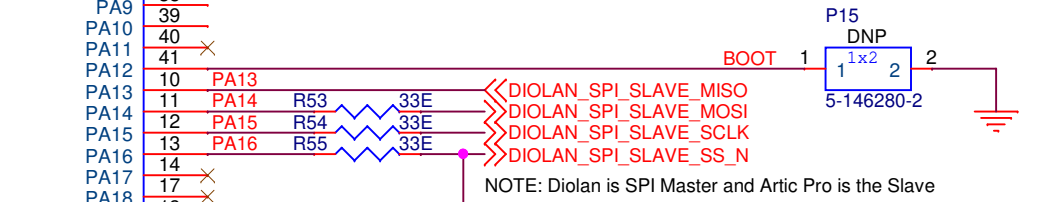
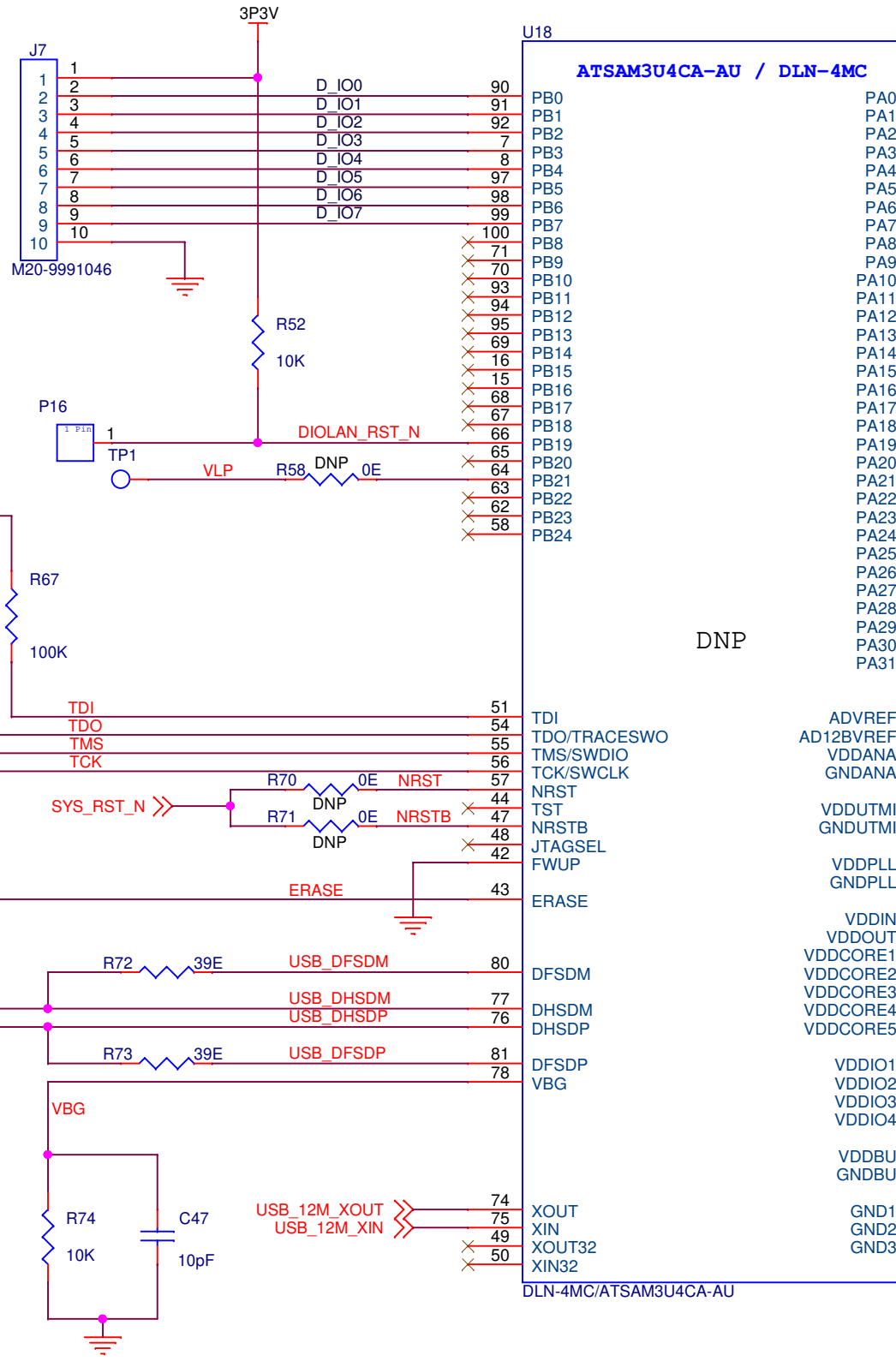
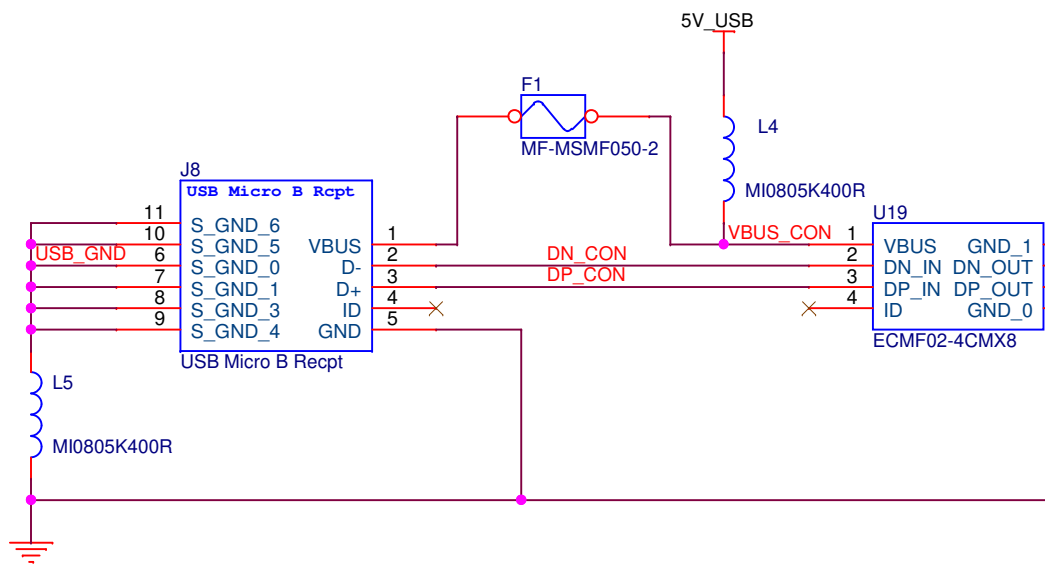
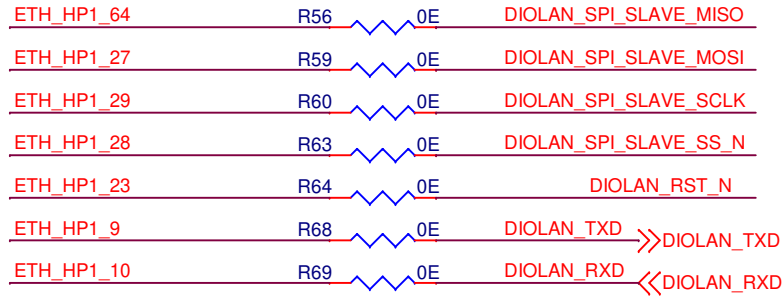
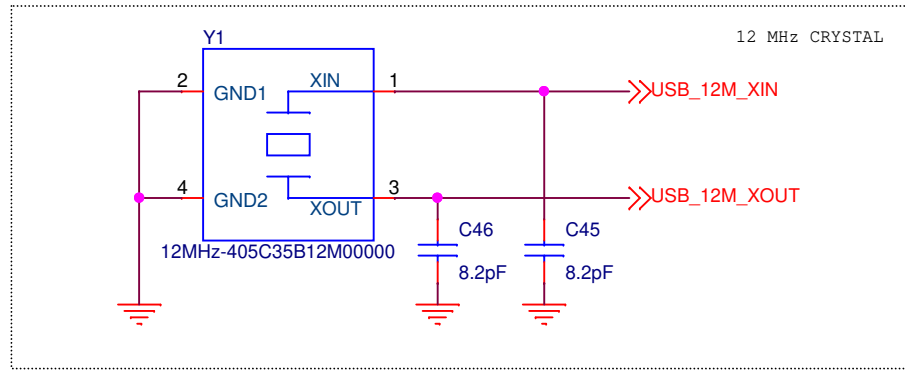
Power Supplies





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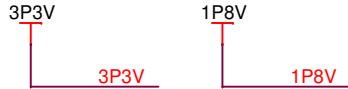
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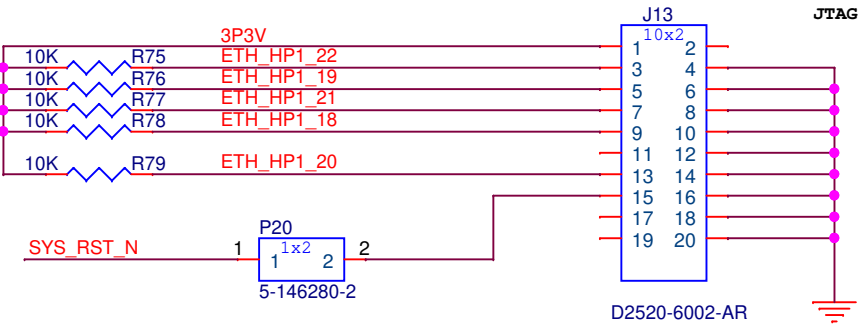
USB to SPI Converter (DIOLAN)

EXPANSION HEADERS

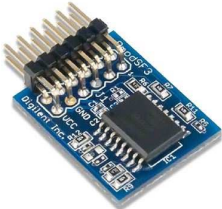
ETH_HP1_6 <> ETH_HP1_6
ETH_HP1_2 <> ETH_HP1_2
ETH_HP1_3 <> ETH_HP1_3
ETH_HP1_8 <> ETH_HP1_8
ETH_HP1_7 <> ETH_HP1_7
SYS_RST_N <> SYS_RST_N
ETH_HP1_4 <> ETH_HP1_4
ETH_HP1_5 <> ETH_HP1_5



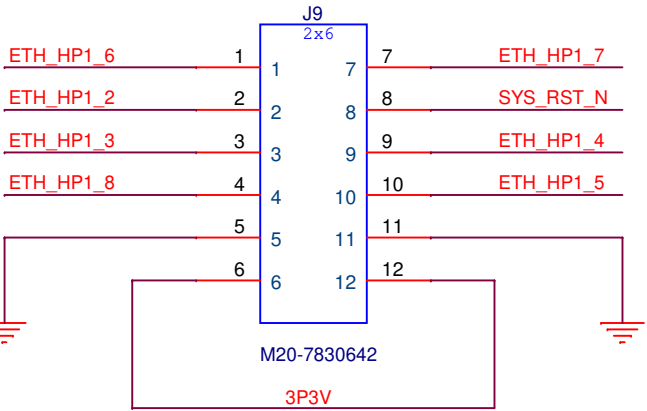
ETH_HP1_22 <> ETH_HP1_22
ETH_HP1_19 <> ETH_HP1_19
ETH_HP1_21 <> ETH_HP1_21
ETH_HP1_18 <> ETH_HP1_18
ETH_HP1_20 <> ETH_HP1_20



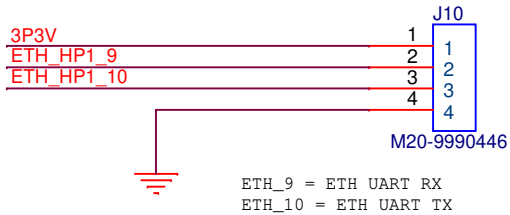
ETH_6 = PMOD CSn (1)
ETH_2 = PMOD SI/SDIO0 (2)
ETH_3 = PMOD SO/SDIO1 (3)
ETH_8 = PMOD SCK (4)
ETH_7 = PMOD NC/CSn1 (7)
SYS_RST_N = PMOD RSTn (8)
ETH_4 = PMOD WP/SDIO2 (9)
ETH_5 = PMOD HLC/SDIO3 (10)



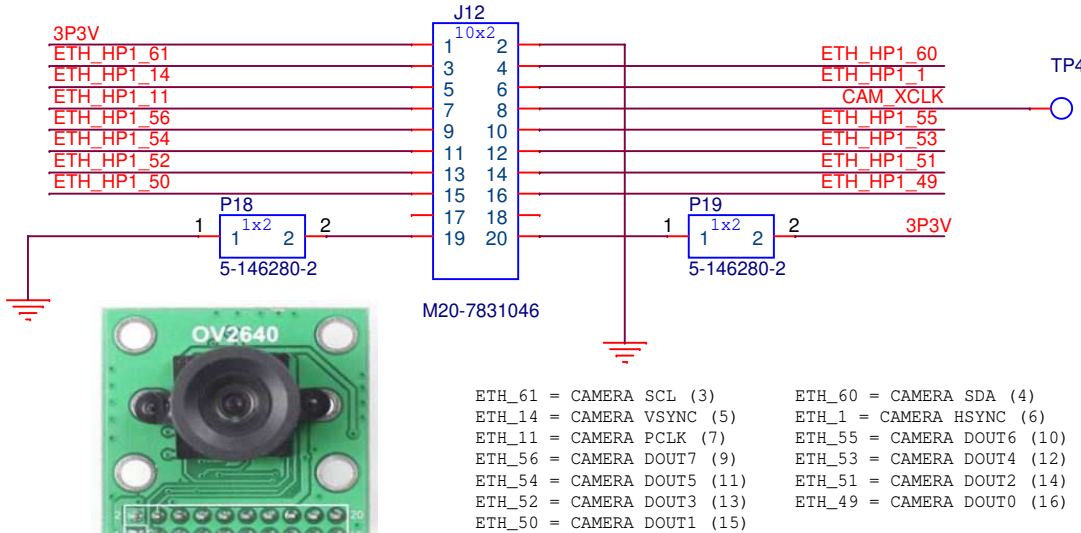
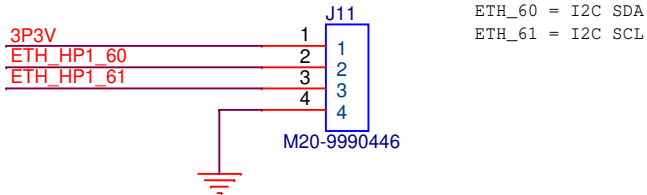
SPI MASTER (PMOD FLASH)



UART



I2C

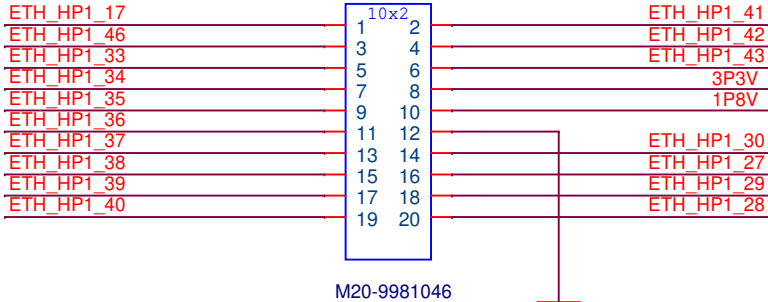


ETH_22 = JTAG RSTn (3)
ETH_19 = JTAG TDI (5)
ETH_21 = JTAG TMS (7)
ETH_18 = JTAG TCK (9)
ETH_20 = JTAG TDO (13)

VTref	1	2	NC
nTRST	3	4	GND
TDI	5	6	GND
TMS	7	8	GND
TCK	9	10	GND
RTCK	11	12	GND
TDO	13	14	GND*
RESET	15	16	GND*
DBGREQ	17	18	GND*
SV-Supply	19	20	GND*

ETH_HP1_17 <> ETH_HP1_17
ETH_HP1_46 <> ETH_HP1_46
ETH_HP1_33 <> ETH_HP1_33
ETH_HP1_34 <> ETH_HP1_34
ETH_HP1_35 <> ETH_HP1_35
ETH_HP1_36 <> ETH_HP1_36
ETH_HP1_37 <> ETH_HP1_37
ETH_HP1_38 <> ETH_HP1_38
ETH_HP1_39 <> ETH_HP1_39
ETH_HP1_40 <> ETH_HP1_40
ETH_HP1_41 <> ETH_HP1_41
ETH_HP1_42 <> ETH_HP1_42
ETH_HP1_43 <> ETH_HP1_43
ETH_HP1_30 <> ETH_HP1_30
ETH_HP1_27 <> ETH_HP1_27
ETH_HP1_29 <> ETH_HP1_29
ETH_HP1_28 <> ETH_HP1_28

ETH_17 = HYPER CK
ETH_46 = HYPER CKn
ETH_33 = HYPER DQ0
ETH_34 = HYPER DQ1
ETH_35 = HYPER DQ2
ETH_36 = HYPER DQ3
ETH_37 = HYPER DQ4
ETH_38 = HYPER DQ5
ETH_39 = HYPER DQ6
ETH_40 = HYPER DQ7




ETH_41 = HYPER CSn0
ETH_42 = HYPER CSn1
ETH_43 = HYPER RWDS

ETH_30 = SPIS MISO
ETH_27 = SPIS MOSI
ETH_29 = SPIS CLK
ETH_28 = SPIS SSn

ETH_HP1_9 <> ETH_HP1_9
ETH_HP1_10 <> ETH_HP1_10
ETH_HP1_14 <> ETH_HP1_14
ETH_HP1_11 <> ETH_HP1_11
ETH_HP1_56 <> ETH_HP1_56
ETH_HP1_54 <> ETH_HP1_54
ETH_HP1_52 <> ETH_HP1_52
ETH_HP1_50 <> ETH_HP1_50

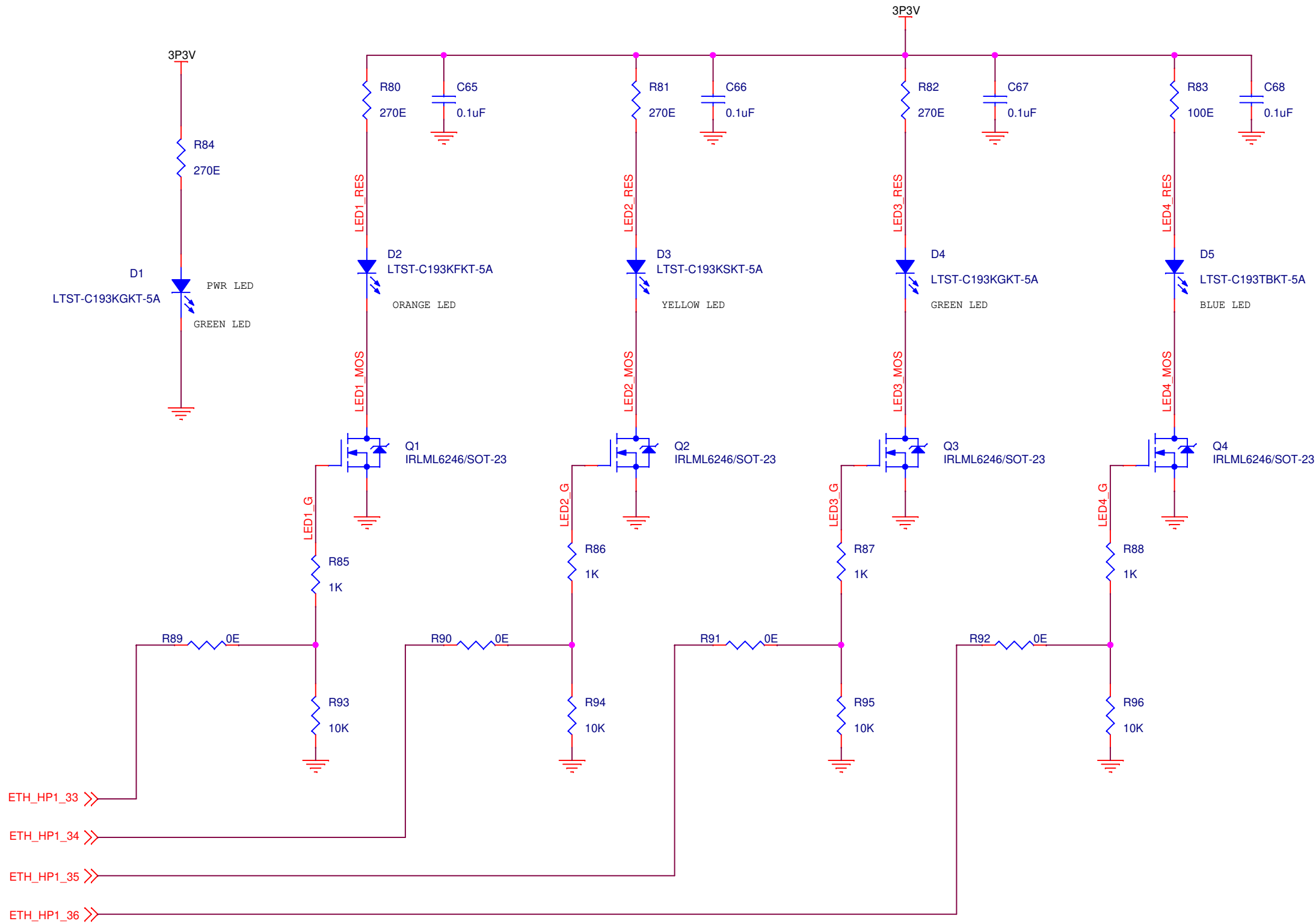
ETH_HP1_60 <> ETH_HP1_60
ETH_HP1_61 <> ETH_HP1_61
ETH_HP1_1 <> ETH_HP1_1
ETH_HP1_55 <> ETH_HP1_55
ETH_HP1_53 <> ETH_HP1_53
ETH_HP1_51 <> ETH_HP1_51
ETH_HP1_49 <> ETH_HP1_49



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LEDs



ETH_33 = FPGA_22
ETH_34 = FPGA_23
ETH_35 = FPGA_24
ETH_36 = FPGA_25

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