```
#include <stdio.h>
   #include <stdbool.h>
   // Constants for the cache configuration
   #define NUM_SETS /* Configure number of sets */
   #define ASSOC /* Configure cache associativity */
   #define BLOCK_SIZE 64 // 64 bytes as block size
   // Define the arrays for the cache simulation
   long long int tag_array[NUM_SETS][ASSOC];
10
   long long int lru_position[NUM_SETS][ASSOC];
11
   bool dirty[NUM_SETS][ASSOC];
12
13
   // Variables to maintain the simulation statistics
14
15
   int Hit = 0;
   int Miss = 0;
16
17
   // Forward declarations
18
   void Update_lru(long long int add);
19
   void Update_fifo(long long int add);
20
21
   // Function to simulate cache access
22
   void Simulate_access(char op, long long int add) {
23
       int set = (add / BLOCK_SIZE) % NUM_SETS;
24
       long long int tag = add / BLOCK_SIZE;
25
26
       for(int i = 0; i < ASSOC; i++) {</pre>
27
            if(tag == tag_array[set][i]) {
                // Cache hit scenario
29
                Hit++;
30
                // Choose policy (LRU or FIFO) based on the configuration
31
                if(/* LRU policy is chosen */) {
32
33
                    Update_lru(add);
                } else {
34
                    Update_fifo(add);
35
                }
36
            } else {
37
                // Cache miss scenario
38
                Miss++;
39
                // Handle the miss scenario here
40
           }
41
       }
42
43
44
   // Update functions for different policies
45
   void Update_lru(long long int add) {
46
       // Logic for updating LRU policy
47
48
49
   void Update_fifo(long long int add) {
50
       // Logic for updating FIFO policy
51
   }
52
53
   int main() {
54
       char op;
55
       long long int add;
56
       FILE *file = fopen(/* Path to the trace file */, "r");
57
58
       // Check if the file opened successfully
59
       if(!file) {
```

```
printf("Error: Could not open the trace file.\n");
61
            return 1;
62
63
65
       // Read until end of file
       while(!feof(file)) {
66
            \ensuremath{//} Read operation and address
67
            fscanf(file, " %c %llx", &op, &add);
68
69
            // Begin the simulation for each address read
70
            Simulate_access(op, add);
71
       }
72
73
       // Print out the statistics
74
       printf("Hits: %d\n", Hit);
75
       printf("Misses: %d\n", Miss);
76
77
78
       return 0;
   }
79
```