

# EEL 4742 – Embedded Systems

## Module 4 – Finite State Machines in Embedded Systems - Interrupts

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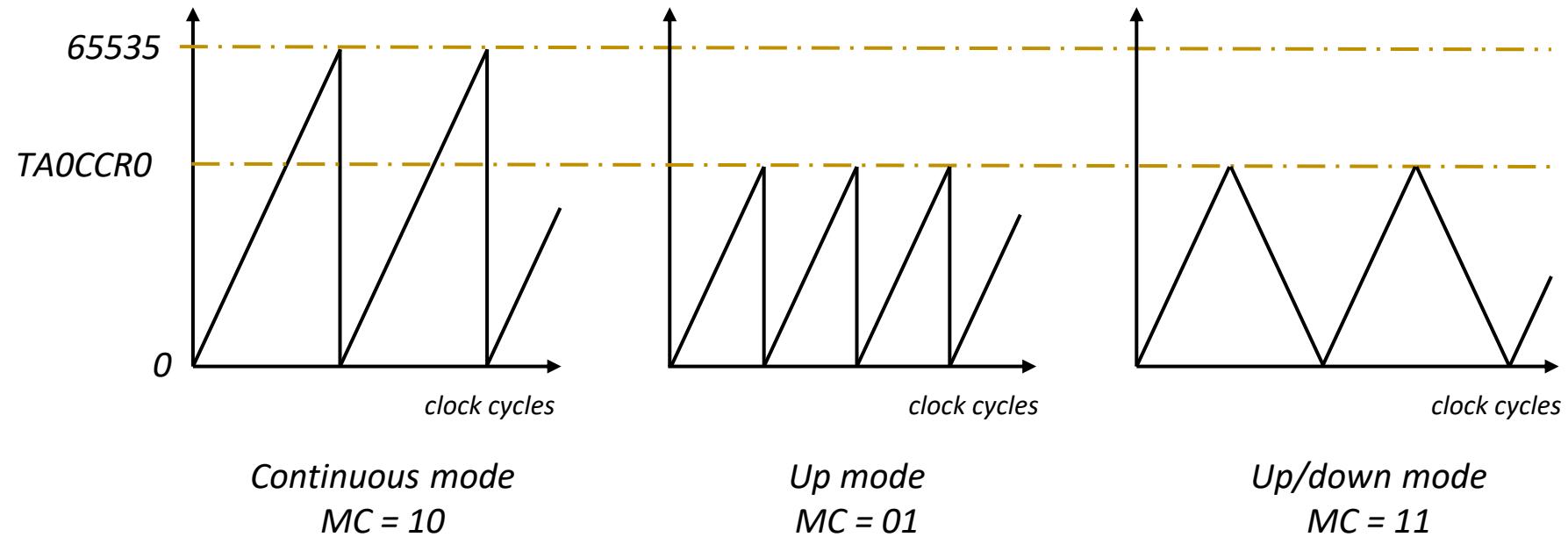


# A Quick Recap

- Timer Mode Control
  - Controlled using MC (2 bits)

MC	Mode	Description
00	Stop	The timer is halted.
01	Up	The timer repeatedly counts from zero to the value of TA0CCR0
10	Continuous	The timer repeatedly counts from zero to OFFFFh (cycling with no stop)
11	Up/down	The timer repeatedly counts from zero up to the value of TA0CCR0 and back down to zero

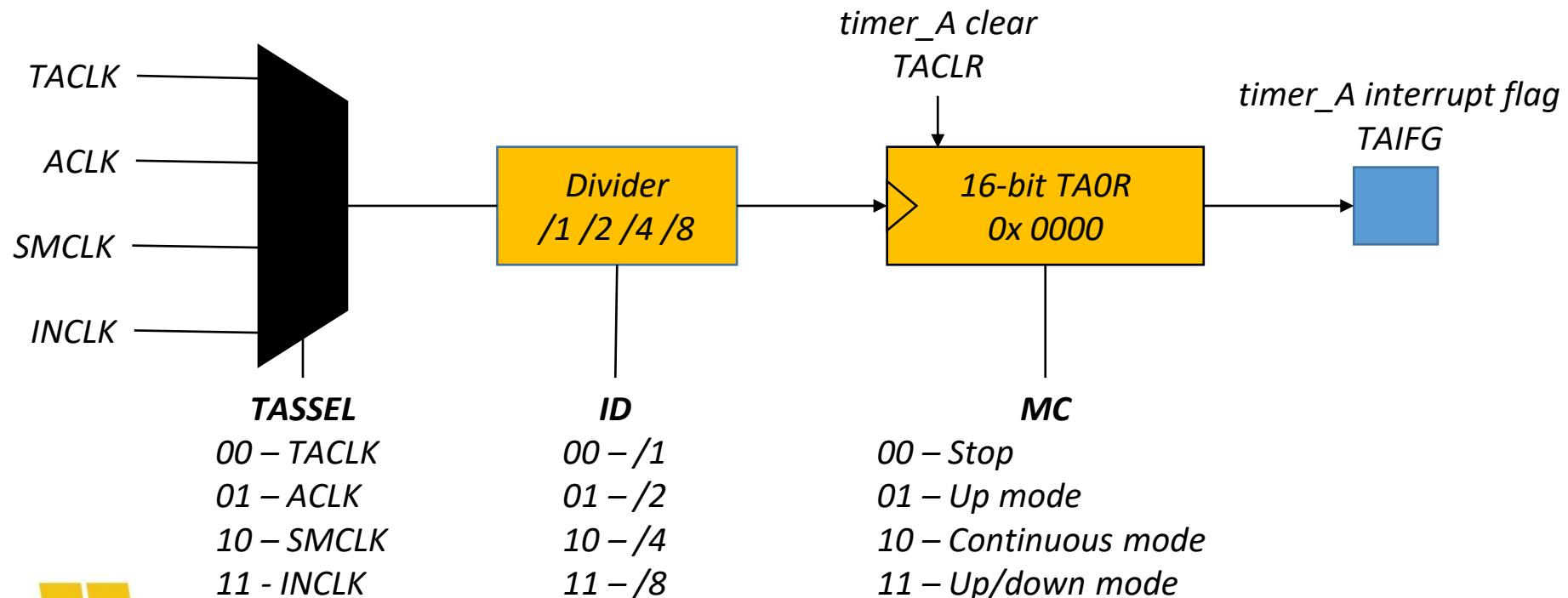
*TA0CCR0 is a 16-bit register. TA0CCR0 stands for timer\_A 0 capture/compare register for channel 0.*



# A Quick Recap

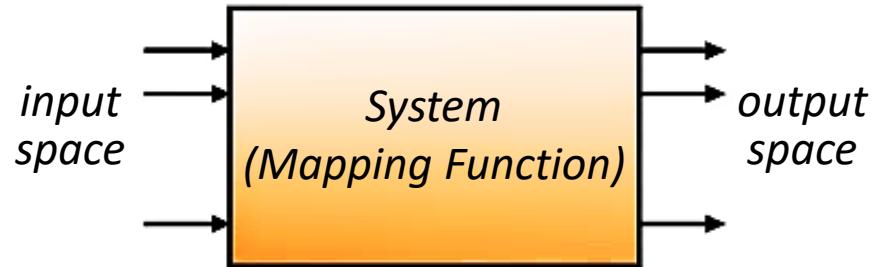
- Timer Mode Control
  - Controlled using MC (2 bits)

<i>TAOCTL</i>	rsvd.						TASSEL	ID	MC	rsvd.	TACLR	TAIE	TAIFG
	15	14	13	12	11	10	9	8	7	6	5	4	3



# Finite State Machines

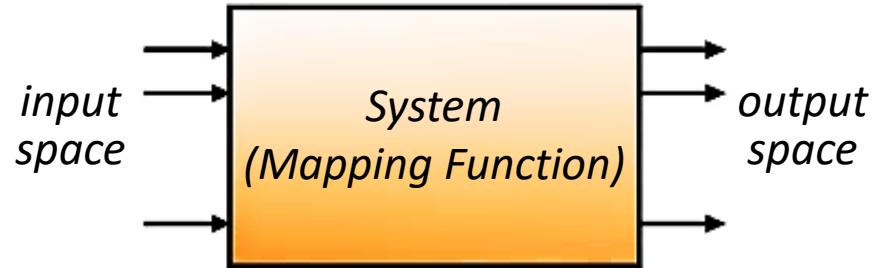
- Is output only dependent to input?



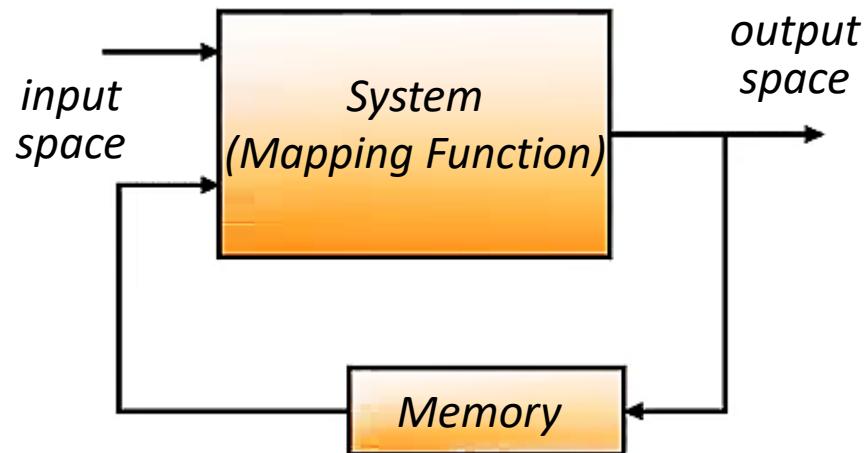
*A system is a mapping of a set of inputs into a set of outputs??*

# Finite State Machines

- Is output only dependent to input?



*A system is a mapping of a set of inputs into a set of outputs??*



*A system is a mapping of a set of inputs into a set of outputs **with respect to the status of the system!***



# Do we always have states?

- It is Flashing LED!

The red LED is mapped to Port 1 Bit 0!

The green LED is mapped to Port 1 Bit 7!

- Let's recall this example!

- Do we have any specific states for the system here?

```
BIT0=00000001 // Code that flashes the red LED
#include <msp430fr6989.h>
#define redLED BIT0 // Red LED at P1.0
void main(void)
{
    volatile unsigned int i;
    // initialization (reset watchdog, GPIO high-z, etc.
    P1DIR |= redLED; // Direct pin as output
    P1OUT &= ~redLED; // Turn LED Off
    for(;;) {
        // Delay loop
        for(i=0; i<20000; i++) {}
        P1OUT ^= redLED; // Toggle the LED
    }
}
```



# Do we always have states?

- It is Flashing LED!

*The red LED is mapped to Port 1 Bit 0!*

*The green LED is mapped to Port 1 Bit 7!*

- Let's recall this example!

- Do we have any specific states for the system here?

*Basically YES! The current Status of LED (ON or OFF)*

- Do we need it to know for coding?

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```



# Do we always have states?

- It is Flashing LED!

The red LED is mapped to Port 1 Bit 0!

**BIT0=00000001** // Code that flashes the red LED

The green LED is mapped to Port 1 Bit 7!

**BIT7=10000000** #include <msp430fr6989.h>

#define redLED BIT0 // Red LED at P1.0

void main(void)

{

volatile unsigned int i;

// initialization (reset watchdog, GPIO high-z, etc.

P1DIR |= redLED; // Direct pin as output

P1OUT &= ~redLED; // Turn LED Off

for(;;) {

// Delay loop

for(i=0; i<20000; i++) {}

P1OUT ^= redLED; // Toggle the LED

}

- Let's recall this example!

- Do we have any specific states for the system here?

**Basically YES!** The current Status of LED (ON or OFF)

- Do we need it to know for coding?

We can make it required (based on the coding style).

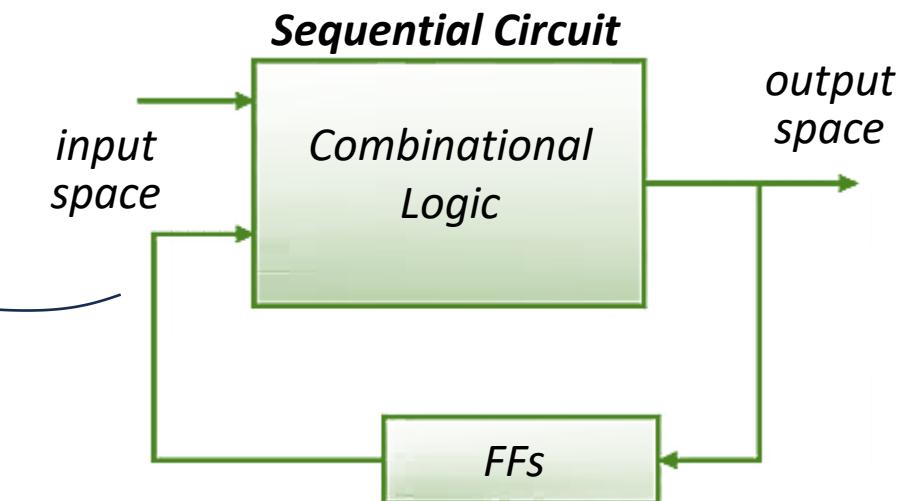
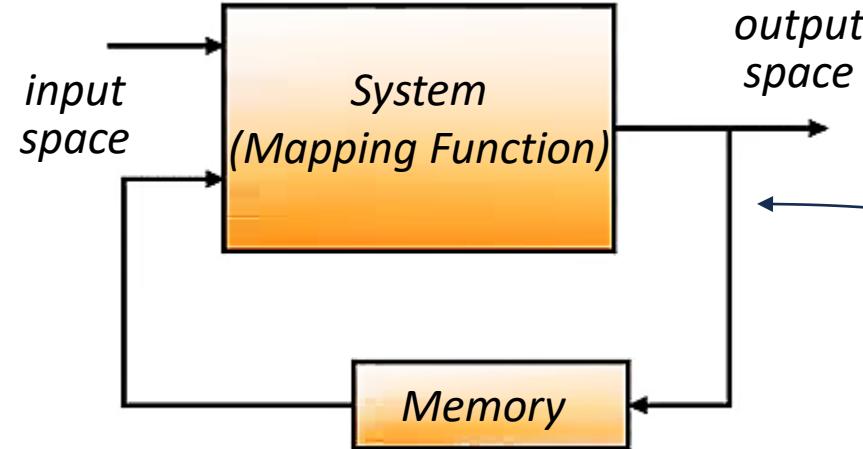
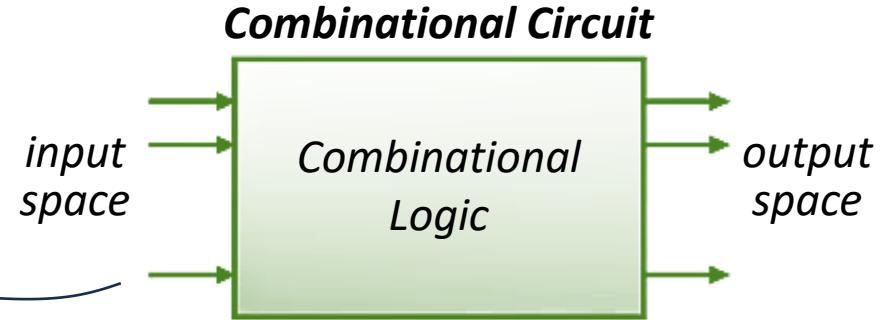
**But generally, NO!**

Toggling (regardless its value)

}

# Inherited from Hardware Abstraction

- Combinational Circuits vs. Sequential Circuits

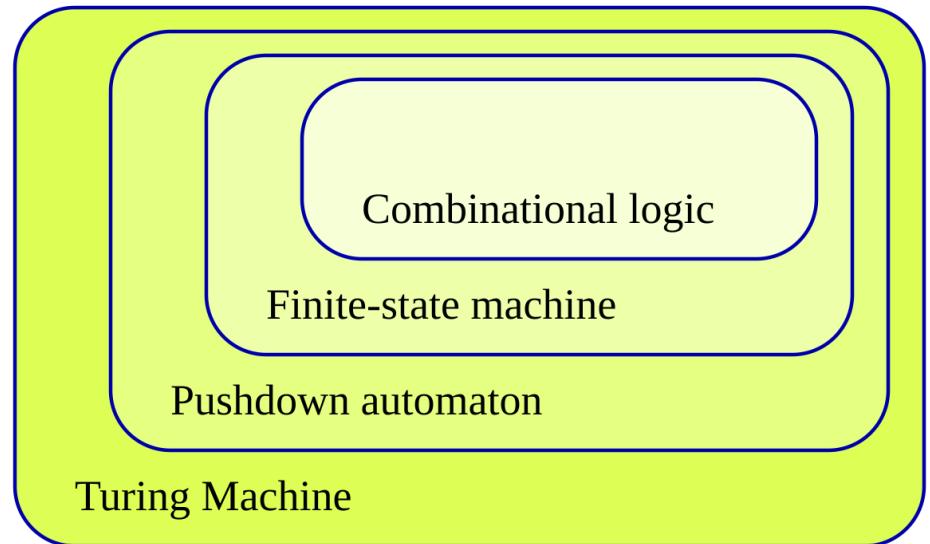


# Hierarchy of Systems in relation to States

- Combinational logic
- Sequential logic (using finite state machines)
- Pushdown automaton
- Turing Machines

More limited (for simple problems)  
More generic (for complex problems)

Automata theory

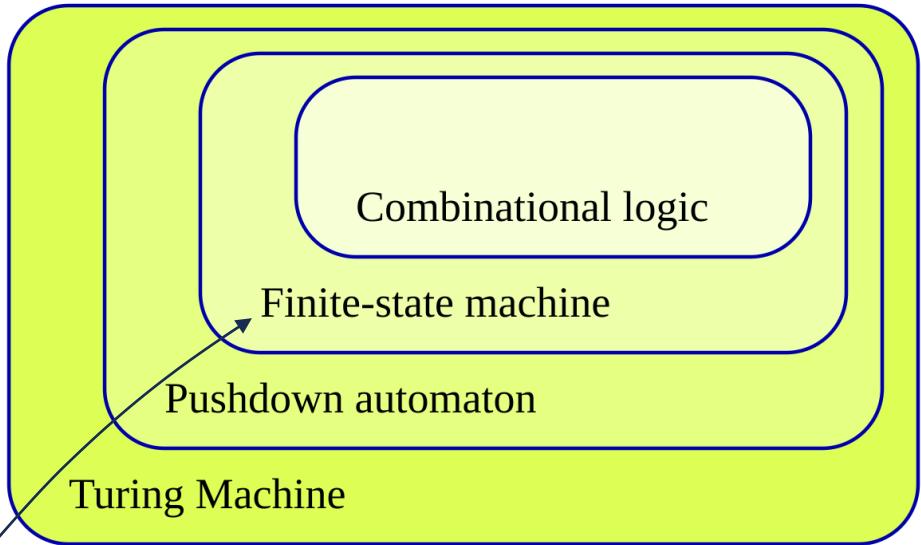


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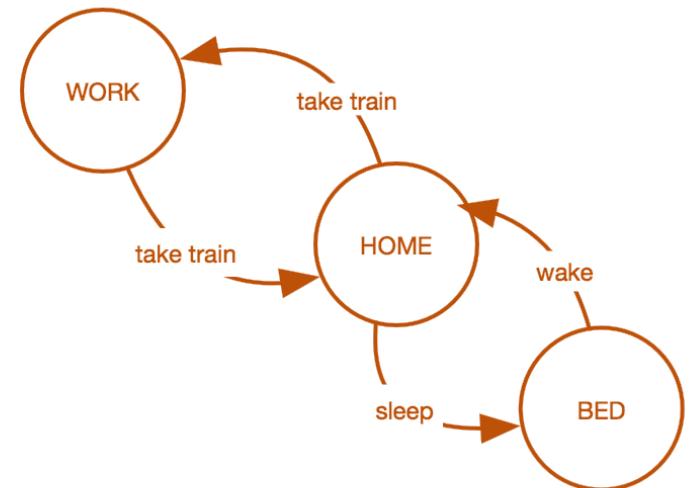
Automata theory



*Right choice for embedded systems  
(not too simple and not too complex)*

# Finite State Machine (FSM)

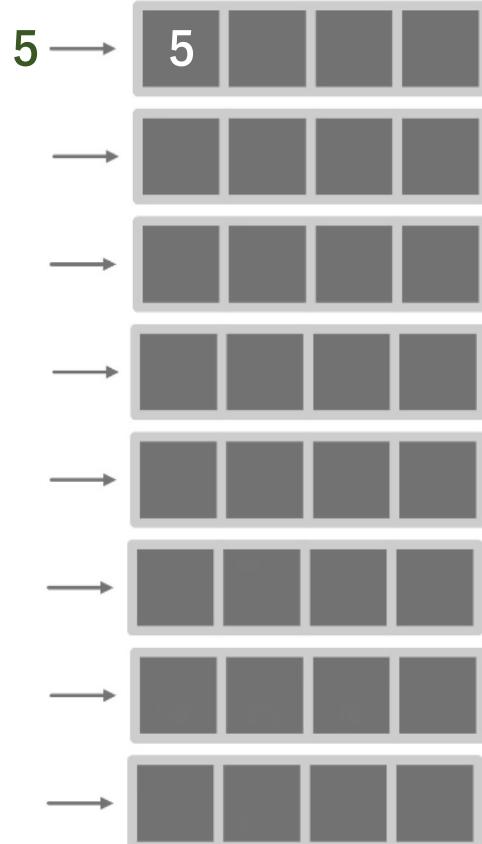
- Finite state machine (FSM) or finite state automata (FSA)
  - An abstract machine that can only be in one of several finite states at any given time
    - You can't sleep and work at the same time...
  - It is finite: fixed number of states (finite space)
    - $\{S_0, \dots, S_n\}$  & One state is initial state (e.g.,  $S_0$ )
    - Each state → a specific status
  - A finite number of inputs/outputs to the system
    - $\{I_0, \dots, I_m\}$  for inputs and  $\{O_1, \dots, O_n\}$  for outputs
  - A transition function  $T_S(\text{current state}, I_x, \dots, I_z) = \text{new state}$ 
    - $I_x, \dots, I_z$  is the sequence of inputs
  - An output function  $F_O(\text{current state}, I_x, \dots, I_z) = O_x, \dots, O_z$





# An FSM Example: Passcode Check

- Passcode lock: Correct passcode is **5202**

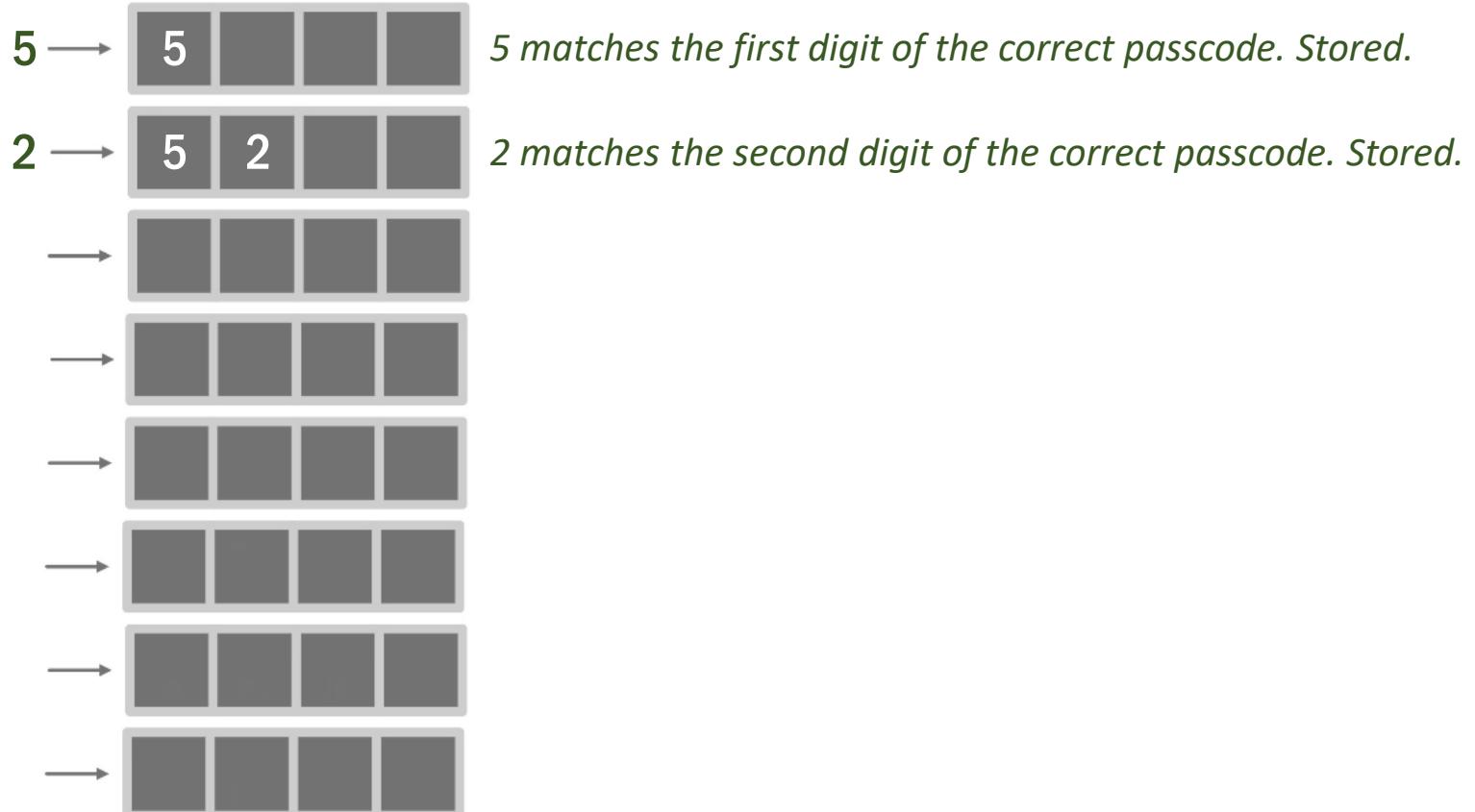


*5 matches the first digit of the correct passcode. Stored.*



# An FSM Example: Passcode Check

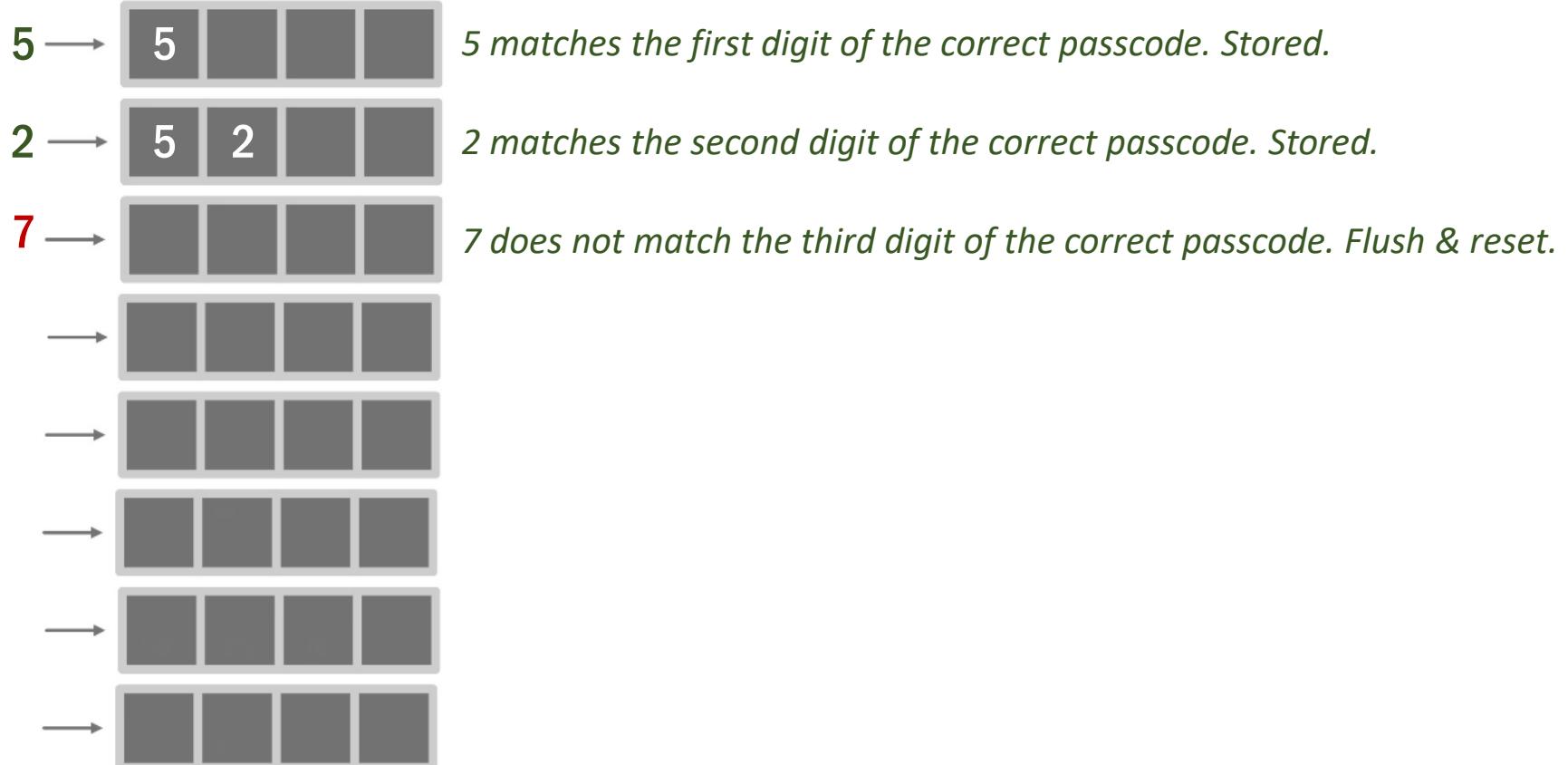
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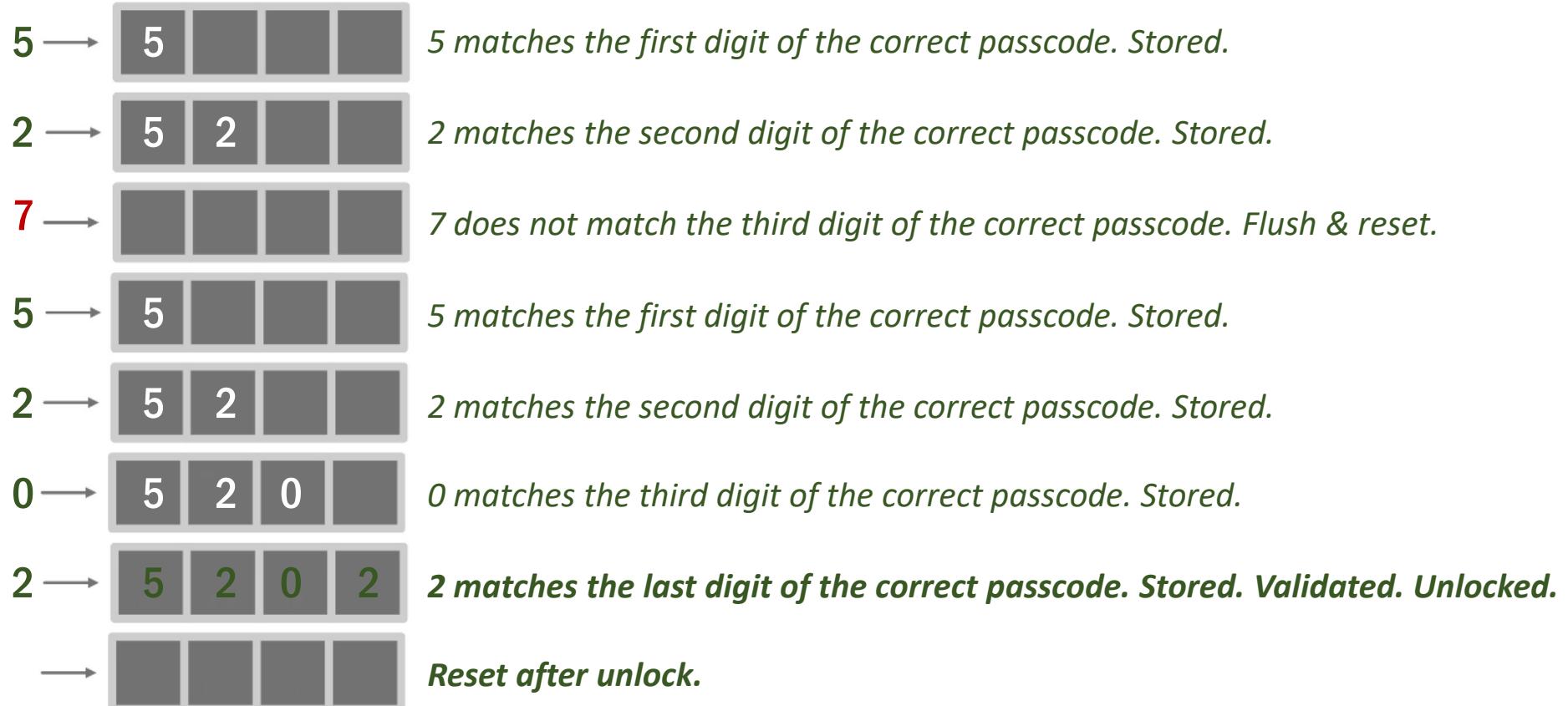
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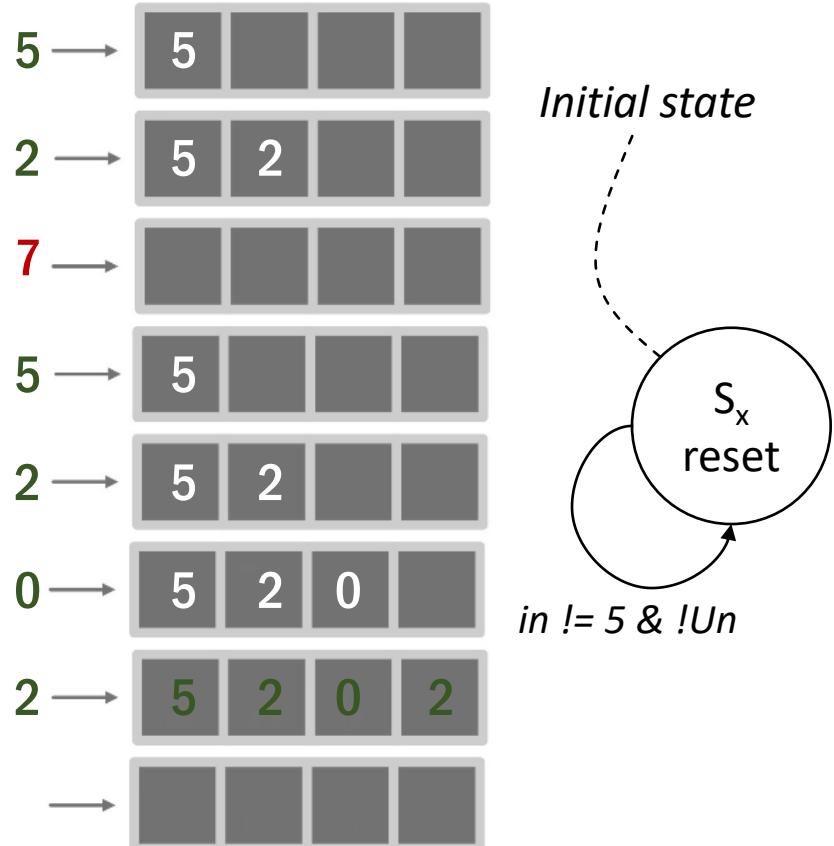
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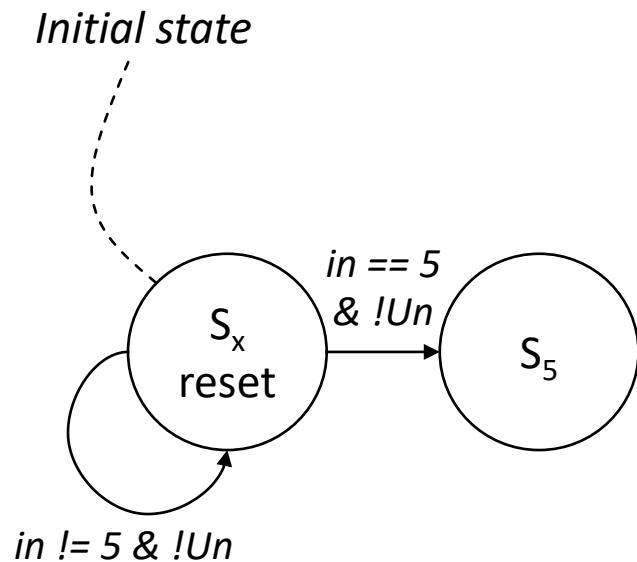
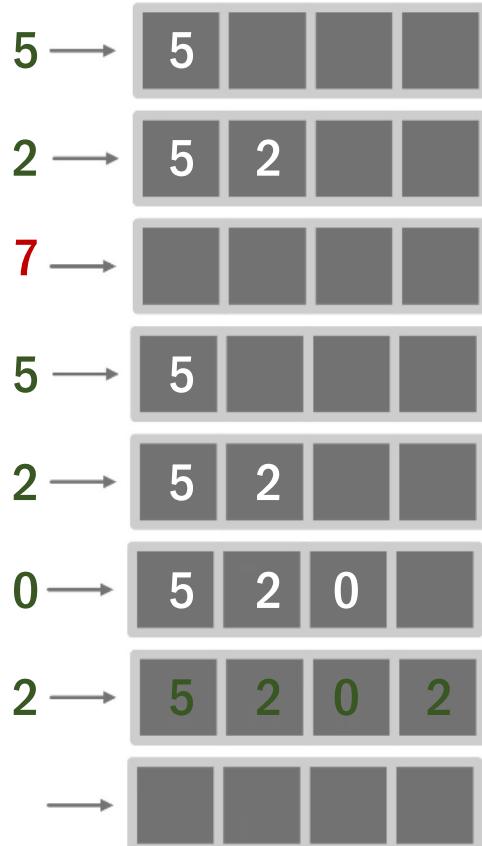
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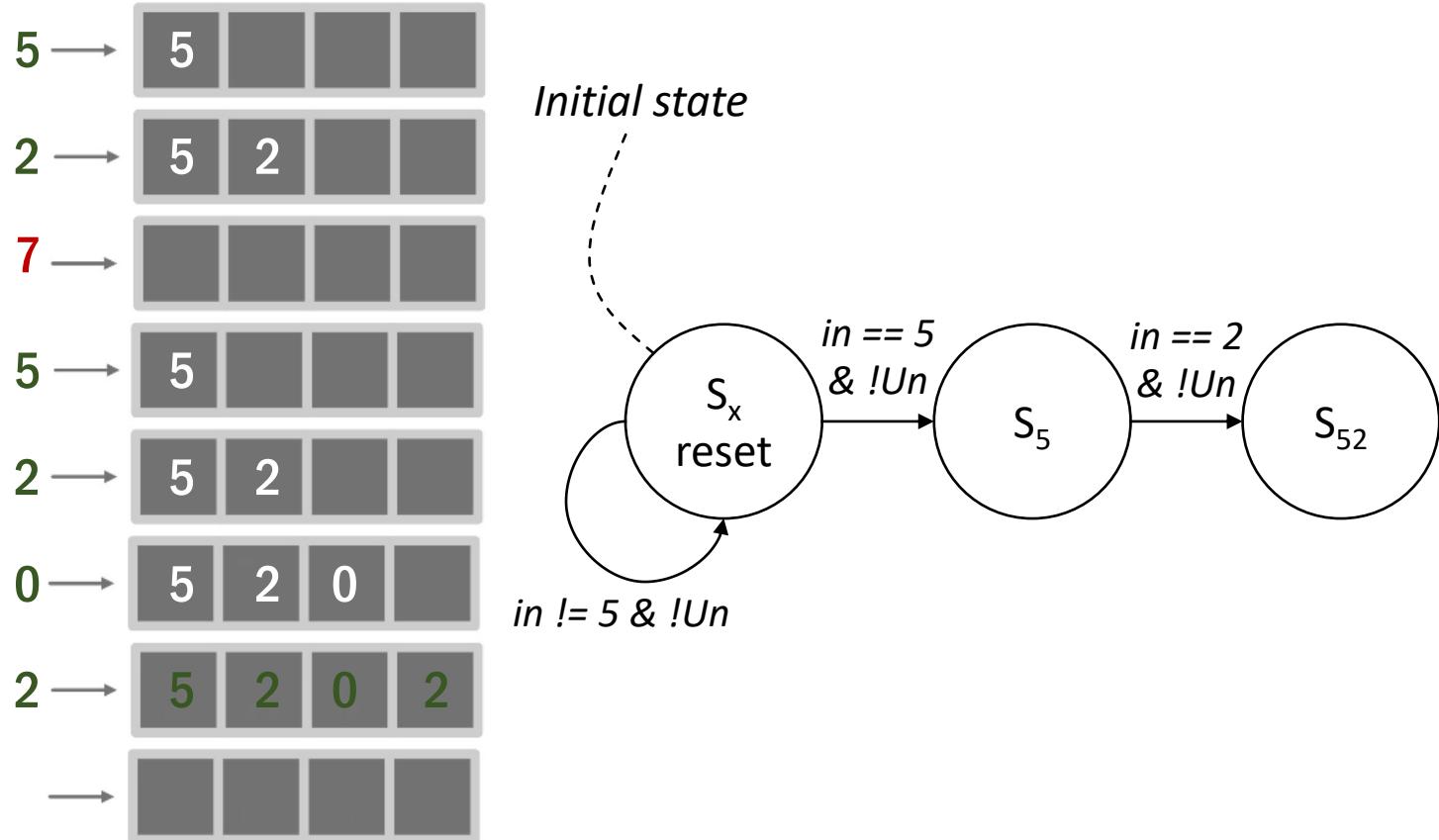
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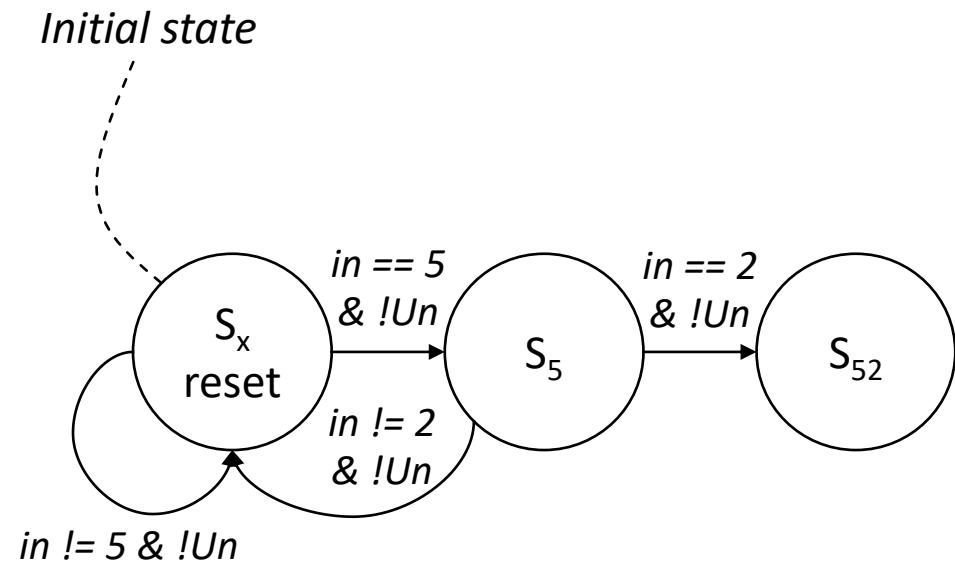
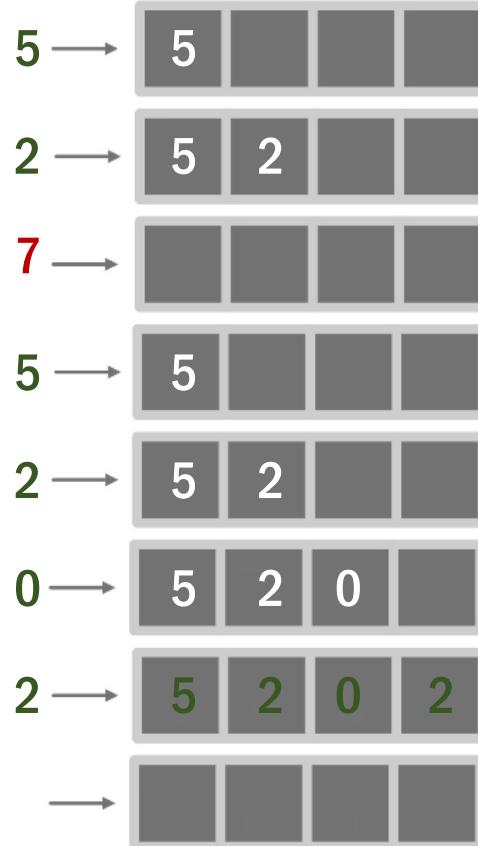
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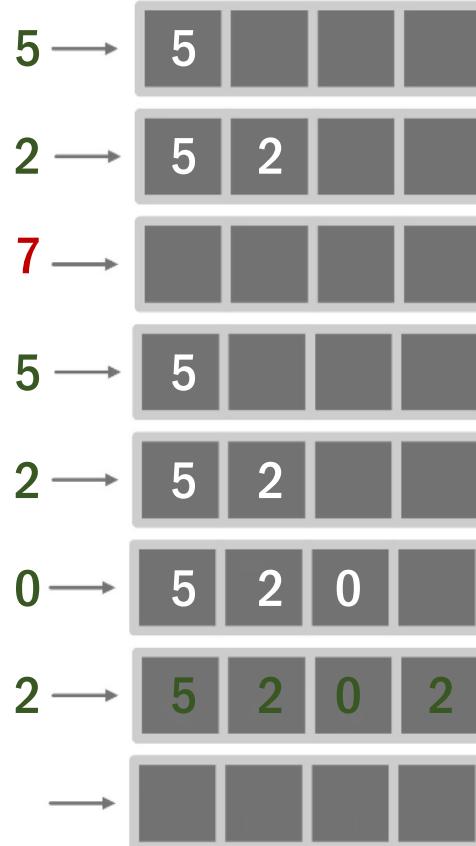
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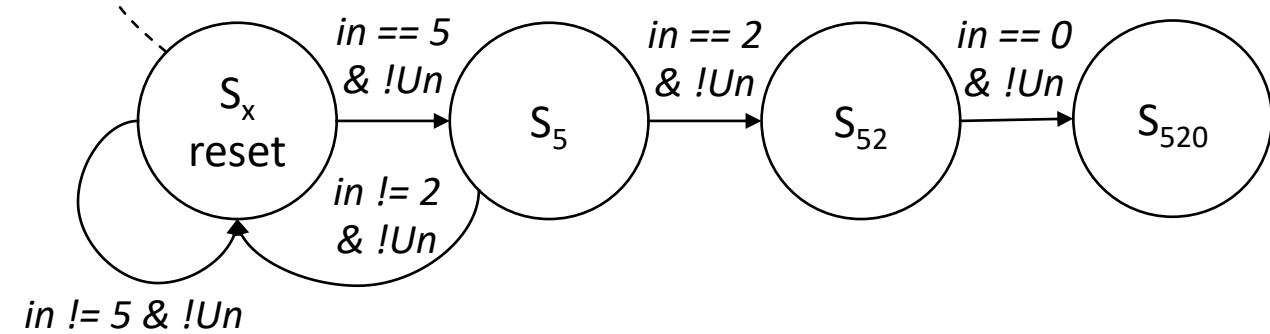


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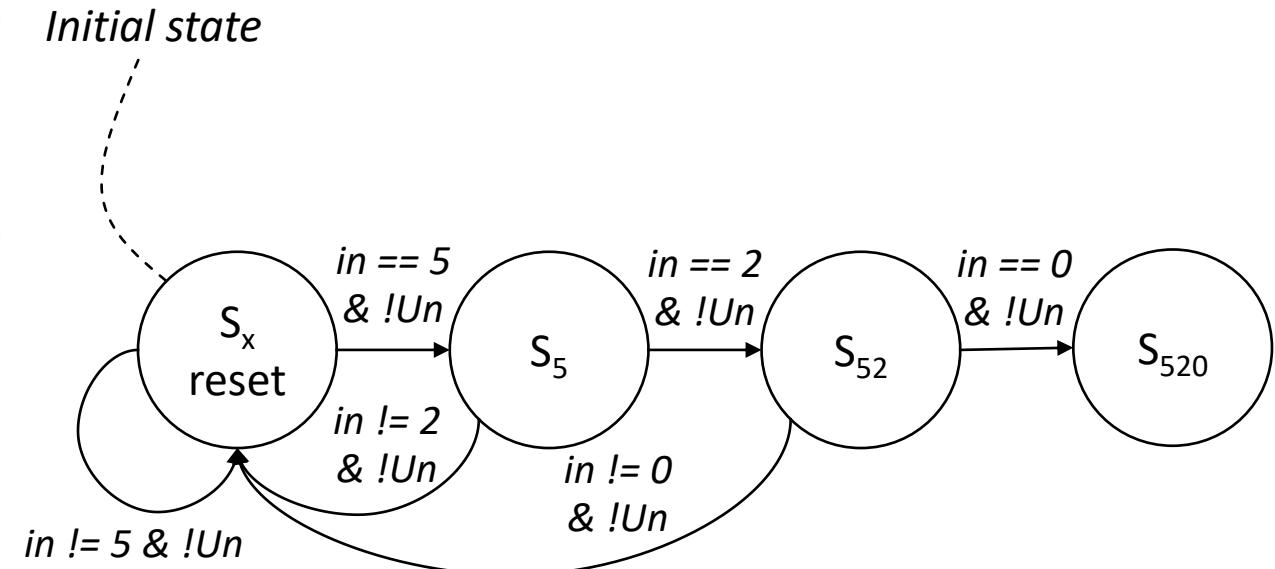
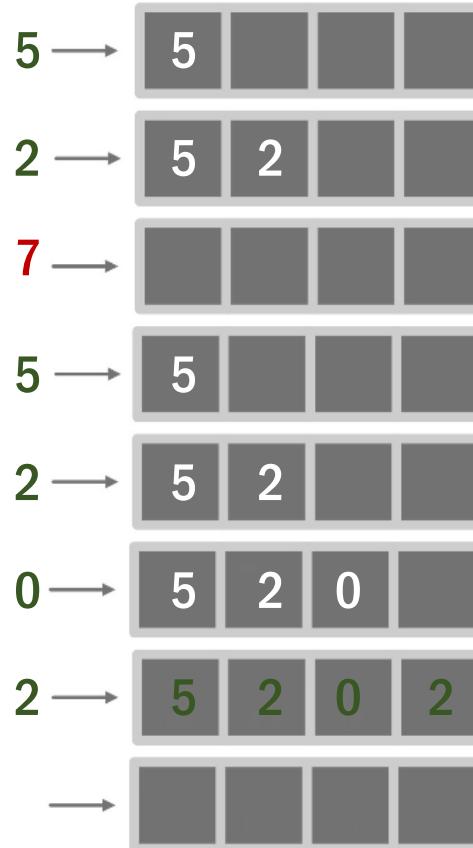


*Initial state*



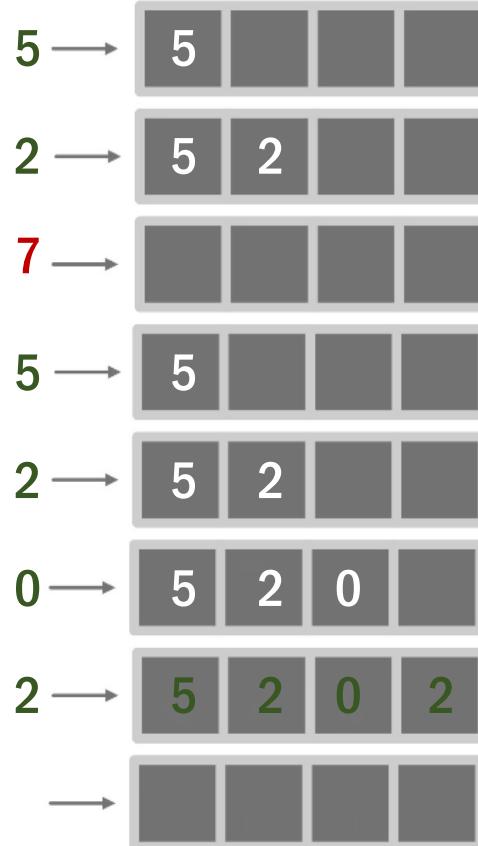
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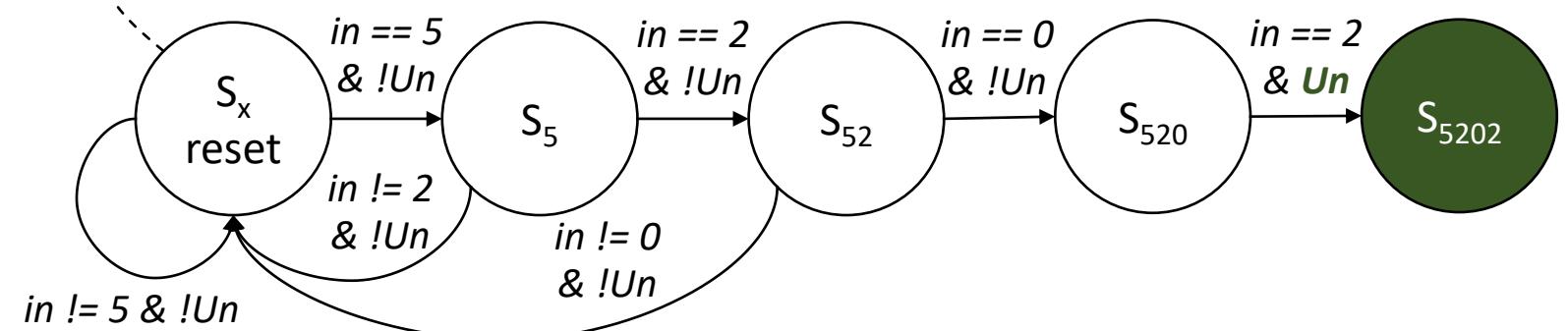


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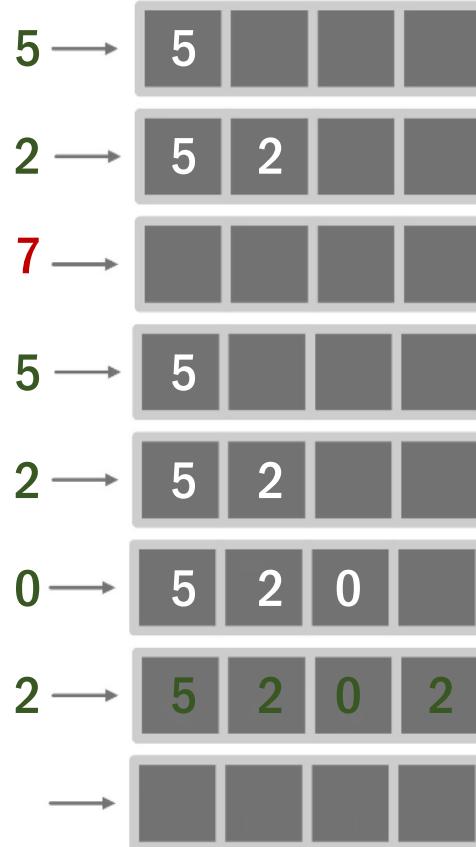


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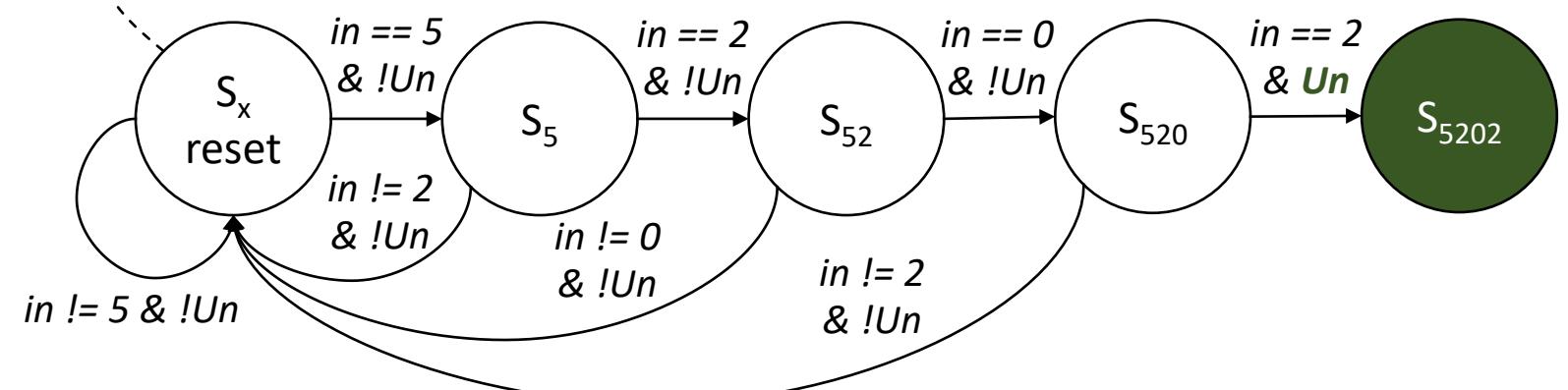


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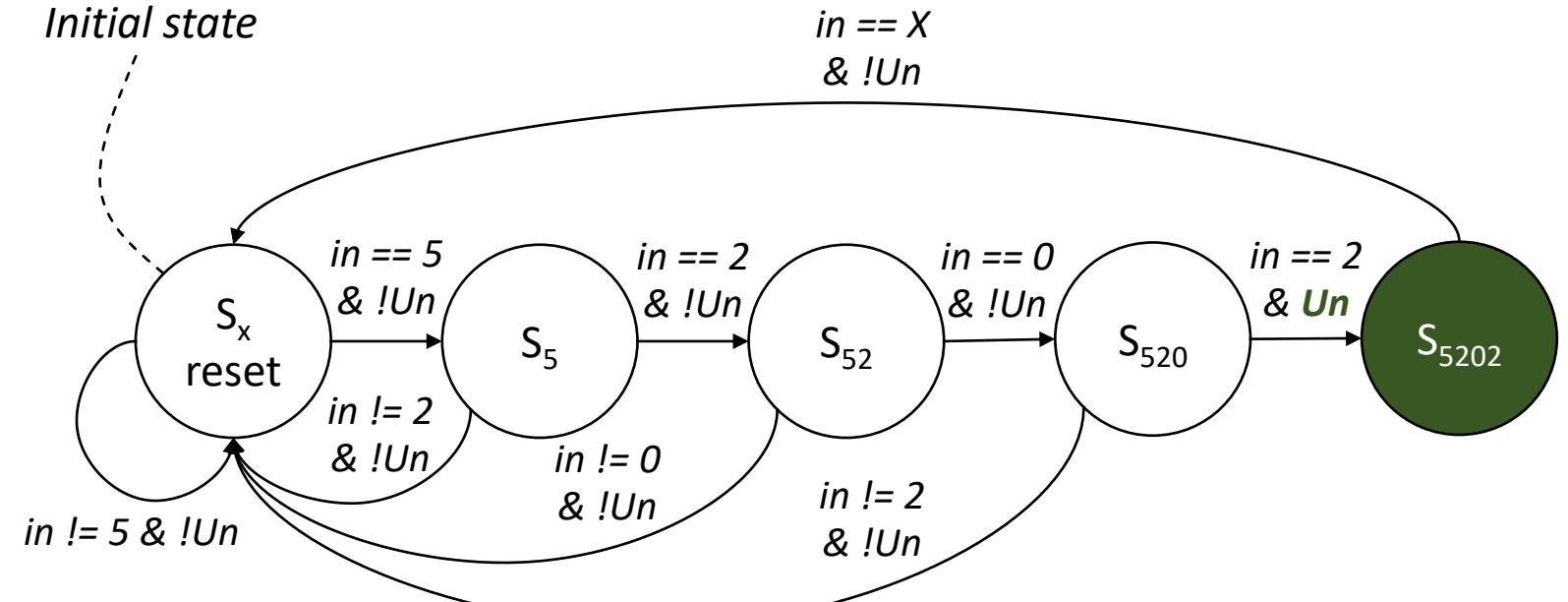
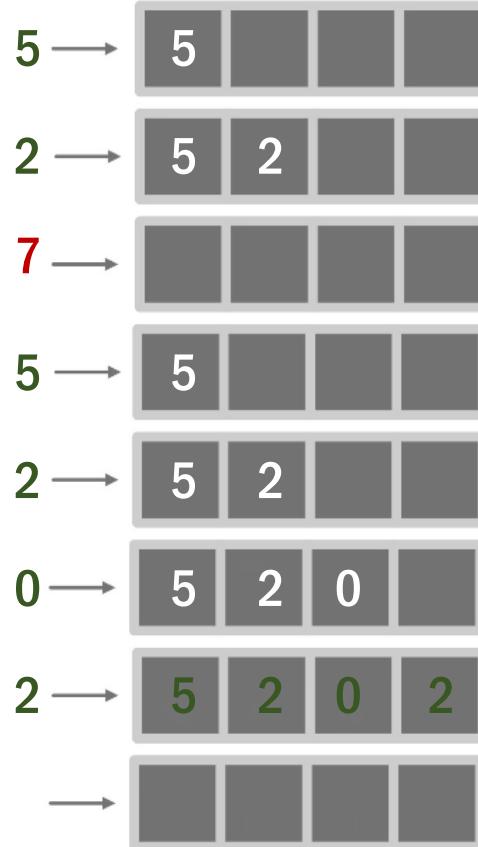


*Initial state*



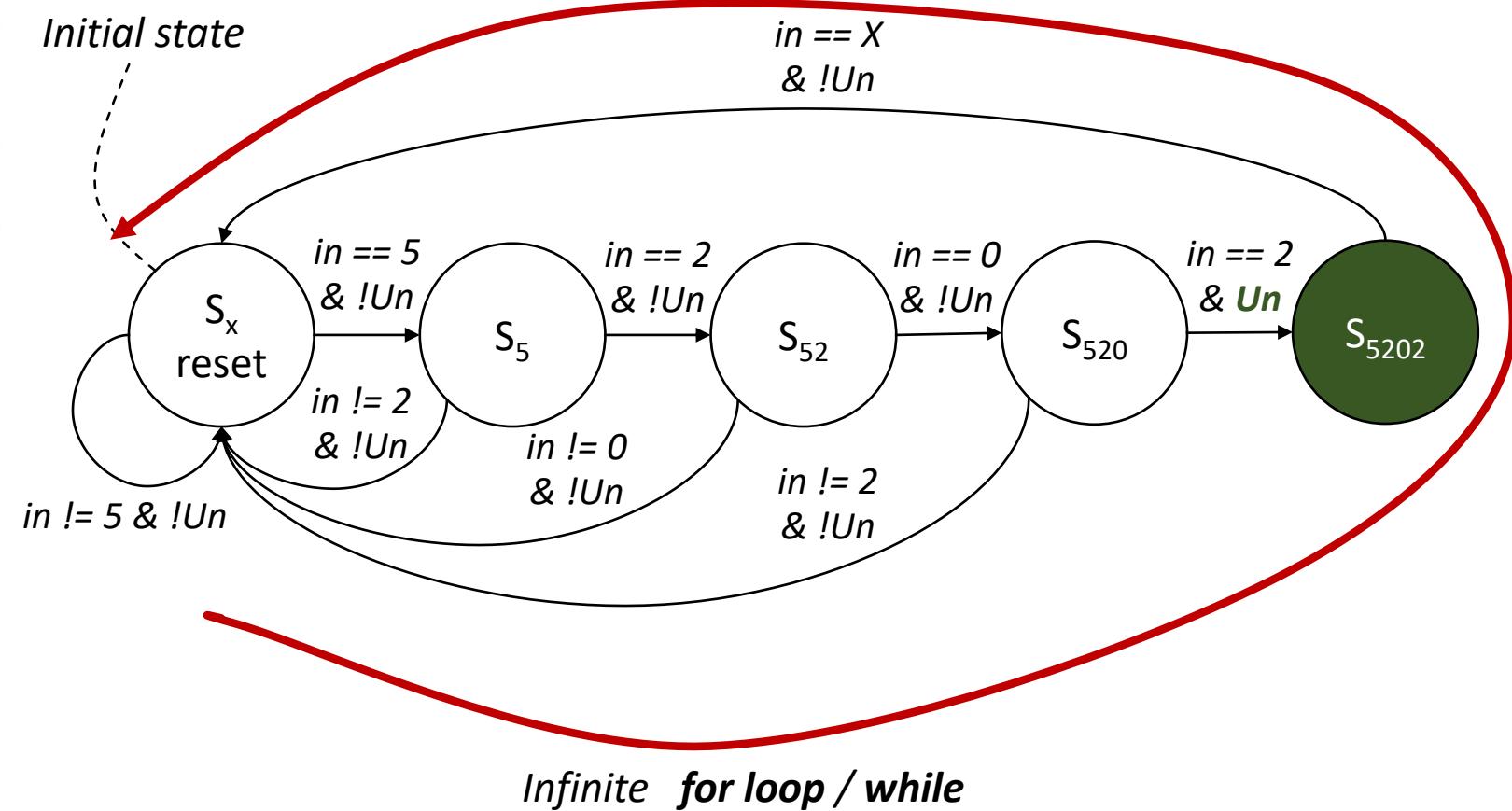
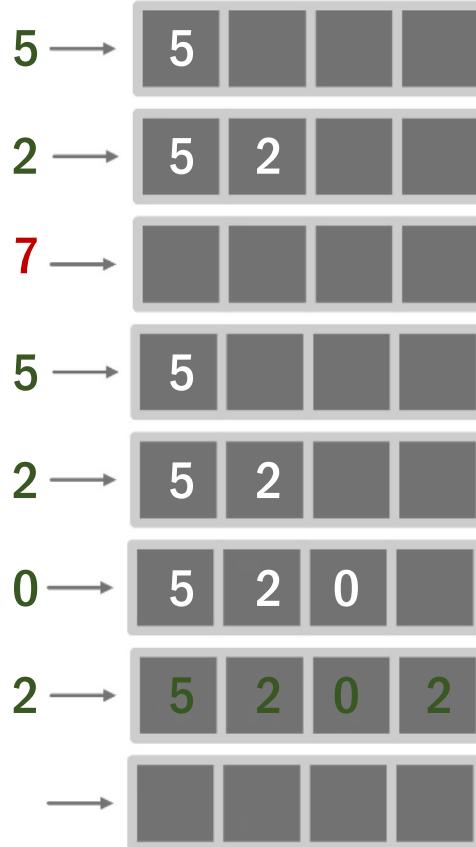
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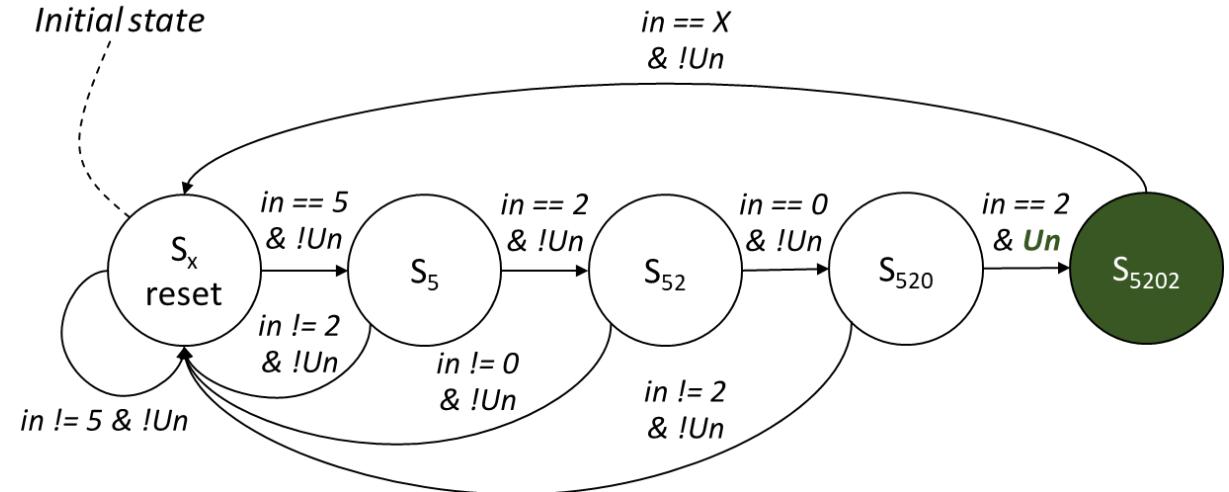
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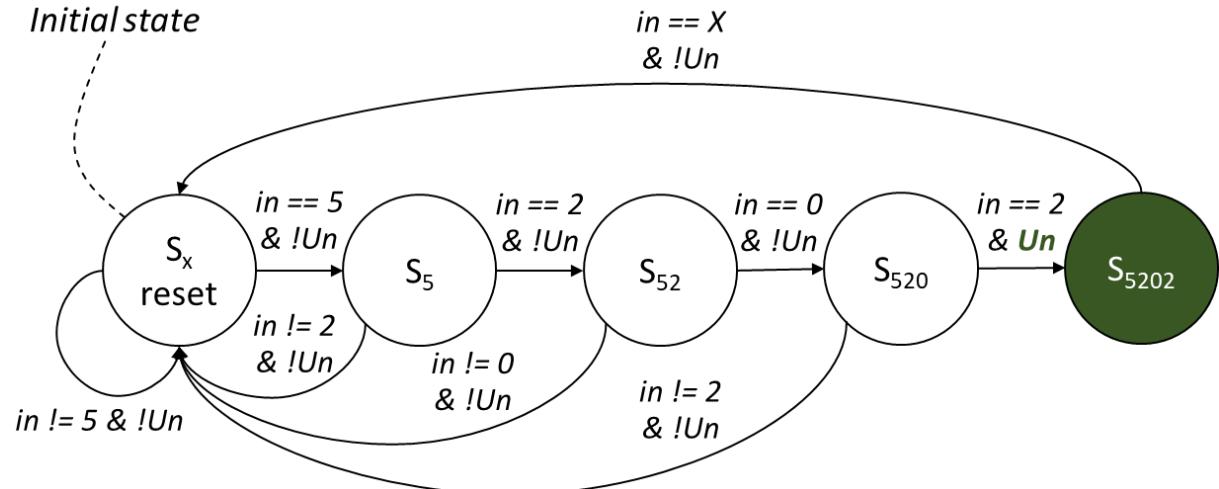
# From FSM to C Code in Embedded Systems

- First step → Create your FSM
  - Define inputs
  - Define outputs
  - Define FSM states
  - Define transition function
  - Define output function



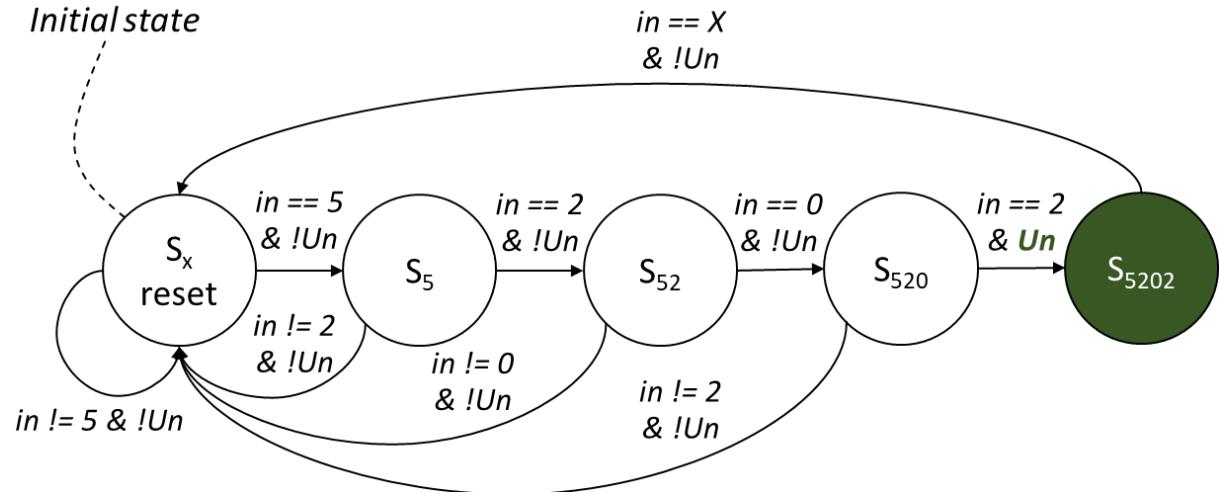
# From FSM to C Code in Embedded Systems

- Second Step → Create C Code
  - create a skeleton for the FSM
  - Define “enum” for the states
    - Initial state must be defined (and static)
  - Define Booleans for output
  - Build an empty switch-case
    - A break per each state
    - All states must be defined
  - Write the code for each state in switch-case
    - Assign output
    - Assign next state
    - Take action (if needed)



# From FSM to C Code in Embedded Systems

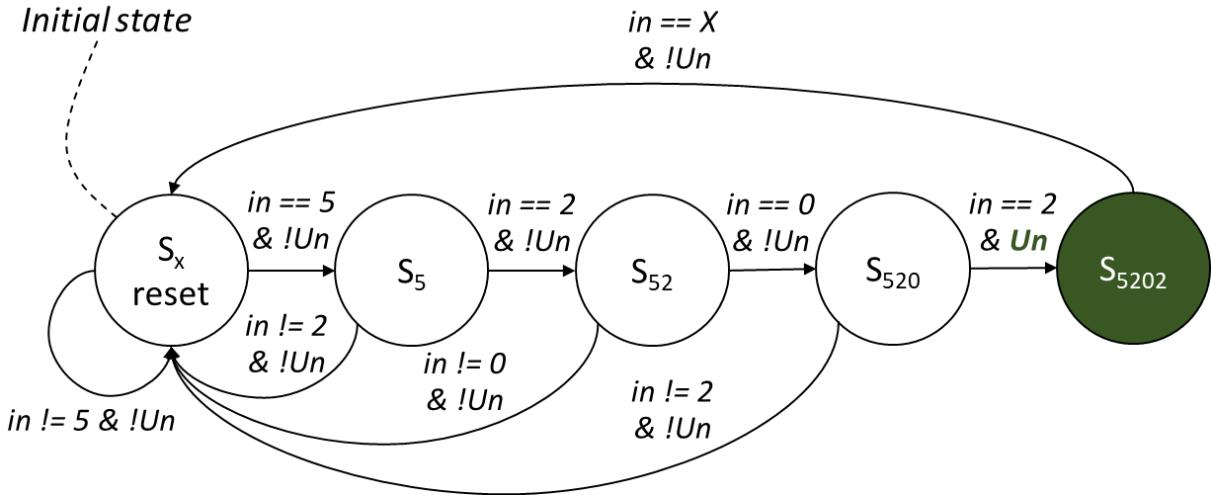
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```
// Define enum for the states
typedef enum {SX, S5, S52, S520, S5202} passcode_state_t;
static passcode_state_t currentState = SX;
```

# From FSM to C Code in Embedded Systems

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```

// Define output function
bool unlocked = false;
if (unlocked)
    unlock();
else
    lock();
    
```

# From FSM to C Code in Embedded Systems

- Second Step → Create C Code

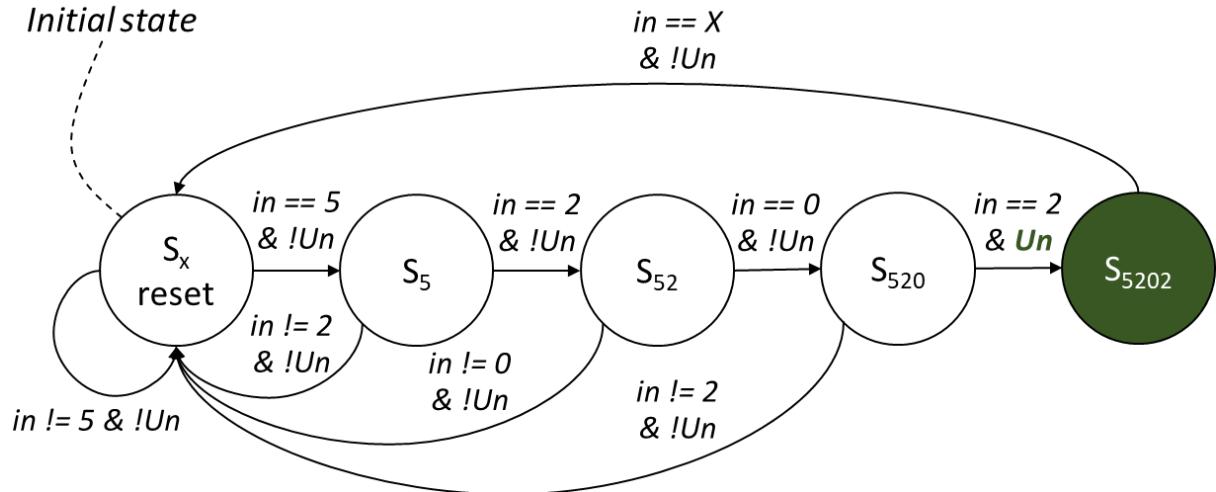
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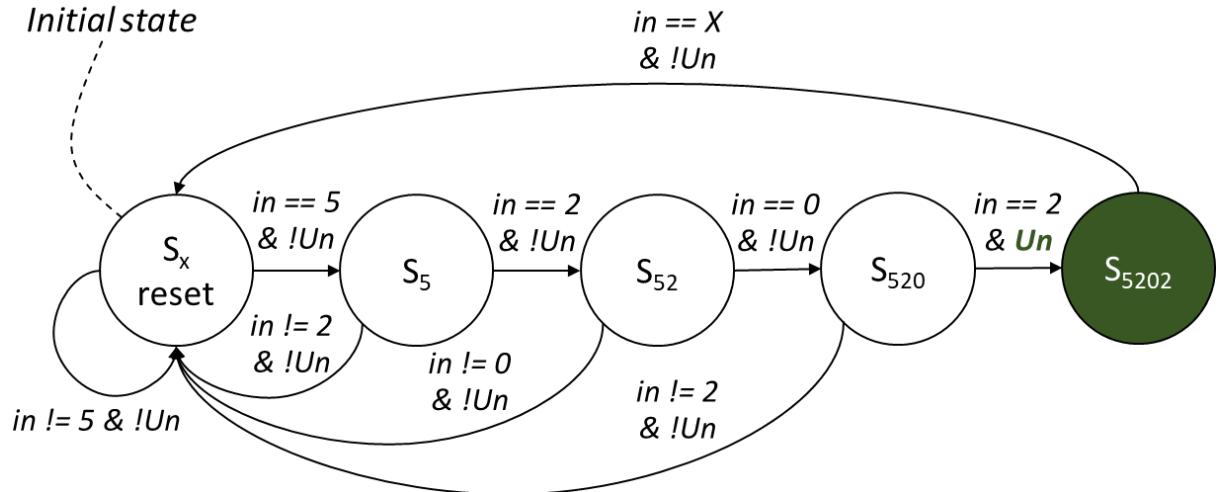
```

// Build an empty switch-case
switch (currentState) {
  case SX:
    break;
  case S5:
    break;
  case S52:
    break;
  case S520:
    break;
  case S5202:
    break;
}
  
```

# From FSM to C Code in Embedded Systems

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*We don't need else here! It's already in SX.*



```

// Build an empty switch-case
switch (currentState) {
    case SX:
        if (in == 5)
            currentState = S5;
        break;
    case S5:
        if (in == 2)
            currentState = S52;
        else
            currentState = SX;
        break;
    case S52:
        if (in == 0)
            currentState = S520;
        else
            currentState = SX;
        break;
    case S520:
        if (in == 2 & Un)
            currentState = S5202;
        else
            currentState = SX;
        break;
}
  
```

# From FSM to C Code in Embedded Systems

- Second Step → Create C Code

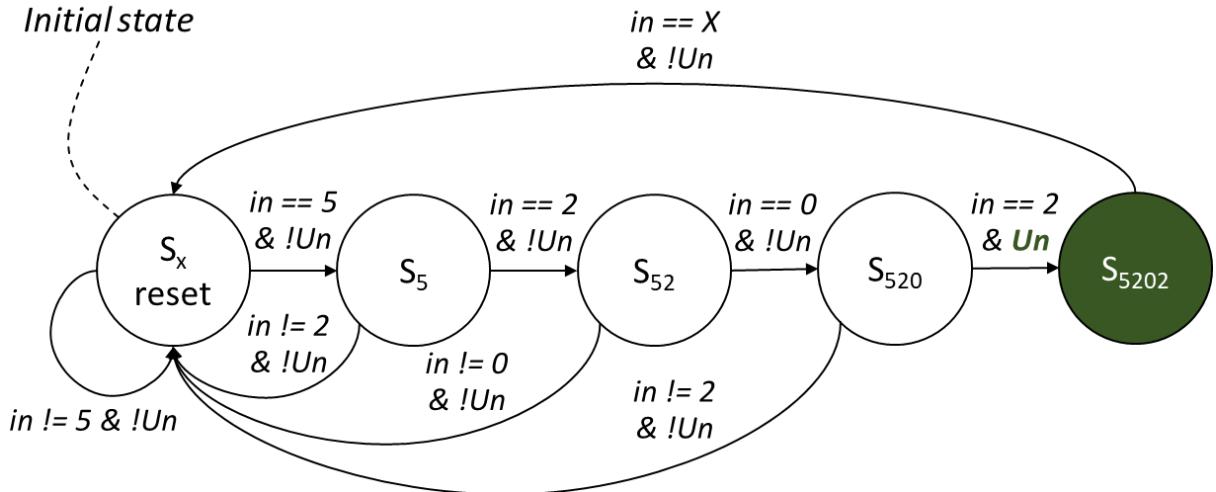
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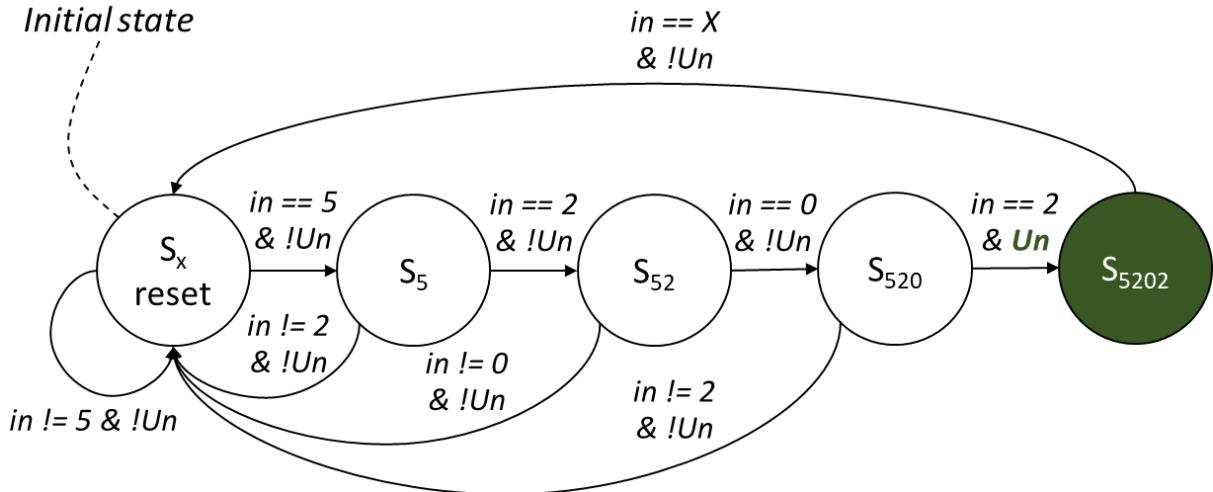


```

// Build an empty switch-case
switch (currentState) {
    case S52:
        if (in == 0)
            currentState = S520;
        else
            currentState = SX;
        break;
    case S520:
        if (in == 2)
            currentState = S5202;
        unlocked = true;
        else
            currentState = SX;
        break;
}
  
```

# From FSM to C Code in Embedded Systems

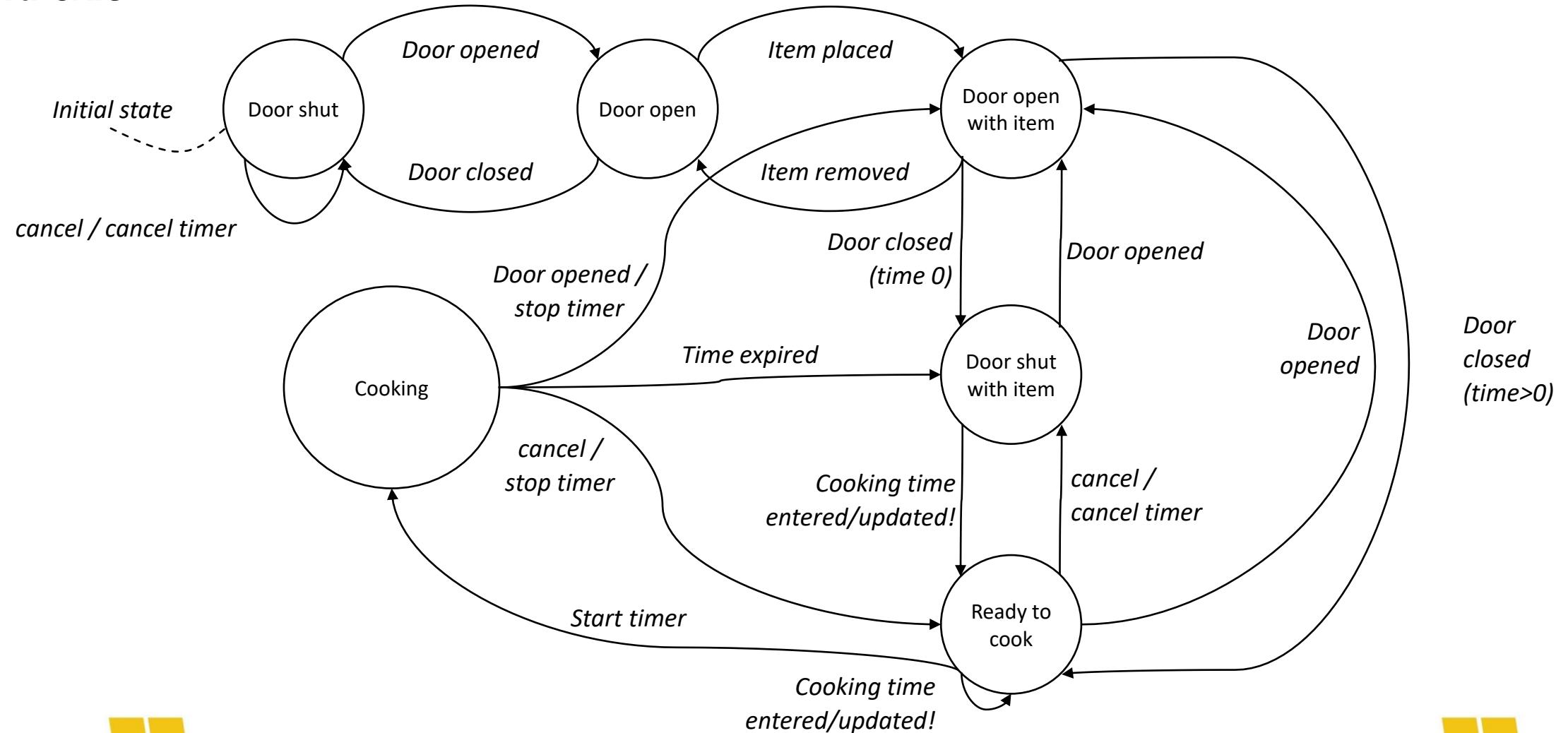
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```
// Build an empty switch-case
switch (currentState) {
  case S520:
    currentState = SX;
    break;
```

# Another example: Microwave

- Entry and exit



# Timer for Lab 3 – You can use FSM now!

- **Signal Repeater**

- Whatever you push (and hold) – the duration - using push button S1
  - will be replayed on redLED!
- If you exceed a maximum time (64K cycle or 2 seconds)
  - An error flag will be shown on greenLED!
- Once error flag is raised
  - not working anymore until reset is pushed by push button S2.
- *Counter in continuous mode*
- *Once push button is pressed → counter starts to count.*
- *Once push button is released → counter will be stopped (TAOR will be recorded).*
- *If TAOR < 0xffff → the redLED will be toggled on for the same amount of count.*
- *If TAOR > 0xffff (TAIFG is triggered) → greenLED will be ON. Waiting for S2 to rst.*



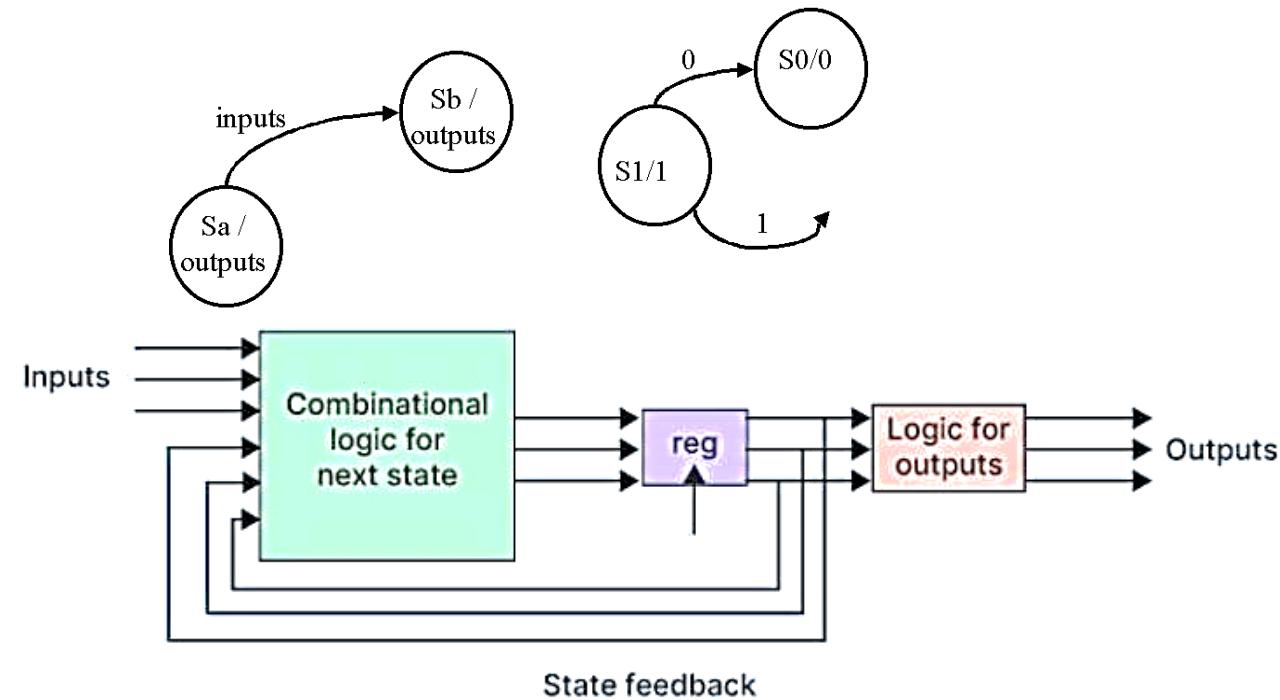
# Mealy and Moore FSM

- Depending on the definition of output function

## Moore FSM

*Outputs are a function of current state!*

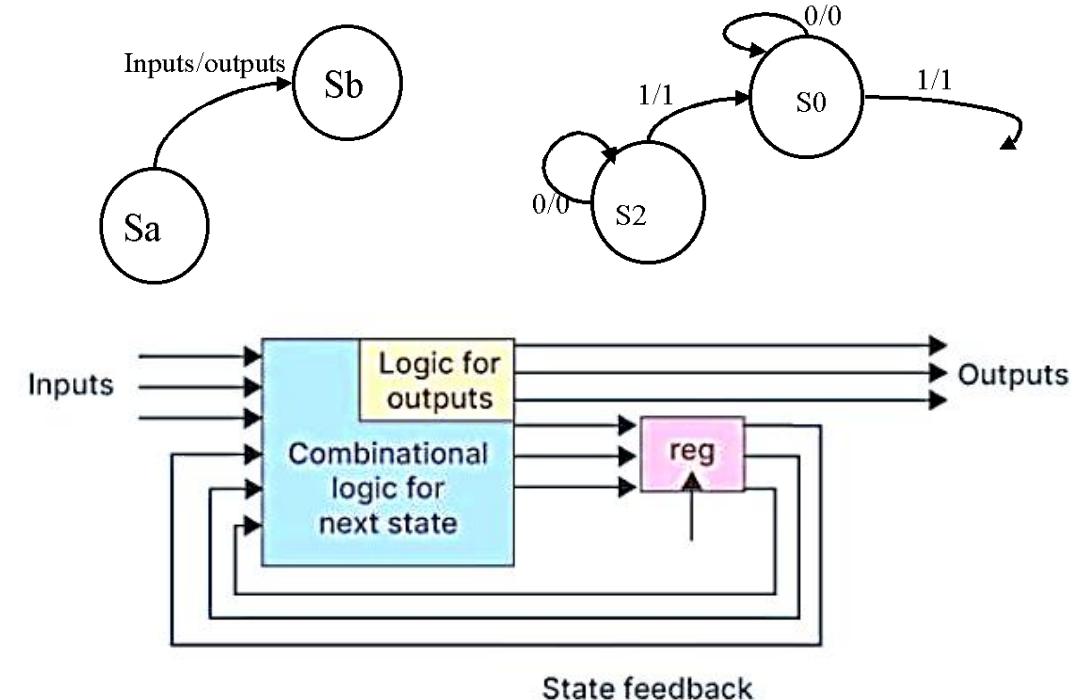
*Outputs change synchronously with state changes!*



## Mealy FSM

*Outputs are a function of current state and inputs!*

*Outputs change can cause immediately w.r.t. inputs!*



# Back to the issues of Blinking LEDs



- Delay for blinking LEDs
    - The first approach was to use a for loop to create a delay between LED toggles.

*for* (*i*=50000; *i*>=0; *i*--){}

**Drawback:** When ‘for loop’ is used, **CPU is heavily utilized because of the delay calculation!**  
(decrement and compare operations)

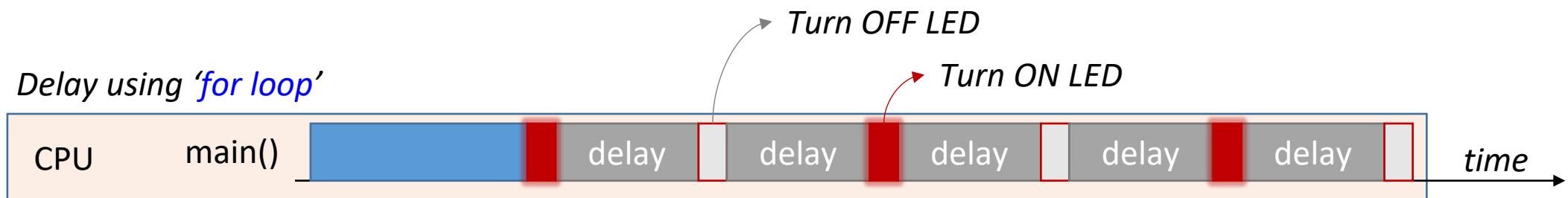
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(decrement and compare operations)



# Timer as an Alternative Option

- Delay for blinking LEDs
  - The first approach was to use a for loop to create a delay between LED toggles.

*for (i=50000; i>=0; i--){}  
 Drawback: When 'for loop' is used, CPU is heavily utilized because of the delay calculation!  
 (decrement and compare operations)*

- The second approach was to use Timer to create a delay between the LED toggles.

*while (TAIFG is not set){}  
 Advantage: When it is replaced with a timer, the decrement (or increment) operation is taken over by the Timer peripheral.*

TAOCTL	rsvd.						TASSEL	ID	MC	rsvd.	TACLR	TAIE	TAIFG		
	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1

# Timer is Good?

- Delay for blinking LEDs
  - The first approach was to use a for loop to create a delay between LED toggles.

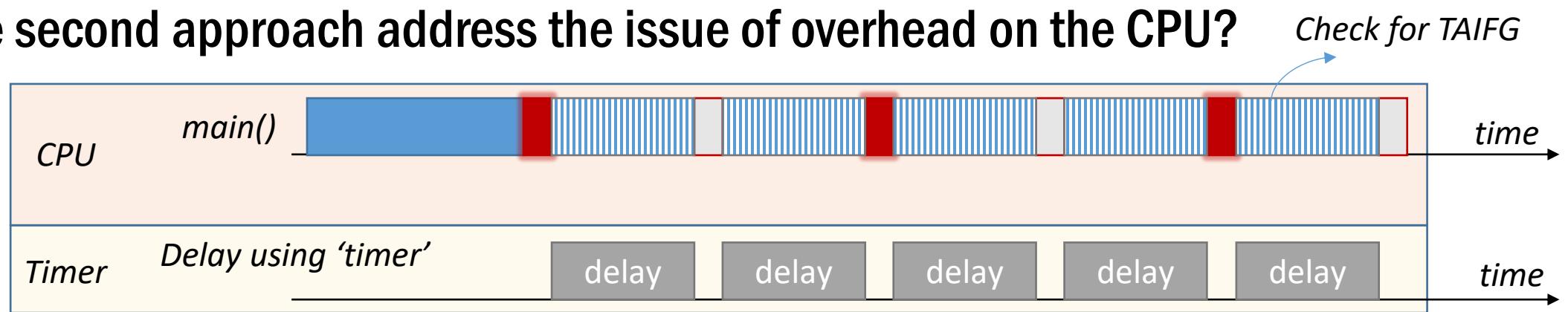
`for (i=50000; i>=0; i--){}`

*Drawback: When 'for loop' is used, CPU is heavily utilized because of the delay calculation!  
(decrement and compare operations)*

- The second approach was to use Timer to create a delay between the LED toggles.

`while (TAIFG is not set){}`

- Does the second approach address the issue of overhead on the CPU?





# Timer is Good?

- Delay for blinking LEDs
  - The first approach was to use a for loop to create a delay between LED toggles.

`for (i=50000; i>=0; i--){}`

*Drawback: When 'for loop' is used, CPU is heavily utilized because of the delay calculation!  
(decrement and compare operations)*

- The second approach was to use Timer to create a delay between the LED toggles.

`while (TAIFG is not set){}`

*Advantage: When it is replaced with a timer, the decrement (or increment) operation is taken over by the Timer peripheral.*

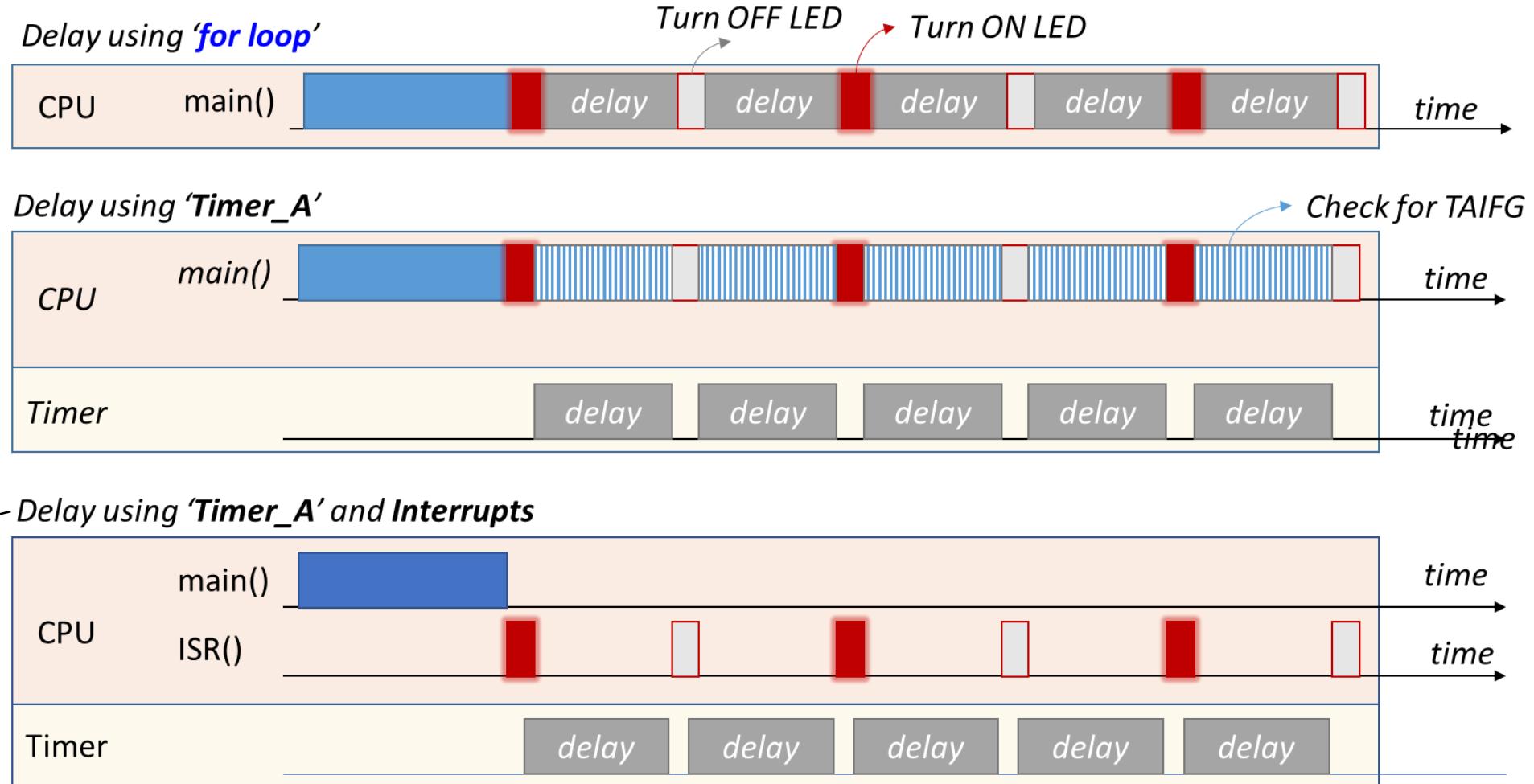
- Does the second approach address the issue of overhead on the CPU?

*Drawback: the flag still needs to be checked periodically by the CPU.*

- This technique is also called as 'polling'.

# What is the Ideal Solution: Interrupts

- We need more automation, leaving the CPU less busy for other important operations!



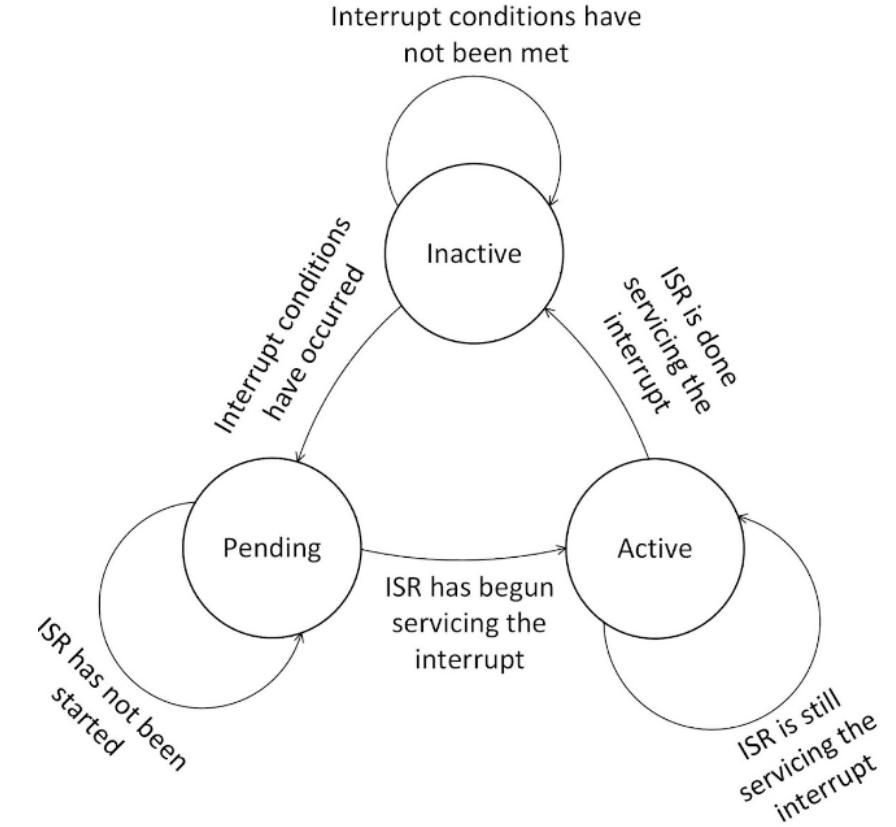


# Interrupts

- A signal calling attention to an event that needs immediate attention!
  - Generated by software or hardware
  - To the processing unit (CPUs or microcontrollers or FPGAs – Any Master)
  - To efficiently managing relationships with external devices
    - e.g., data arrival, user pressing a key, a specific time has passed, etc.
- When an interrupt occurs
  - The processor temporarily halts the execution of the code
  - Calls an interrupt handler function, also known as an **interrupt service routine (ISR)**
  - Once the processor handles the event, the processor resumes
    - continuing from where the execution had previously stopped

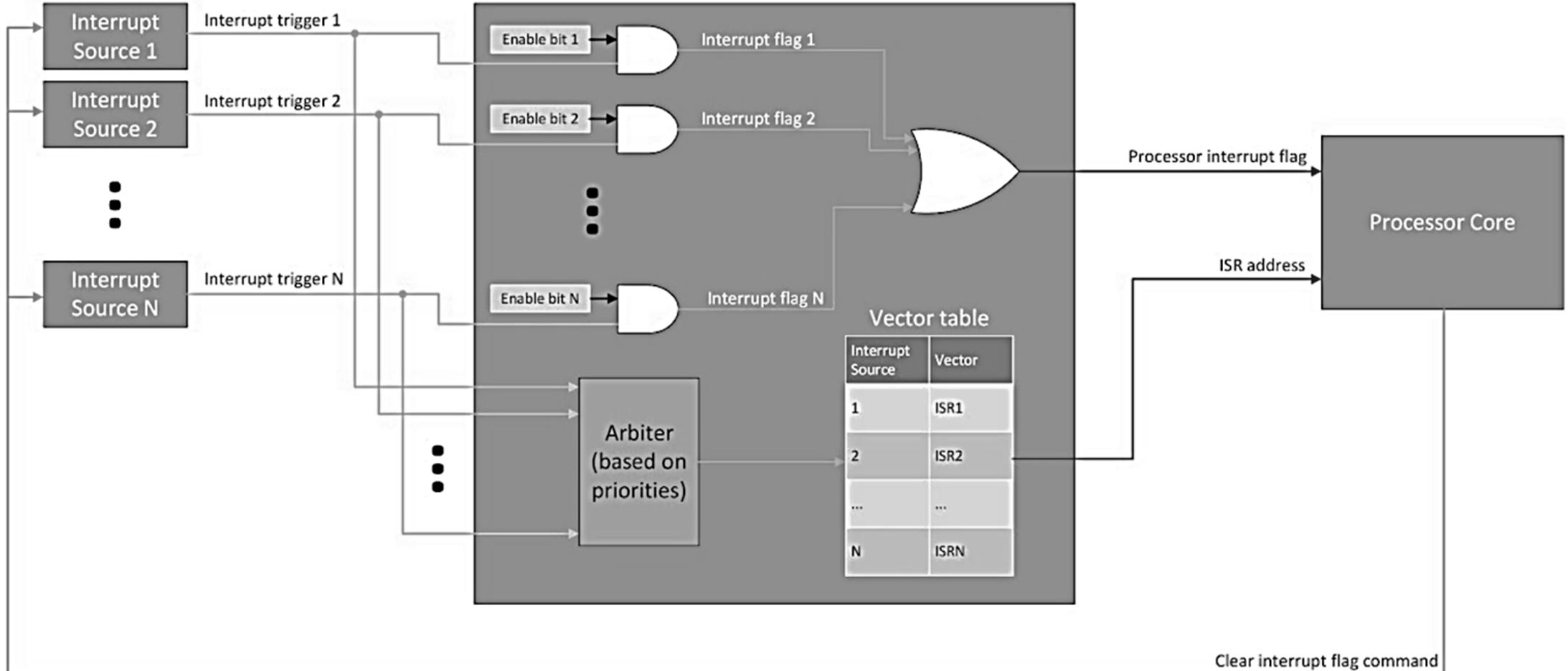
# Interrupts

- Status of Interrupts
  - Inactive: the conditions to generate the interrupt haven't been met.
  - Pending: the conditions have been met, but the ISR has not been called.
  - Active: the ISR is servicing the interrupt.
- Building Blocks
  - Controller: peripheral that helps the processor manage the interrupts.
  - Source: any peripheral that can interrupt the processor.
  - Trigger: a hardware event that generates the interrupt via an electrical signal



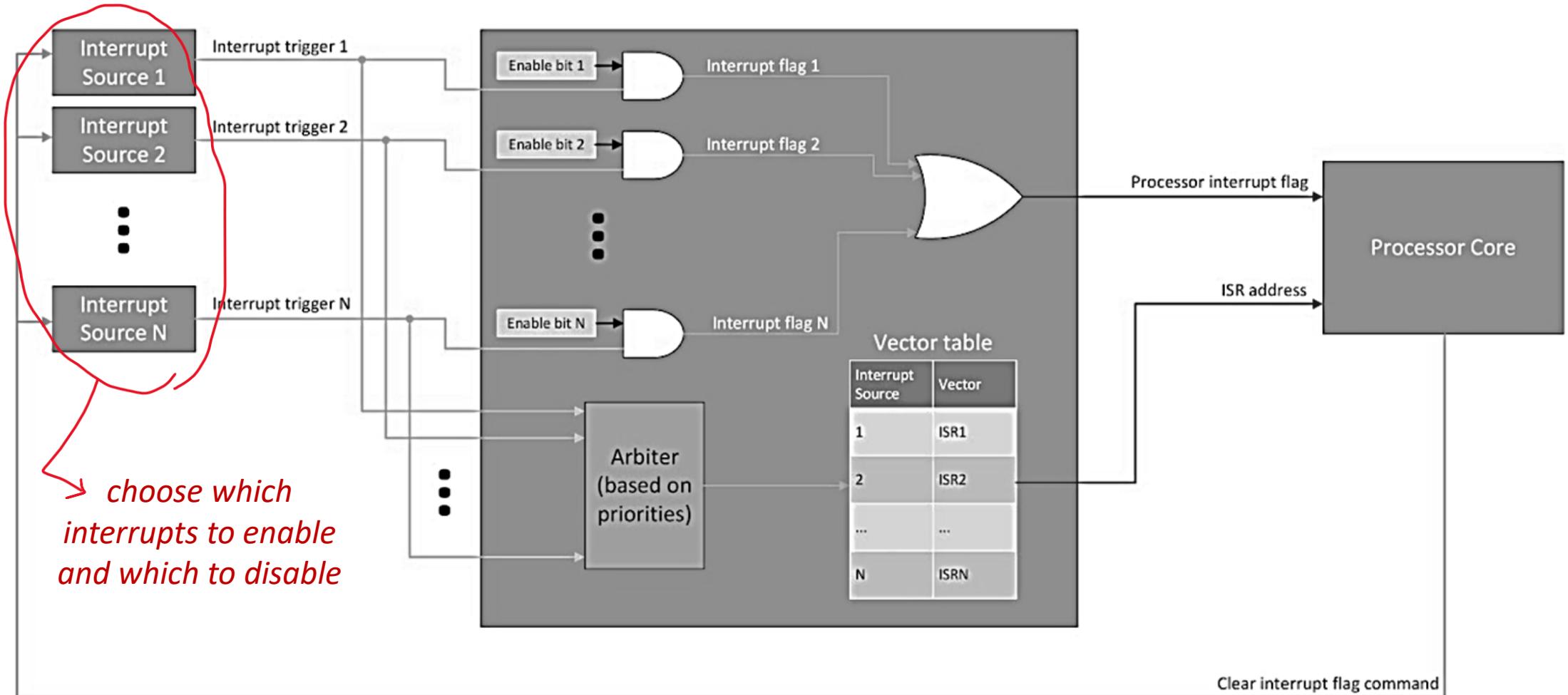
# Interrupt Architecture

- How it works...



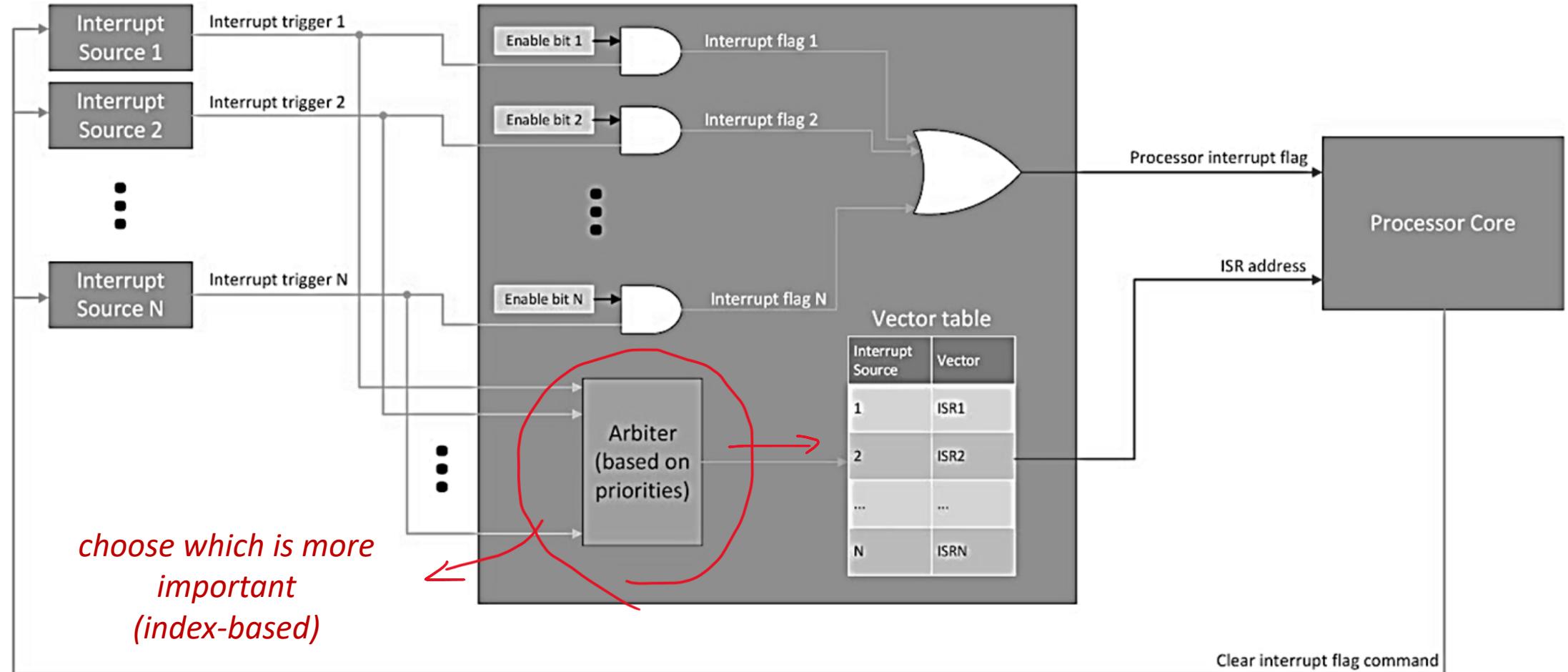
# Interrupt Architecture

- Sources of Interrupts



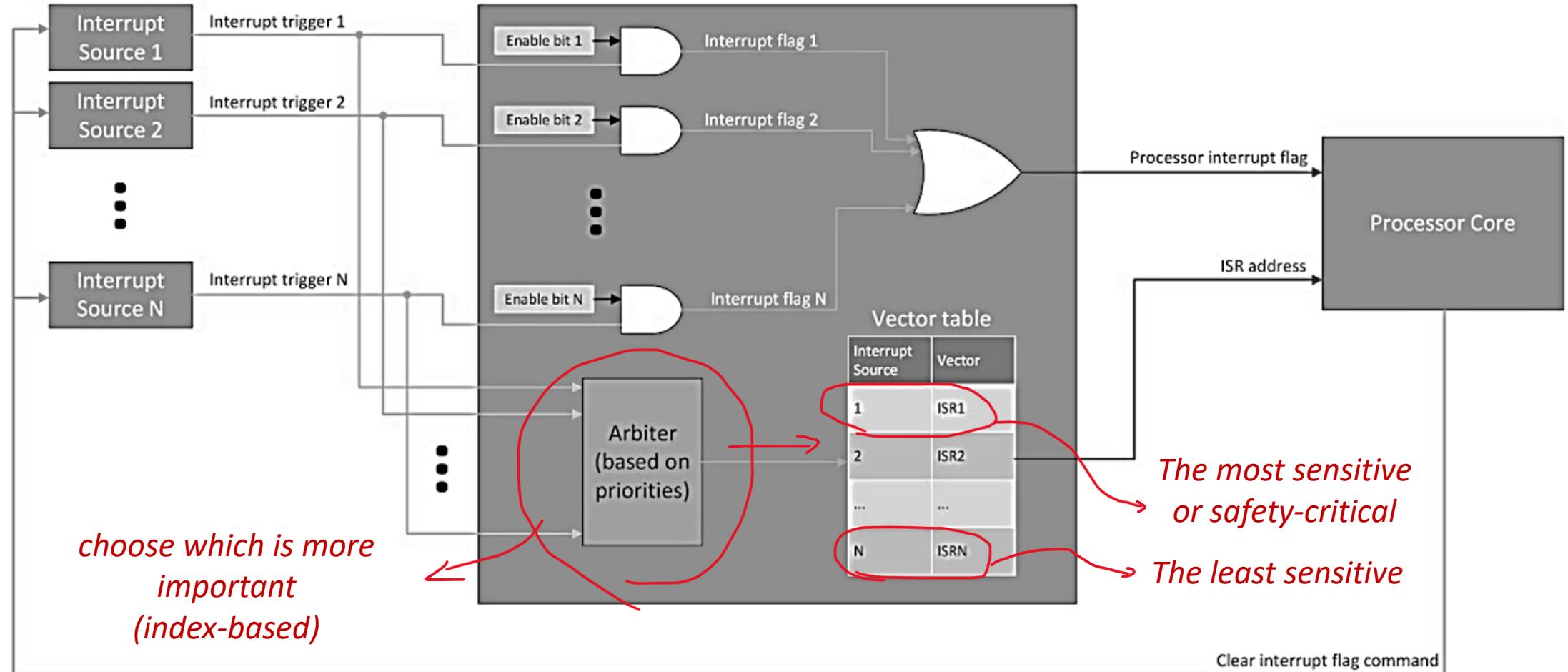
# Interrupt Architecture

- Priority of the Interrupts



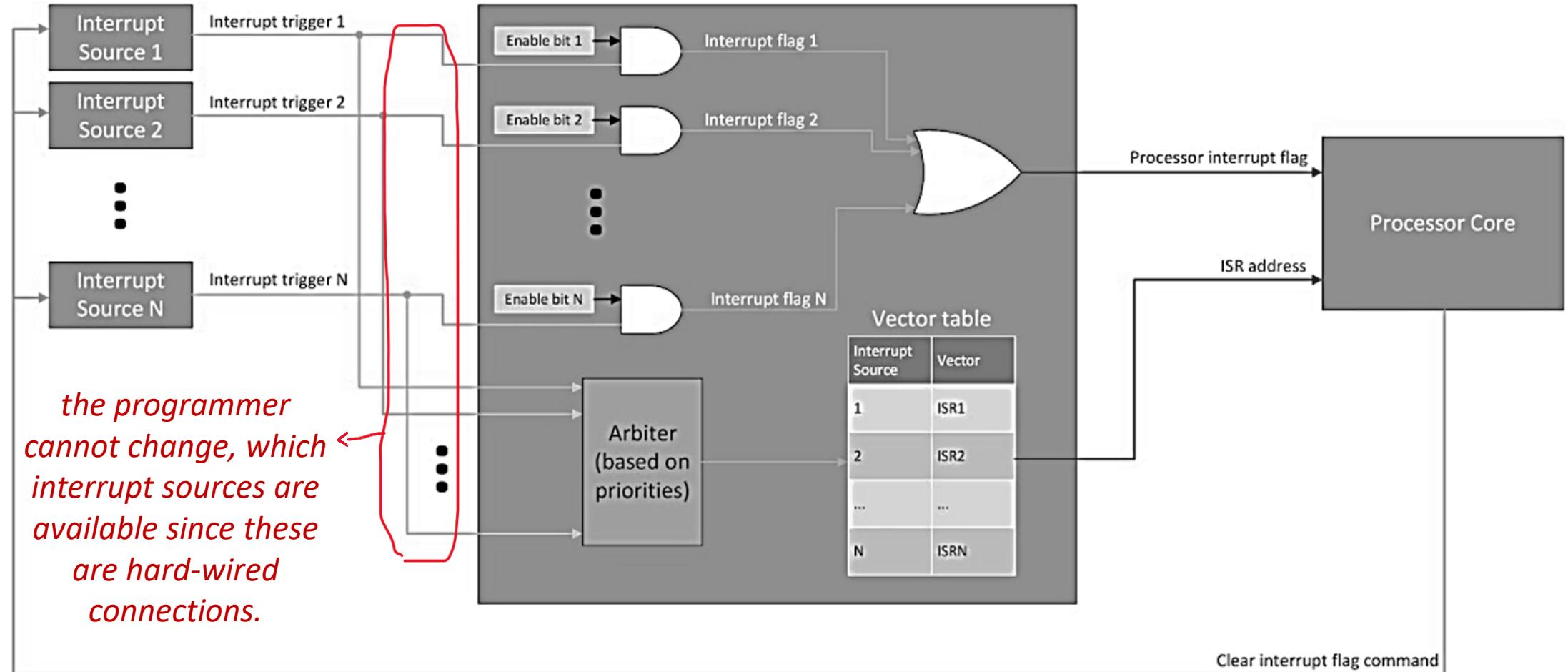
# Interrupt Architecture

- ISR Priority Indexing



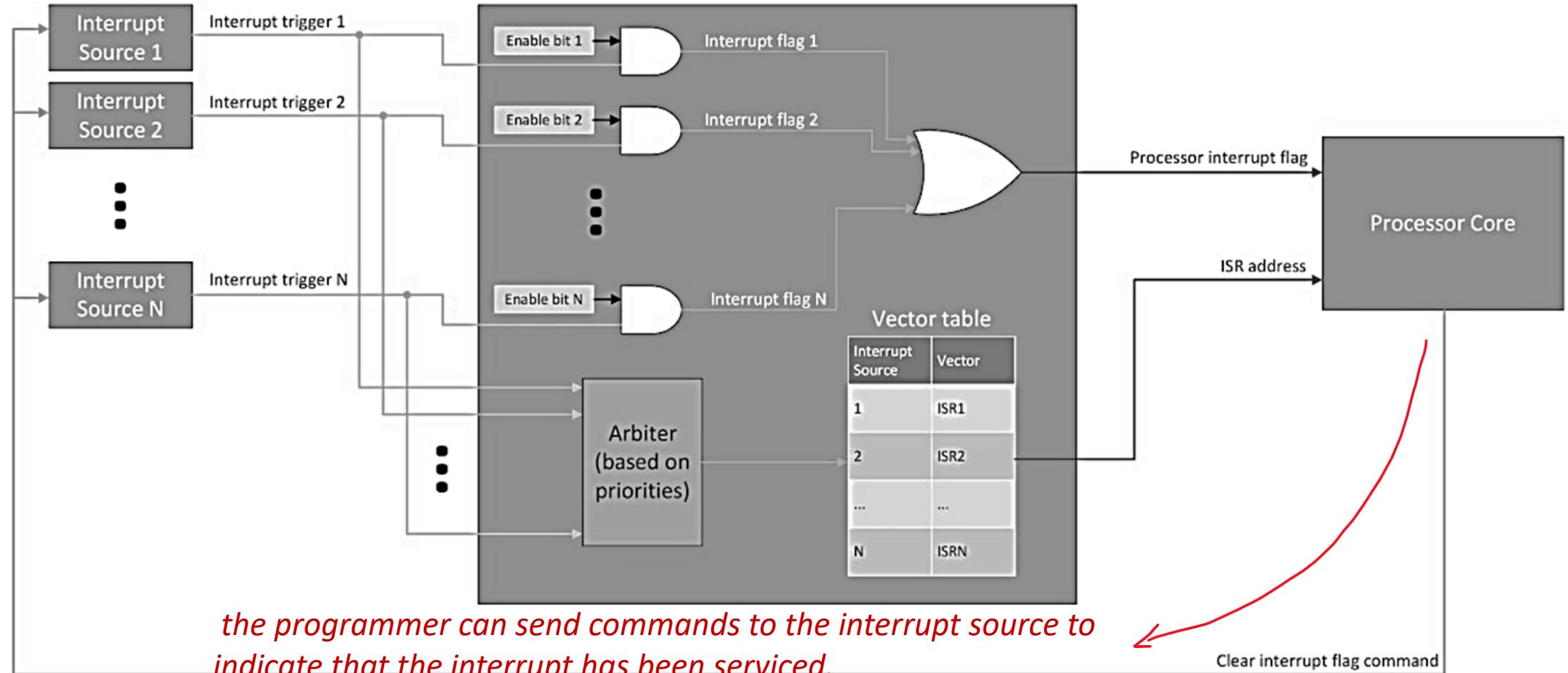
# Interrupt Architecture

- Hardware Availability



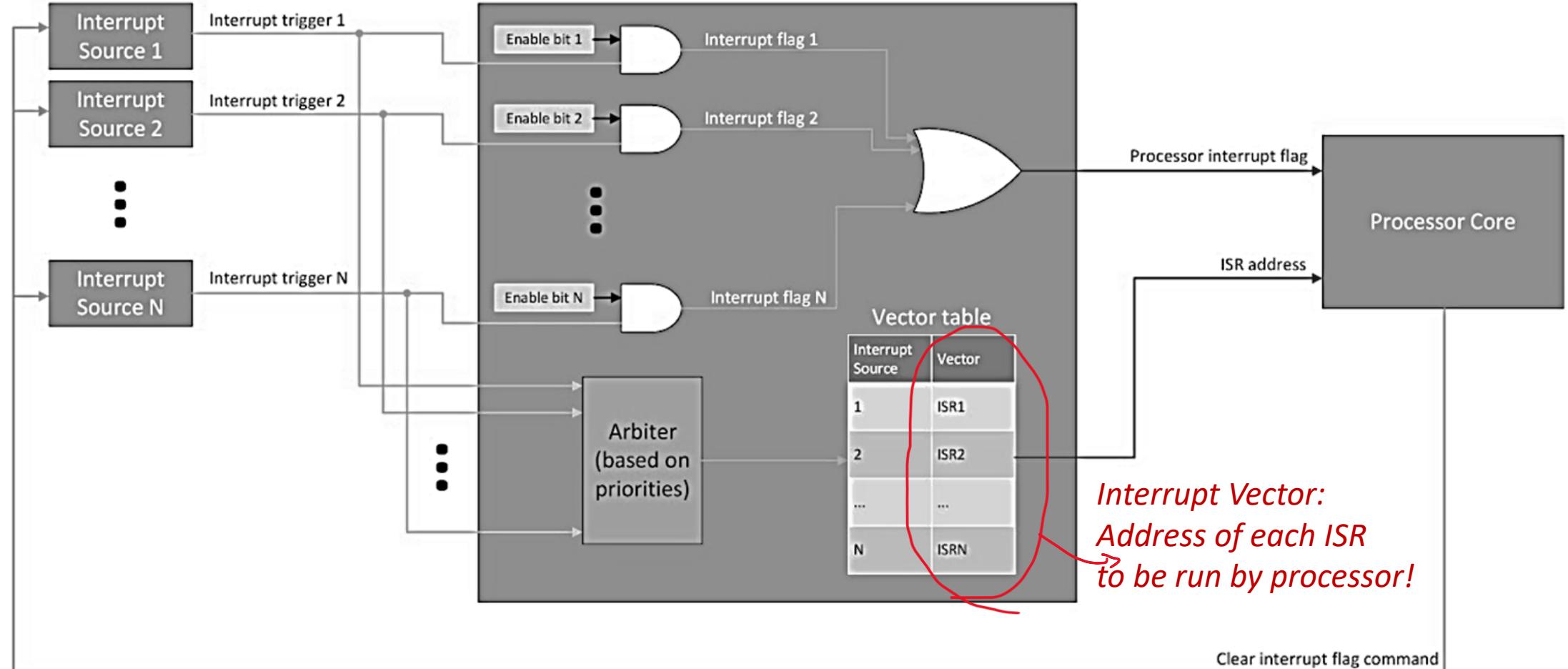
# Interrupt Architecture

- Clearing Interrupt Flag



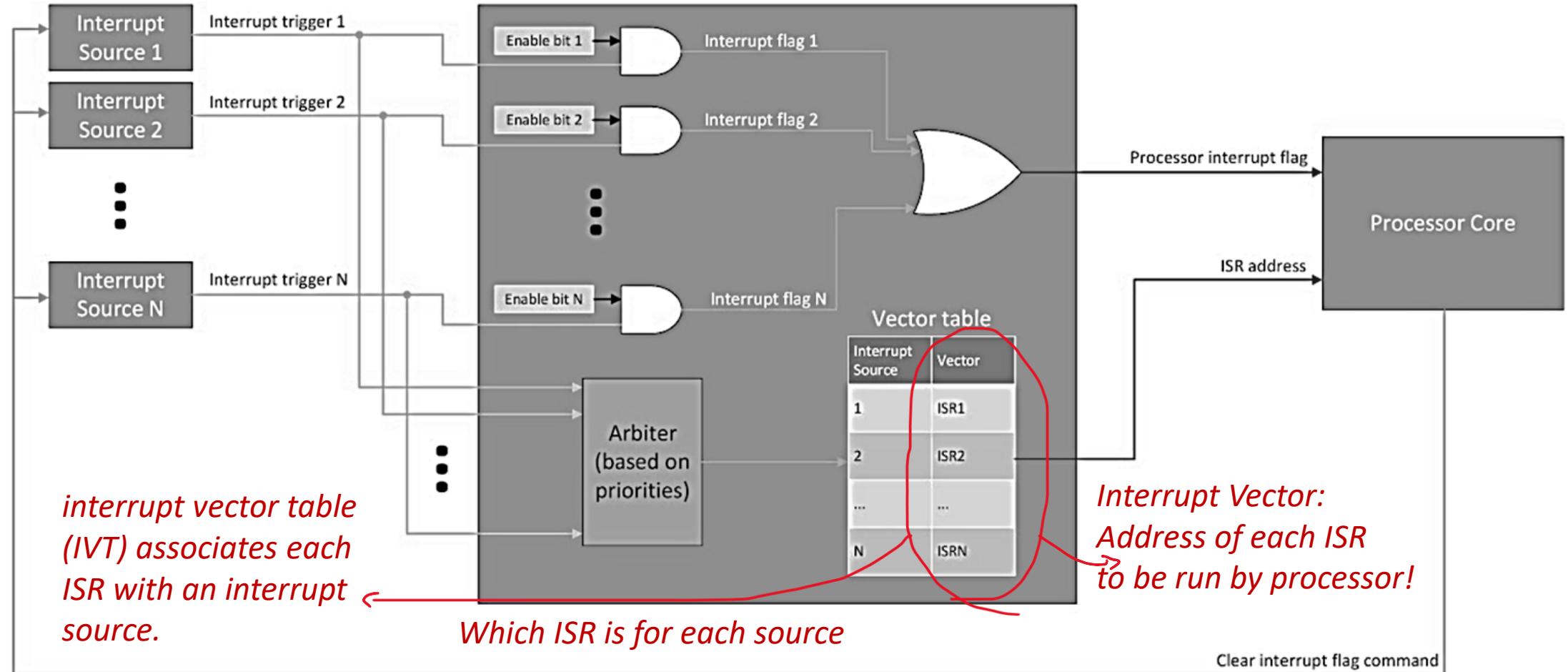
# Interrupt Architecture

- Address of ISRs



# Interrupt Architecture

- ISR Mapping Address

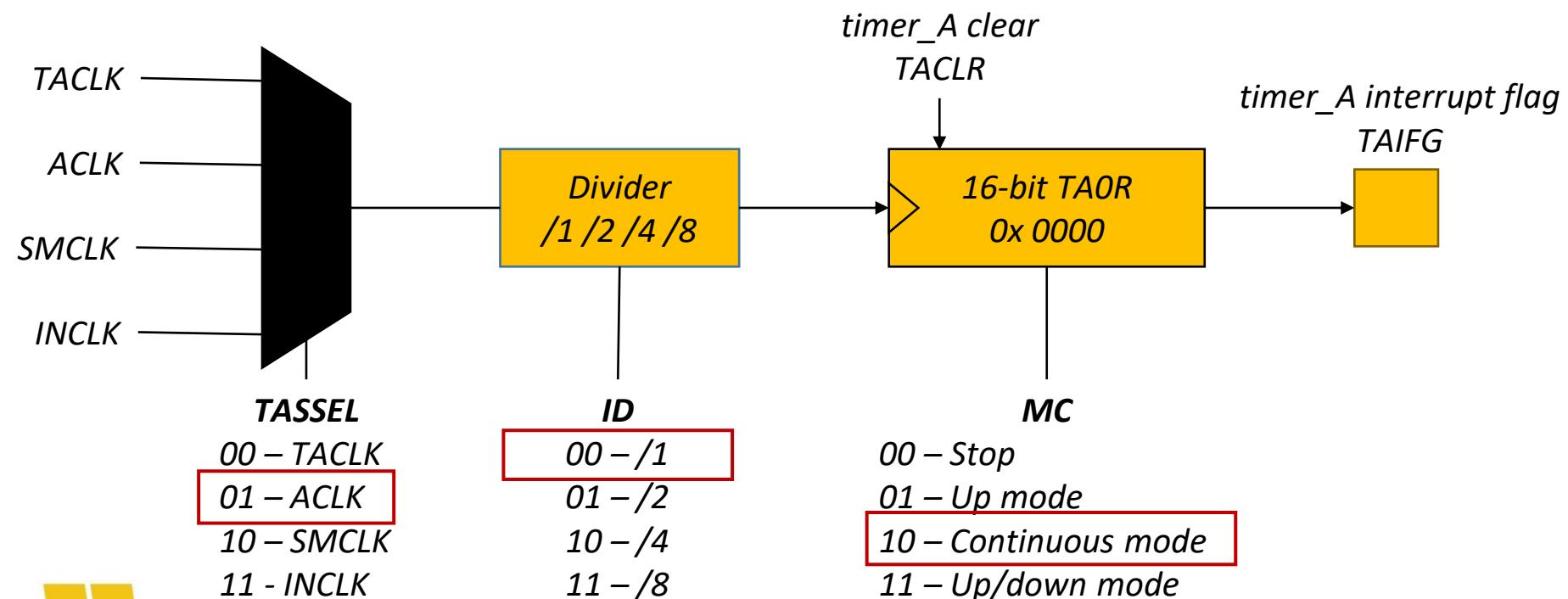


# Timer Interrupt in MSP430

- For Timer\_A

<i>TAOCTL</i>	rsvd.						<i>TASSEL</i>	<i>ID</i>	<i>MC</i>	rsvd.	<i>TACLR</i>	<i>TAIE</i>	<i>TAIFG</i>
	15	14	13	12	11	10	9	8	7	6	5	4	3

- Continuous mode

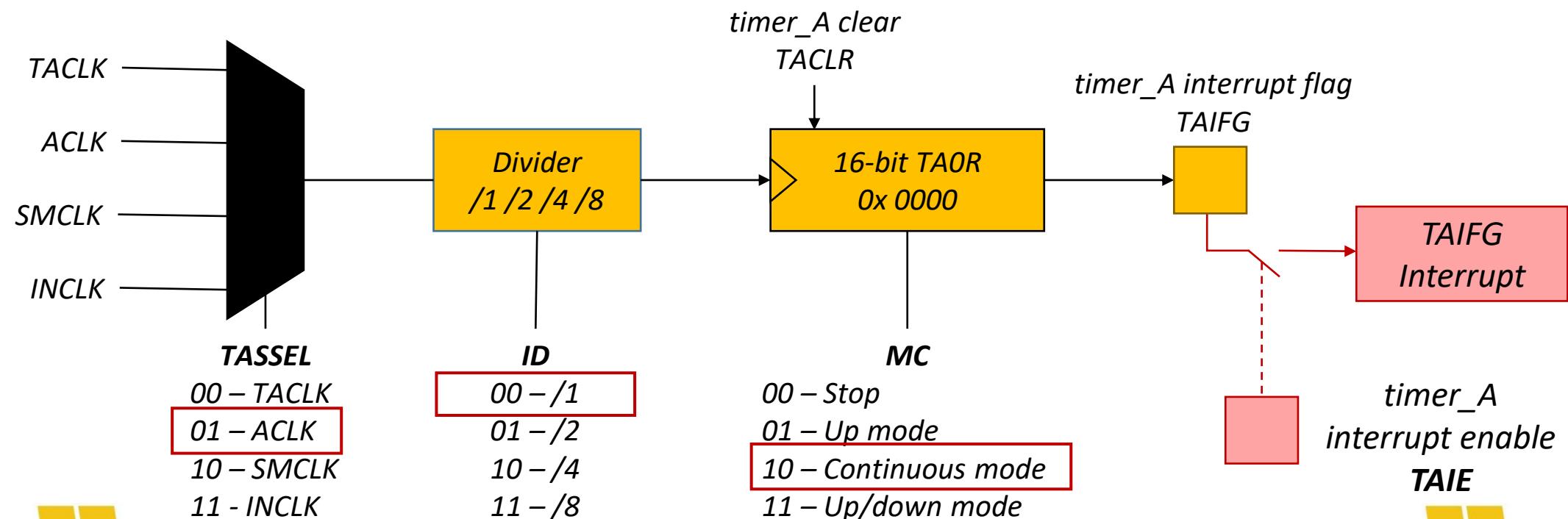


# Timer Interrupt in MSP430

- For Timer\_A

TAOCTL	rsvd.						TASSEL	ID	MC	rsvd.	TACLR	TAIE	TAIFG
	15	14	13	12	11	10	9	8	7	6	5	4	3

- Continuous mode





# Timer Interrupt in MSP430

- For Timer\_A

TAOCTL	rsvd.						TASSEL	ID	MC	rsvd.	TACLR	TAIE	TAIFG
	15	14	13	12	11	10	9	8	7	6	5	4	3

- Continuous mode

How does the MCU know which function to call for the TAIFG interrupt?

**IVT and its mapping.**

Note: There are several peripherals that can trigger interrupts.

```
void main(void)
{
    // Stop watchdog timer & clear Low Power
    // Configure P1.0 to output mode

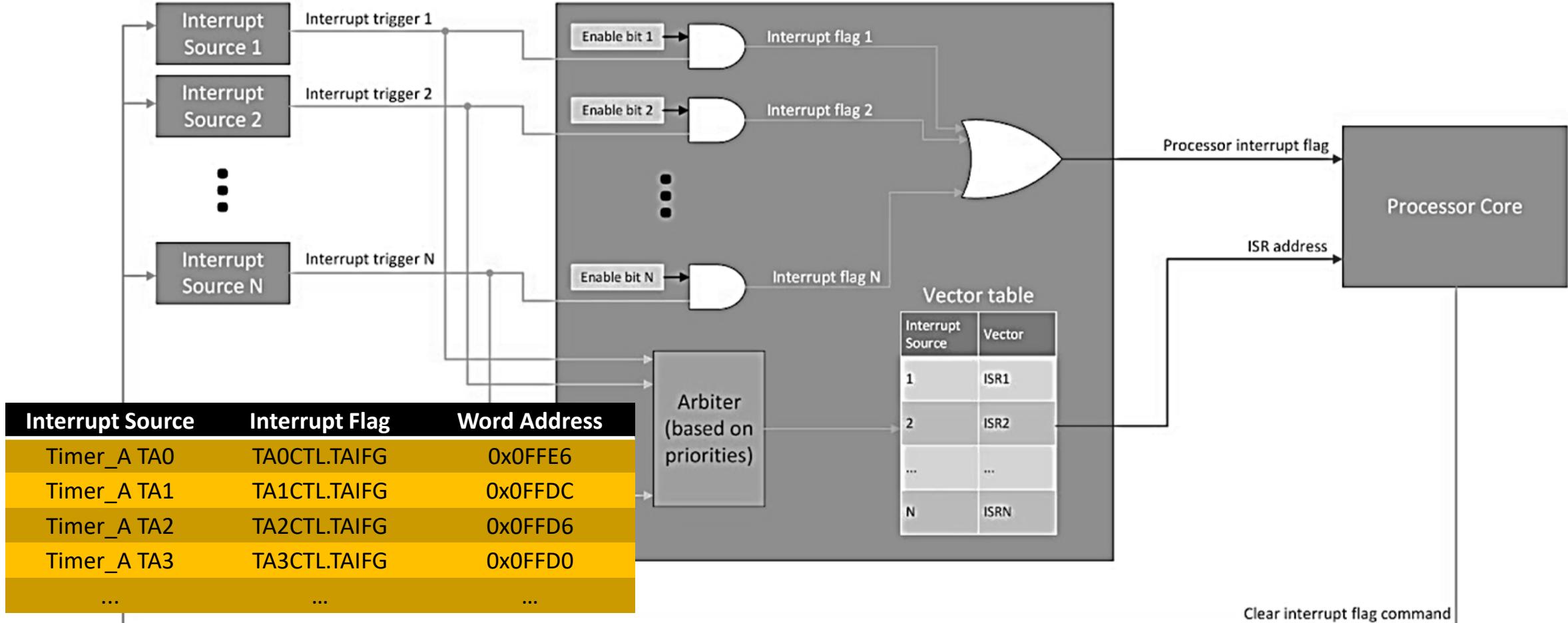
    // Configure timer_A0 to the following configuration
    // - ACLK clock as source, divide by 1
    // - continuous mode, clear the timer register
    // - enable TAIE interrupt, clear the TAIFG flag

    for(;;) {          // Infinite loop
    }

    void blink_ISR(void)
    {
        // Clear the interrupt flag
        // Toggle the LEDs
    }
}
```

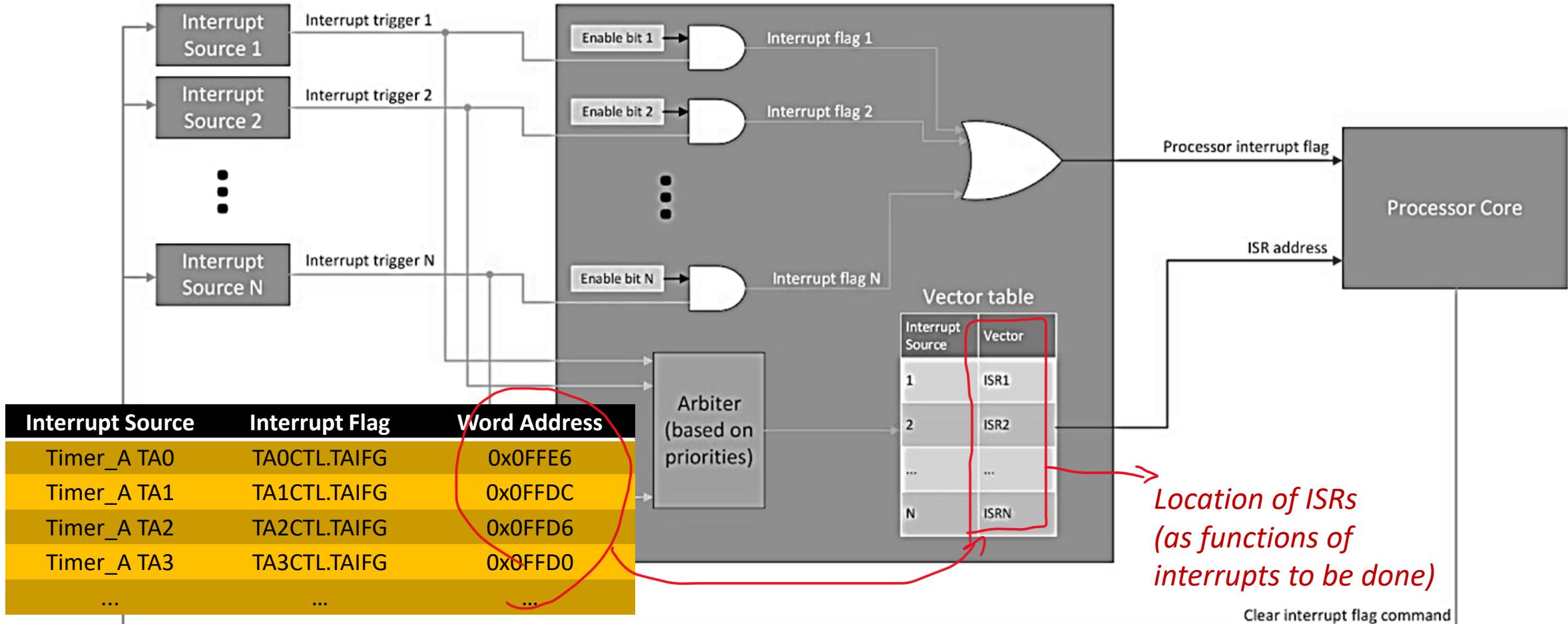
# Timer\_A Interrupt Details

- ISR Mapping Address



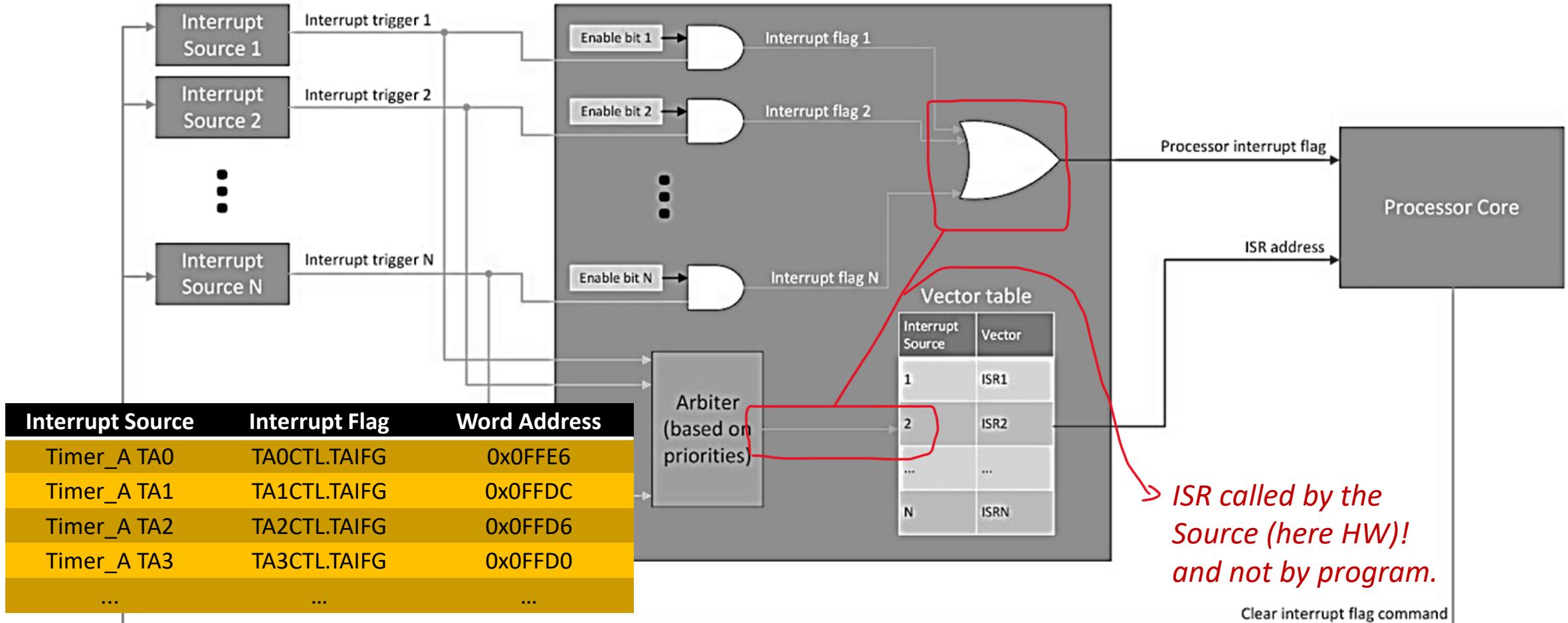
# Timer\_A Interrupt Details

- ISR Mapping Address



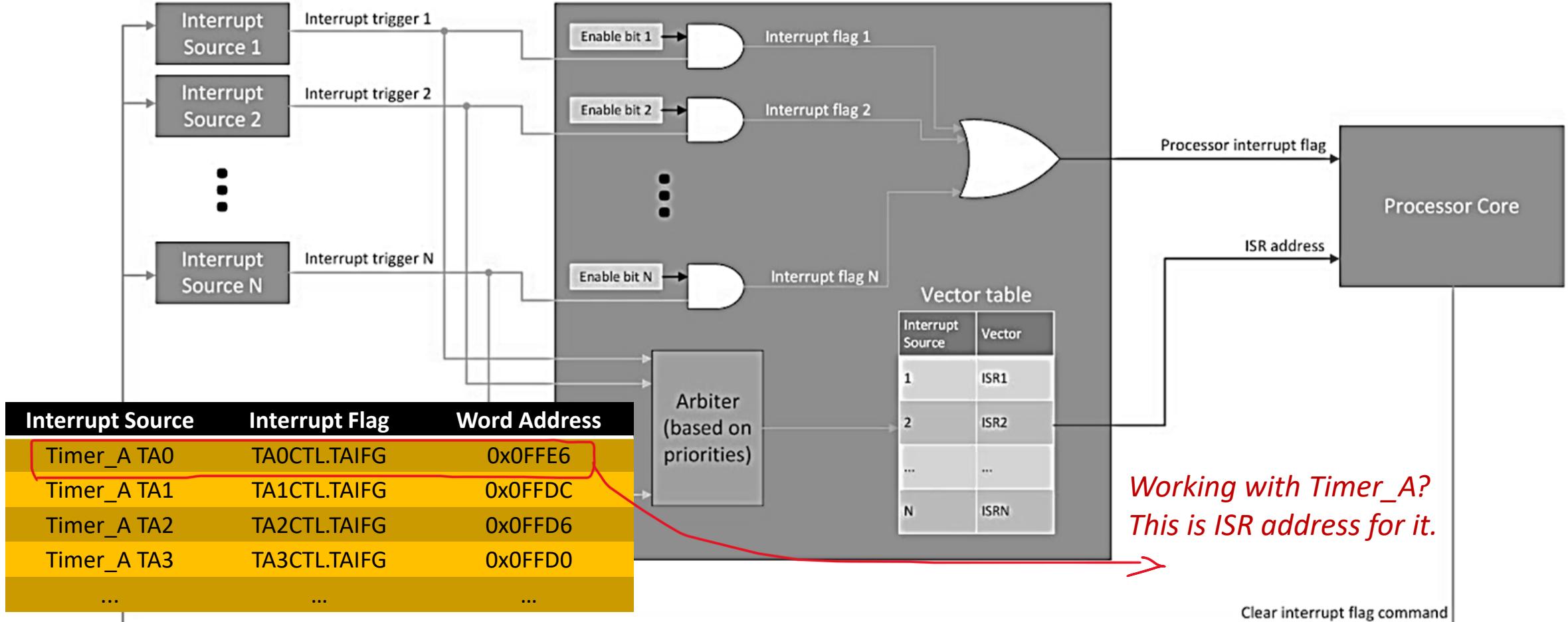
# Timer\_A Interrupt Details

- Enabling Timer\_A Interrupts



# Timer\_A Interrupt Details

- This is Actual Address





# ISRs' Addresses in MSP430

- For Timer\_A

Table 6-4. Interrupt Sources, Flags, and Vectors

INTERRUPT SOURCE	INTERRUPT FLAG	SYSTEM INTERRUPT	WORD ADDRESS	PRIORITY
System Reset Power up, Brownout, Supply Supervisor External Reset RST Watchdog time-out (watchdog mode) WDT, FRCTL MPU, CS, PMM password violation	SVSHIFG PMMRSTIFG WDTIFG WDTPW, FRCTLPW, MPUPW, CSPW, PMMPW UBDIFG MPUSEGIIFG, MPUSEG1IFG, MPUSEG2IFG,	Reset	0FFEh	Highest
eUSCI_B0 receive or transmit	UCSTPIFG, UCRXIFG0, UCTXIFG0, UCRXIFG1, UCTXIFG1, UCRXIFG2, UCTXIFG2, UCRXIFG3, UCTXIFG3, UCCNTIFG, UCBIT9IFG ( $I^2C$ mode) (UCB0IV) <sup>(1)</sup>	Maskable	0FFECh	
ADC12_B	ADC12IFG0 to ADC12IFG31 ADC12LOIFG, ADC12INIFG, ADC12HIIIFG, ADC12RDYIFG, ADC12OVIFG, ADC12TOVIFG (ADC12IV) <sup>(1)</sup>	Maskable	0FFEAh	
Timer_A TA0	TA0CCR0.CCIFG	Maskable	0FFE8h	
Timer_A TA0	TA0CCR1.CCIFG to TA0CCR2.CCIFG, <u>TA0CTL.TAIFG</u> (TA0IV) <sup>(1)</sup>	Maskable	0FFE6h	
eUSCI_A1 receive or transmit	UCA1IFG:UCRXIFG, UCTXIFG (SPI mode) UCA1IFG:UCSTTIIFG, UCTXCPТИFG, UCRXIFG, UCTXIFG (UART mode) (UCA1IV) <sup>(1)</sup>	Maskable	0FFE4h	



# ISRs' Addresses in MSP430

- For Timer\_A

Table 6-4. Interrupt Sources, Flags, and Vectors

INTERRUPT SOURCE	INTERRUPT FLAG	SYSTEM INTERRUPT	WORD ADDRESS	PRIORITY
System Reset Power up, Brownout, Supply Supervisor Watchdog time-out (watchdog mode) WDT, FRCTL MPU, CS, PMM password violation	SVSHIFG WDTPW, FRCTLPW, MPUPW, CSPW, PMMPW UBDIFG MPUSEGIIFG, MPUSEG1IFG, MPUSEG2IFG, UCTXIFG1, UCTXIFG2, UCTXIFG3, UCRXIFG3, UCTXIFG3, UCNTIFG, UCBIT9IFG (I <sup>2</sup> C mode) (UCB0IV) <sup>(1)</sup>	Reset	0FFEh	Highest
ADC12_B	ADC12IFG0 to ADC12IFG31 ADC12LOIFG, ADC12INIFG, ADC12HIIIFG, ADC12RDYIFG, ADC12OVIFG, ADC12TOVIFG (ADC12IV) <sup>(1)</sup>	Maskable	0FFEAh	
Timer_A TA0	TA0CCR0.CCIFG	Maskable	0FFE8h	
Timer_A TA0	TA0CCR1.CCIFG to TA0CCR2.CCIFG, <u>TA0CTL.TAIFG</u> (TA0IV) <sup>(1)</sup>	Maskable	0FFE6h	
eUSCI_A1 receive or transmit	UCA1IFG:UCRXIFG, UCTXIFG (SPI mode) UCA1IFG:UCSTTIIFG, UCTXCPTIIFG, UCRXIFG, UCTXIFG (UART mode) (UCA1IV) <sup>(1)</sup>	Maskable	0FFE4h	

(Q) Where can we find this information? Which document?

(A) In the device datasheet. Because the number of timers and the exact location of the interrupt service routines are specific to the device.

# Writing Code for Timer Interrupt

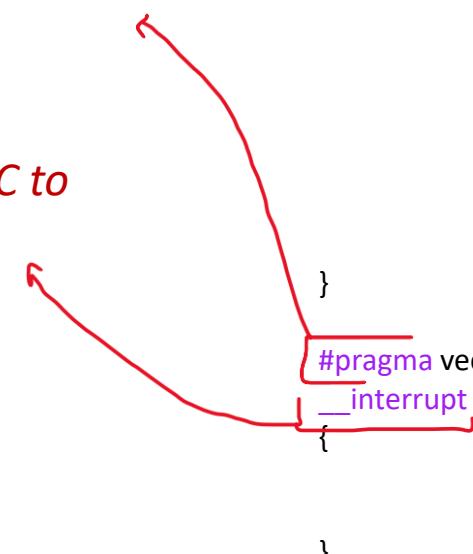
- For Timer\_A

TAOCTL	rsvd.						TASSEL	ID	MC	rsvd.	TACLR	TAIE	TAIFG
	15	14	13	12	11	10	9	8	7	6	5	4	3

- Continuous mode

tells the compiler that the function that follows is to be associated with a specific hardware interrupt vector. Here it is **Timer0\_A1**.

**\_interrupt** is a keyword used in MSP430 C to tell the compiler that this function is an interrupt service routine (ISR),



```
void main(void)
{
    // Stop watchdog timer & clear Low Power
    // Configure P1.0 to output mode

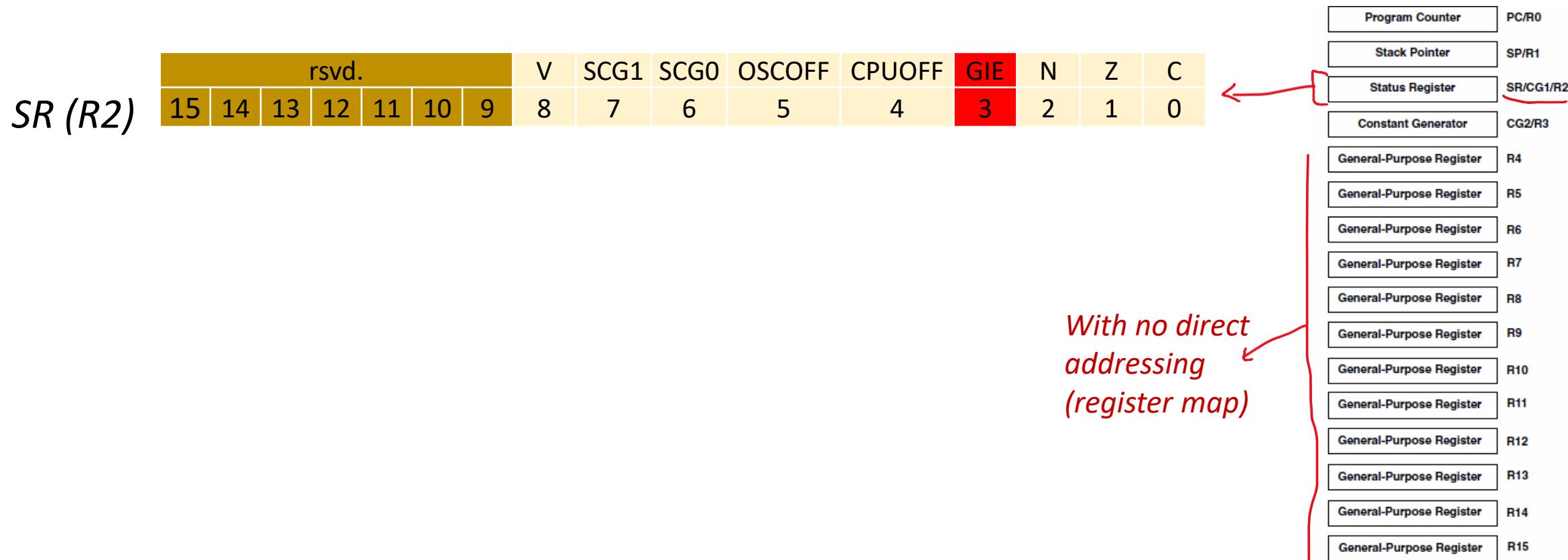
    // Configure timer_A0 to the following configuration
    // - ACLK clock as source, divide by 1
    // - continuous mode, clear the timer register
    // - enable TAIE interrupt, clear the TAIFG flag

    for(;;) {           // Infinite loop
    }

    #pragma vector = TIMER0_A1_VECTOR      // preprocessor directive
    _interrupt void blink_ISR(void)
    {
        // Clear the interrupt flag
        // Toggle the LEDs
    }
}
```

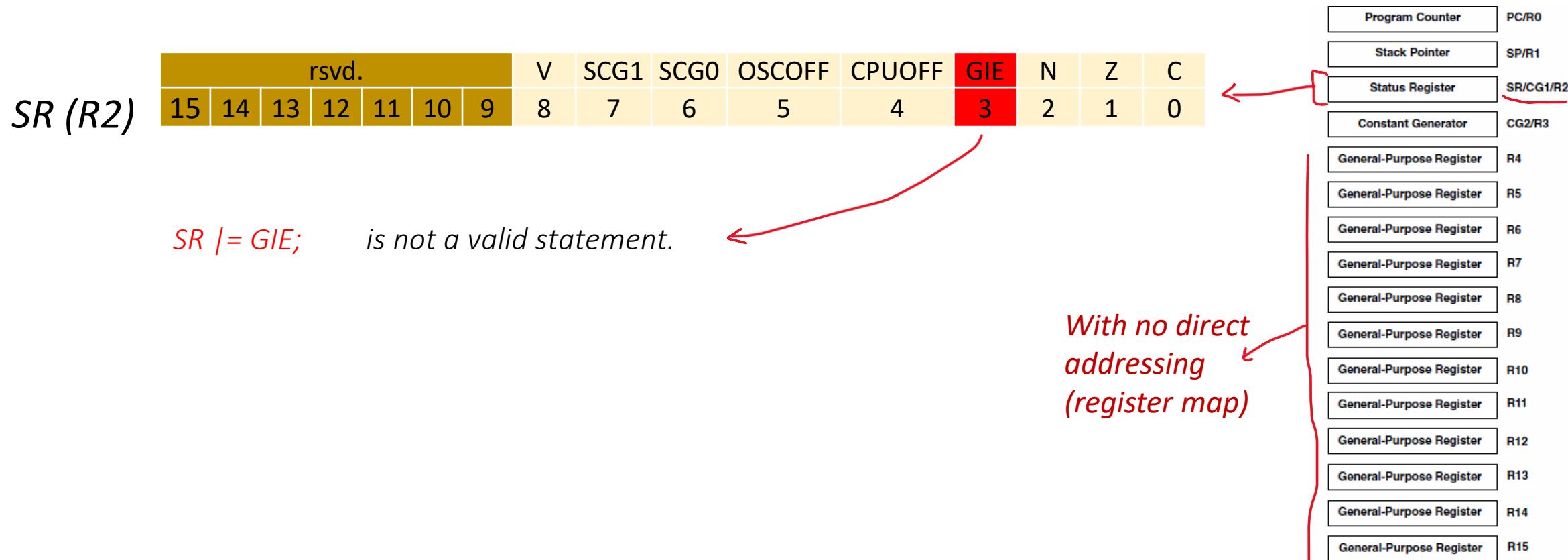
# Enabling Interrupts in MSP430

- Using General Interrupt Enable (GIE)



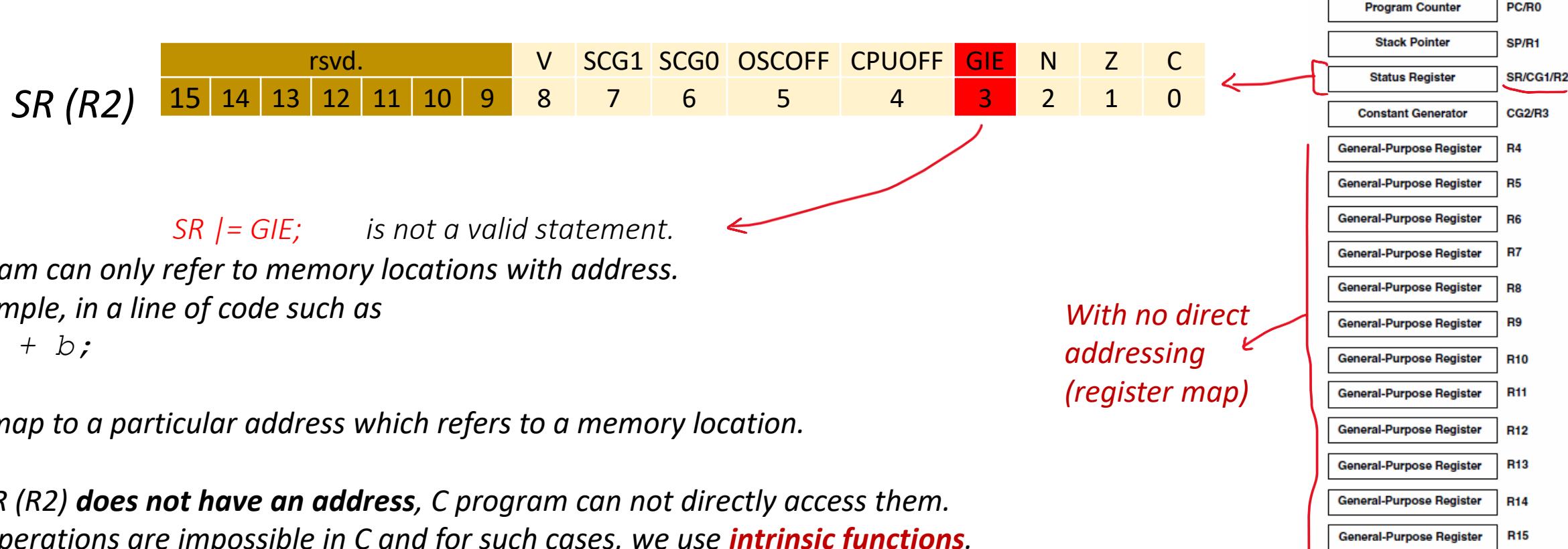
# Enabling Interrupts in MSP430

- Using General Interrupt Enable (GIE)



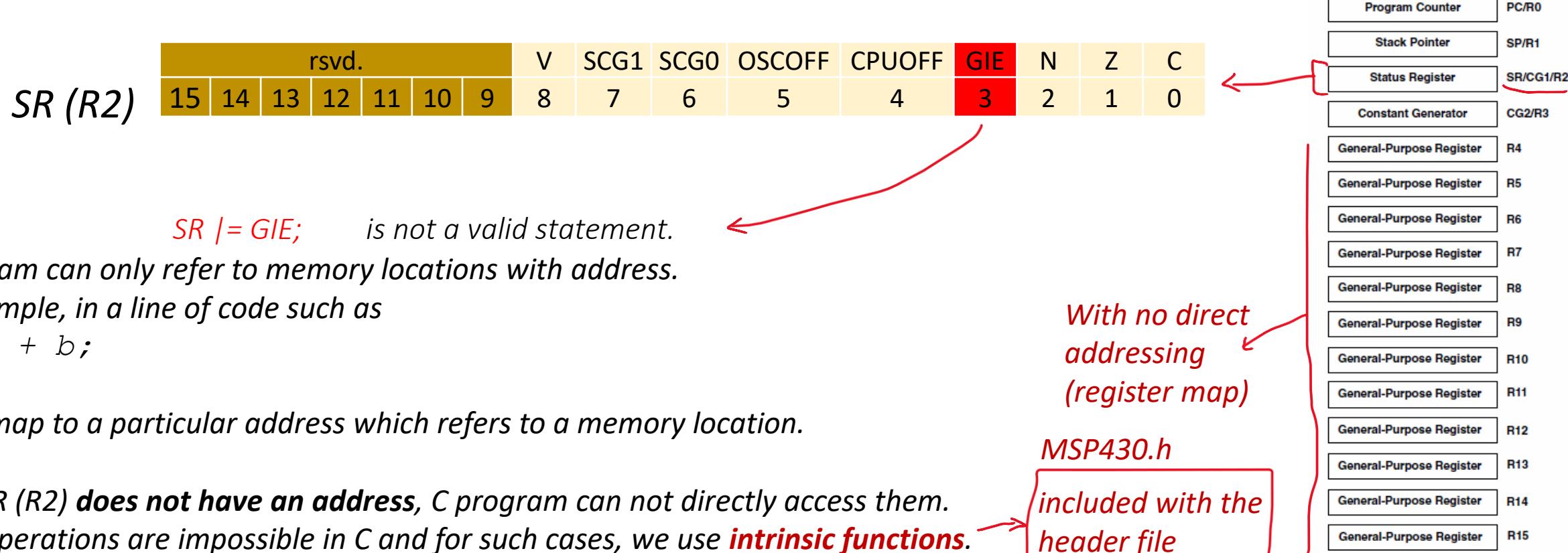
# Enabling Interrupts in MSP430

- Using General Interrupt Enable (GIE)



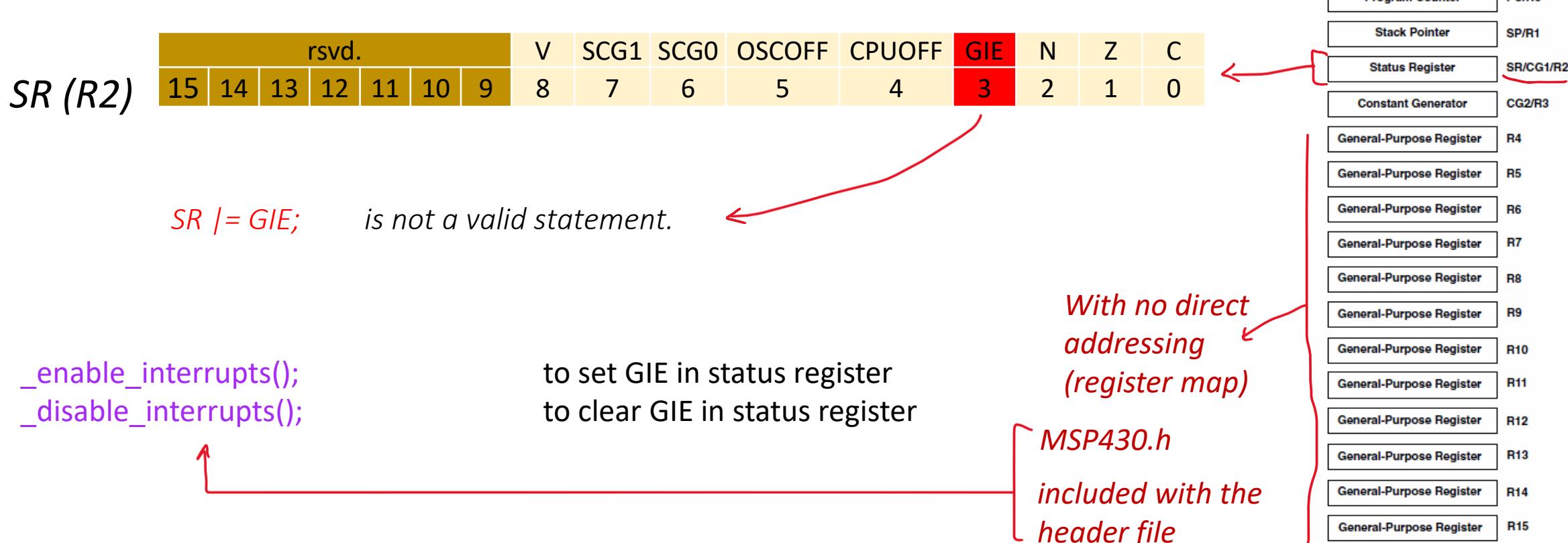
# Enabling Interrupts in MSP430

- Using General Interrupt Enable (GIE)



# Enabling Interrupts in MSP430

- Using General Interrupt Enable (GIE)





# Writing Code for Timer Interrupt

- For Timer\_A

TAOCTL	rsvd.						TASSEL	ID	MC	rsvd.	TACLR	TAIE	TAIFG		
	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1

- Continuous mode

```
// Code that flashes the red LED using timer
#include <msp430fr6989.h>
#define redLED BIT0 // Red LED at P1.0
void main(void)
{
    // Stop watchdog timer
    // Clear Low Power
    // Configure P1.0 to output mode
    // Turn off LED

    // Configure timer_A0 to the following configuration
    // - ACLK clock as source, divide by 1
    // - continuous mode, clear the timer register
    // - enable TAIE interrupt, clear the TAIFG flag
    // - enable GIE using intrinsic functions

    for(;;) {} // Infinite loop
}

#pragma vector = TIMERO_A1_VECTOR // preprocessor directive
__interrupt void blink_ISR(void) {
    // Clear the interrupt flag
    // Toggle the LEDs
}
```



# Writing Code for Timer Interrupt

- For Timer\_A

TAOCTL	rsvd.						TASSEL	ID	MC	rsvd.	TACLR	TAIE	TAIFG		
	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1

- Continuous mode

```
// Code that flashes the red LED using timer
#include <msp430fr6989.h>
#define redLED BIT0 // Red LED at P1.0
void main(void)
{
    WDTCTL = WDTPW | WDTHOLD; // Stop watchdog timer
    PM5CTL0 &= ~LOCKLPM5; // Clear Low Power
    // Configure P1.0 to output mode
    // Turn off LED

    // Configure timer_A0 to the following configuration
    // - ACLK clock as source, divide by 1
    // - continuous mode, clear the timer register
    // - enable TAIE interrupt, clear the TAIFG flag
    // - enable GIE using intrinsic functions

    for(;;) {} // Infinite loop
}

#pragma vector = TIMERO_A1_VECTOR // preprocessor directive
__interrupt void blink_ISR(void) {
    // Clear the interrupt flag
    // Toggle the LEDs
}
```



# Writing Code for Timer Interrupt

- For Timer\_A

TAOCTL	rsvd.						TASSEL	ID	MC	rsvd.	TACLR	TAIE	TAIFG		
	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1

- Continuous mode

```
// Code that flashes the red LED using timer
#include <msp430fr6989.h>
#define redLED BIT0 // Red LED at P1.0
void main(void)
{
    WDTCTL = WDTPW | WDTHOLD; // Stop watchdog timer
    PM5CTL0 &= ~LOCKLPM5; // Clear Low Power
    P1DIR |= LED; // Configure P1.0 to output mode
    P1OUT &= ~LED; // Turn off LED

    // Configure timer_A0 to the following configuration
    // - ACLK clock as source, divide by 1
    // - continuous mode, clear the timer register
    // - enable TAIE interrupt, clear the TAIFG flag
    // - enable GIE using intrinsic functions

    for(;;) {} // Infinite loop
}

#pragma vector = TIMERO_A1_VECTOR // preprocessor directive
__interrupt void blink_ISR(void) {
    // Clear the interrupt flag
    // Toggle the LEDs
}
```



# Writing Code for Timer Interrupt

- For Timer\_A

TAOCTL	rsvd.						TASSEL	ID	MC	rsvd.	TACLR	TAIE	TAIFG		
	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1

- Continuous mode

```
// Code that flashes the red LED using timer
#include <msp430fr6989.h>
#define redLED BIT0 // Red LED at P1.0
void main(void)
{
    WDTCTL = WDTPW | WDTHOLD; // Stop watchdog timer
    PM5CTL0 &= ~LOCKLPM5; // Clear Low Power
    P1DIR |= LED; // Configure P1.0 to output mode
    P1OUT &= ~LED; // Turn off LED

    TAOCTL = TASSEL_1 | ID_0 | MC_2 | TACLR | TAIE; // Configure timer, ACLK clock as source, divide by 1
                                                       // continuous, clear TAOR, enable TAIE interrupt
    TAOCTL &= ~TAIFG; // clear the TAIFG flag
    _enable_interrupts(); // enable GIE using intrinsic functions

    for(;;) {} // Infinite loop
}

#pragma vector = TIMERO_A1_VECTOR // preprocessor directive
__interrupt void blink_ISR(void) {
    // Clear the interrupt flag
    // Toggle the LEDs
}
```



# Writing Code for Timer Interrupt

- For Timer\_A

TAOCTL	rsvd.						TASSEL	ID	MC	rsvd.	TACLR	TAIE	TAIFG		
	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1

- Continuous mode

```
// Code that flashes the red LED using timer
#include <msp430fr6989.h>
#define redLED BIT0 // Red LED at P1.0
void main(void)
{
    WDTCTL = WDTPW | WDTHOLD; // Stop watchdog timer
    PM5CTL0 &= ~LOCKLPM5; // Clear Low Power
    P1DIR |= LED; // Configure P1.0 to output mode
    P1OUT &= ~LED; // Turn off LED

    TAOCTL = TASSEL_1 | ID_0 | MC_2 | TACLR | TAIE; // Configure timer, ACLK clock as source, divide by 1
                                                       // continuous, clear TAOR, enable TAIE interrupt
    TAOCTL &= ~TAIFG; // clear the TAIFG flag
    _enable_interrupts(); // enable GIE using intrinsic functions

    for(;;) {} // Infinite loop
}

#pragma vector = TIMERO_A1_VECTOR // preprocessor directive
__interrupt void blink_ISR(void) {
    TAOCTL &= ~TAIFG; // Clear the interrupt flag
    P1OUT ^= LED; // Toggle the LEDs
}
```

# Writing Code for Timer Interrupt

- For Timer\_A

TAOCTL	rsvd.						TASSEL	ID	MC	rsvd.	TACLR	TAIE	TAIFG			
	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0

- Continuous mode

Interrupt will not be triggered,  
if the flag is not cleared!!

```

// Code that flashes the red LED using timer
#include <msp430fr6989.h>
#define redLED BIT0
void main(void)
{
    WDTCTL = WDTPW | WDTHOLD;           // Stop watchdog timer
    PM5CTL0 &= ~LOCKLPM5;             // Clear Low Power
    P1DIR |= LED;                     // Configure P1.0 to output mode
    P1OUT &= ~LED;                   // Turn off LED

    TAOCTL = TASSEL_1 | ID_0 | MC_2 | TACLR | TAIE; // Configure timer, ACLK clock as source, divide by 1
                                                    // continuous, clear TAOR, enable TAIE interrupt
    TAOCTL &= ~TAIFG;                // clear the TAIFG flag
    __enable_interrupt();            // enable GIE using intrinsic functions

    for(;;) {}                      // Infinite loop

    #pragma vector = TIMERO_A1_VECTOR // preprocessor directive
    __interrupt void blink_ISR(void){ // Clear the interrupt flag
        TAOCTL &= ~TAIFG;          // Toggle the LEDs
        P1OUT ^= LED;
    }
}

```

# Writing Code for Timer Interrupt

- For Timer\_A

TAOCTL	rsvd.						TASSEL	ID	MC	rsvd.	TACLR	TAIE	TAIFG		
	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1

- Continuous mode

When the timer register TAOR counts from 0xFFFF to 0x0000, TAIFG flag is set.

If the GIE is set AND if the TAIE is set, then when TAIFG flag is raised, an interrupt is triggered.

The interrupt service routine to be executed when the interrupt is triggered is defined by using the preprocessor #pragma directive.

The interrupt flag must be cleared, so that the next interrupt occur properly. *Failing to clear the flag, will trigger the interrupt indefinitely.*

```

// Code that flashes the red LED using timer
#include <msp430fr6989.h>
#define redLED BIT0
void main(void)
{
    WDTCTL = WDTPW | WDTHOLD;           // Stop watchdog timer
    PM5CTL0 &= ~LOCKLPM5;             // Clear Low Power
    P1DIR |= LED;                     // Configure P1.0 to output mode
    P1OUT &= ~LED;                   // Turn off LED

    TAOCTL = TASSEL_1 | ID_0 | MC_2 | TACLR | TAIE; // Configure timer, ACLK clock as source, divide by 1
                                                    // continuous, clear TAOR, enable TAIE interrupt
    TAOCTL &= ~TAIFG;                // clear the TAIFG flag
    __enable_interrupts();            // enable GIE using intrinsic functions

    for(;;) {}                      // Infinite loop
}

#pragma vector = TIMERO_A1_VECTOR // preprocessor directive
__interrupt void blink_ISR(void) {
    TAOCTL &= ~TAIFG;              // Clear the interrupt flag
    P1OUT ^= LED;                 // Toggle the LEDs
}

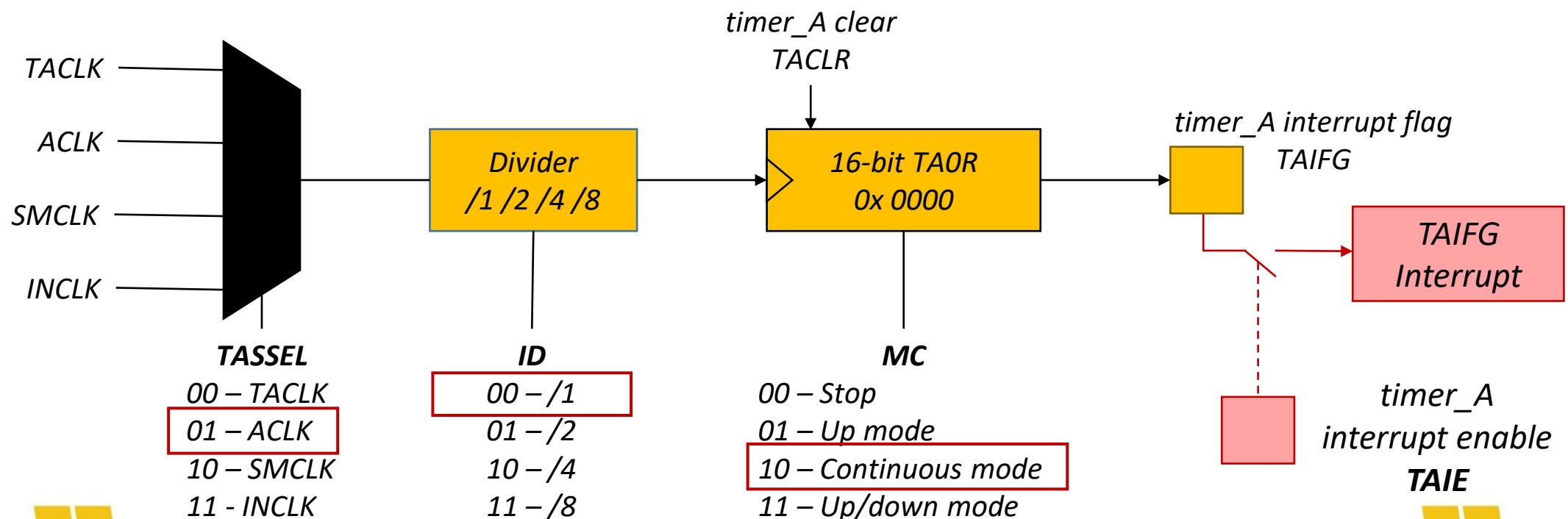
```

# Timer Interrupt in MSP430

- For Timer\_A

*What will happen if we switch the mode?  
From continuous to up mode!*

- Continuous mode

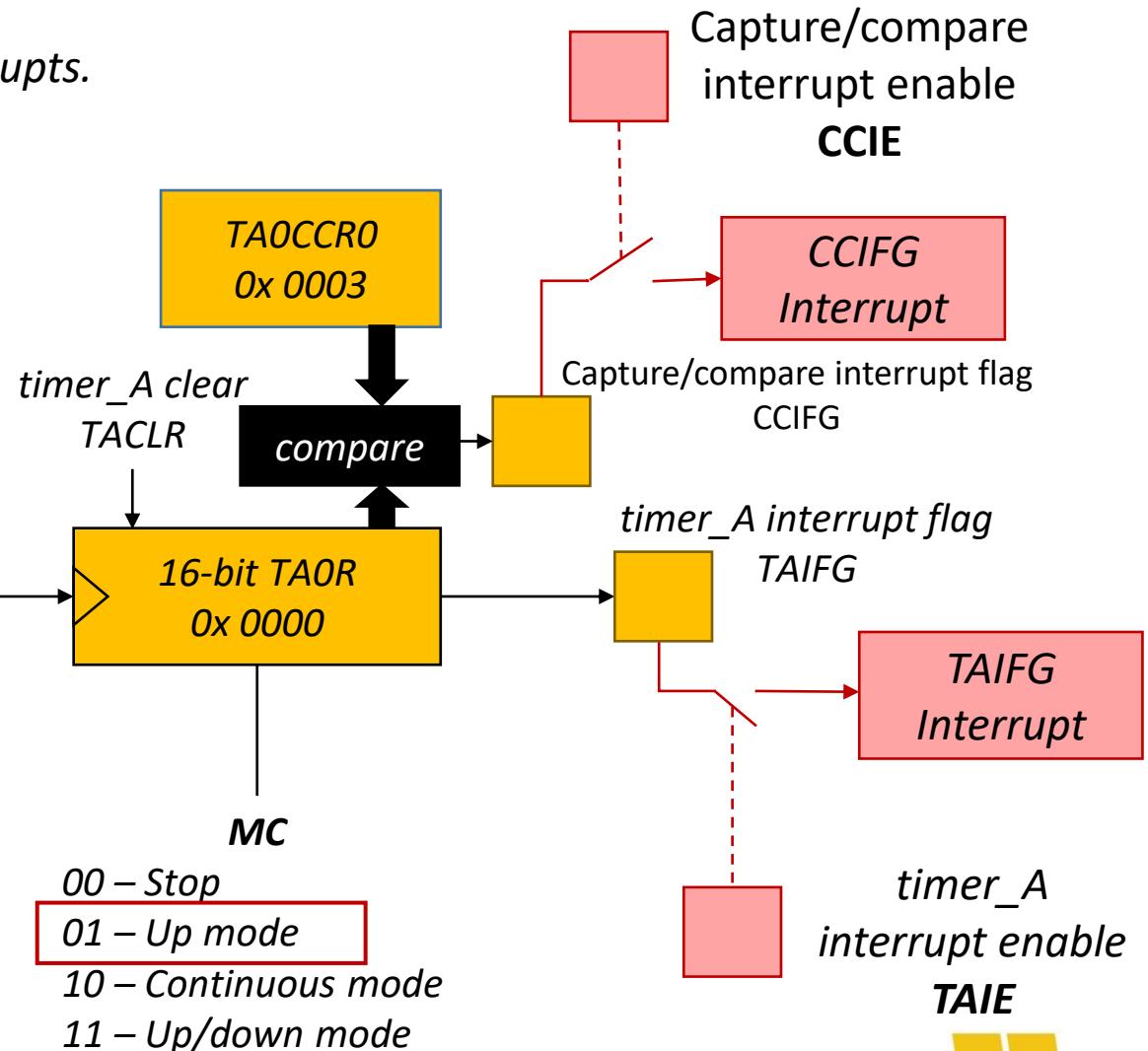
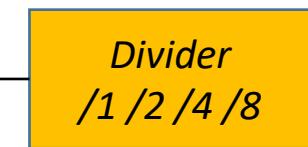
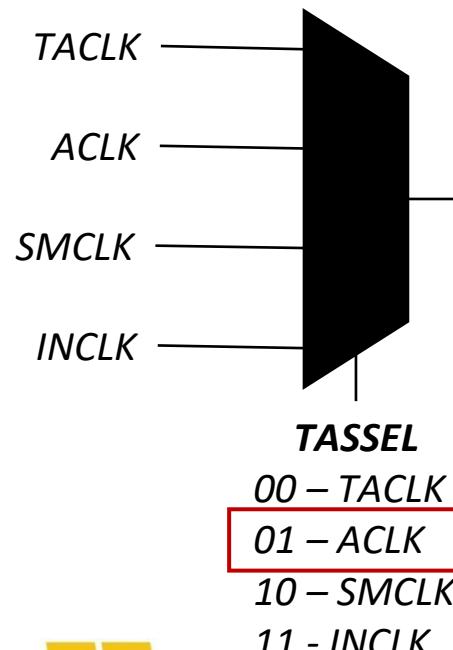


# Timer Interrupt in MSP430

- For Timer\_A

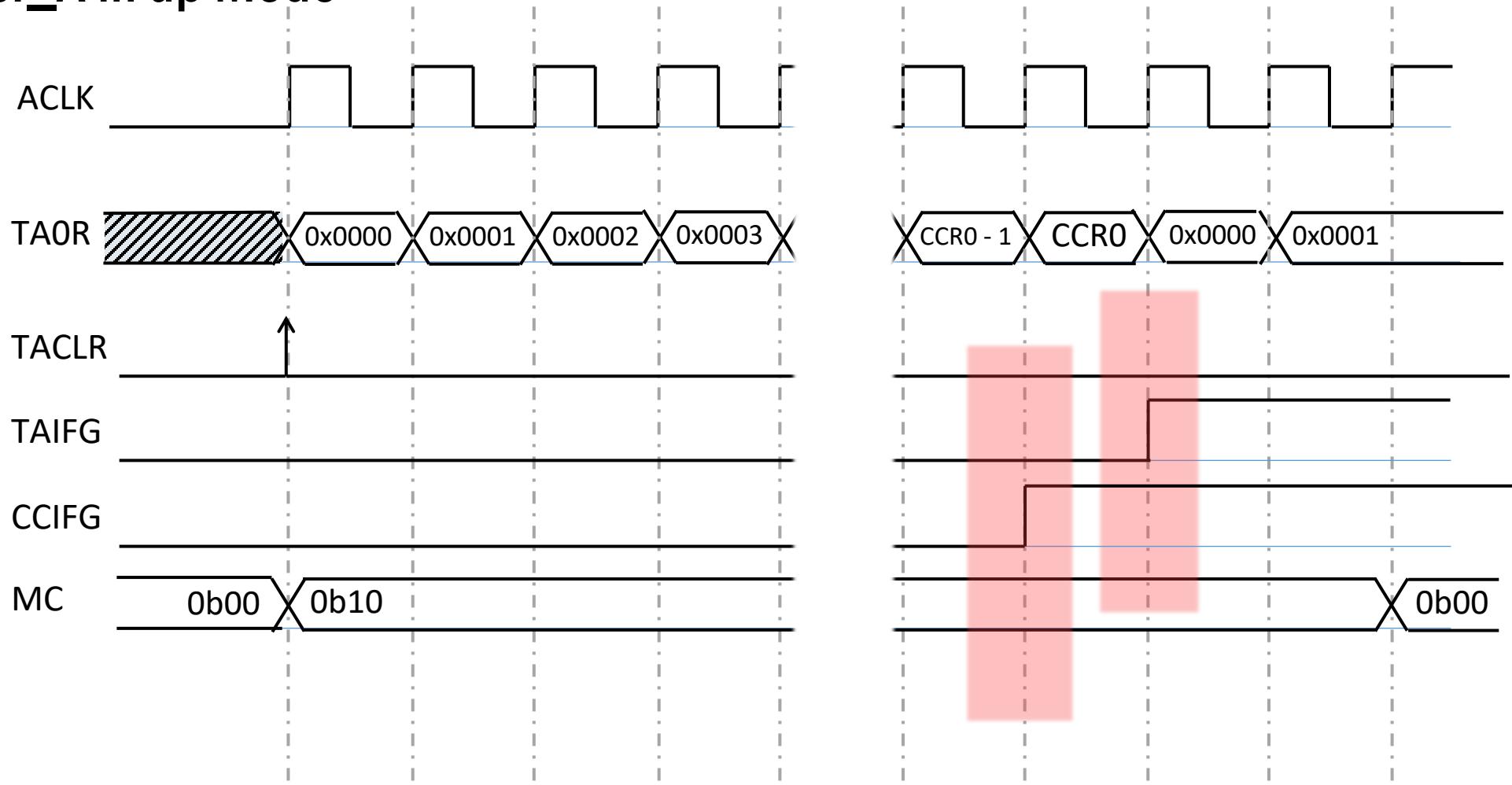
Now we have TWO interrupts.

- Up mode w/ CCIFG



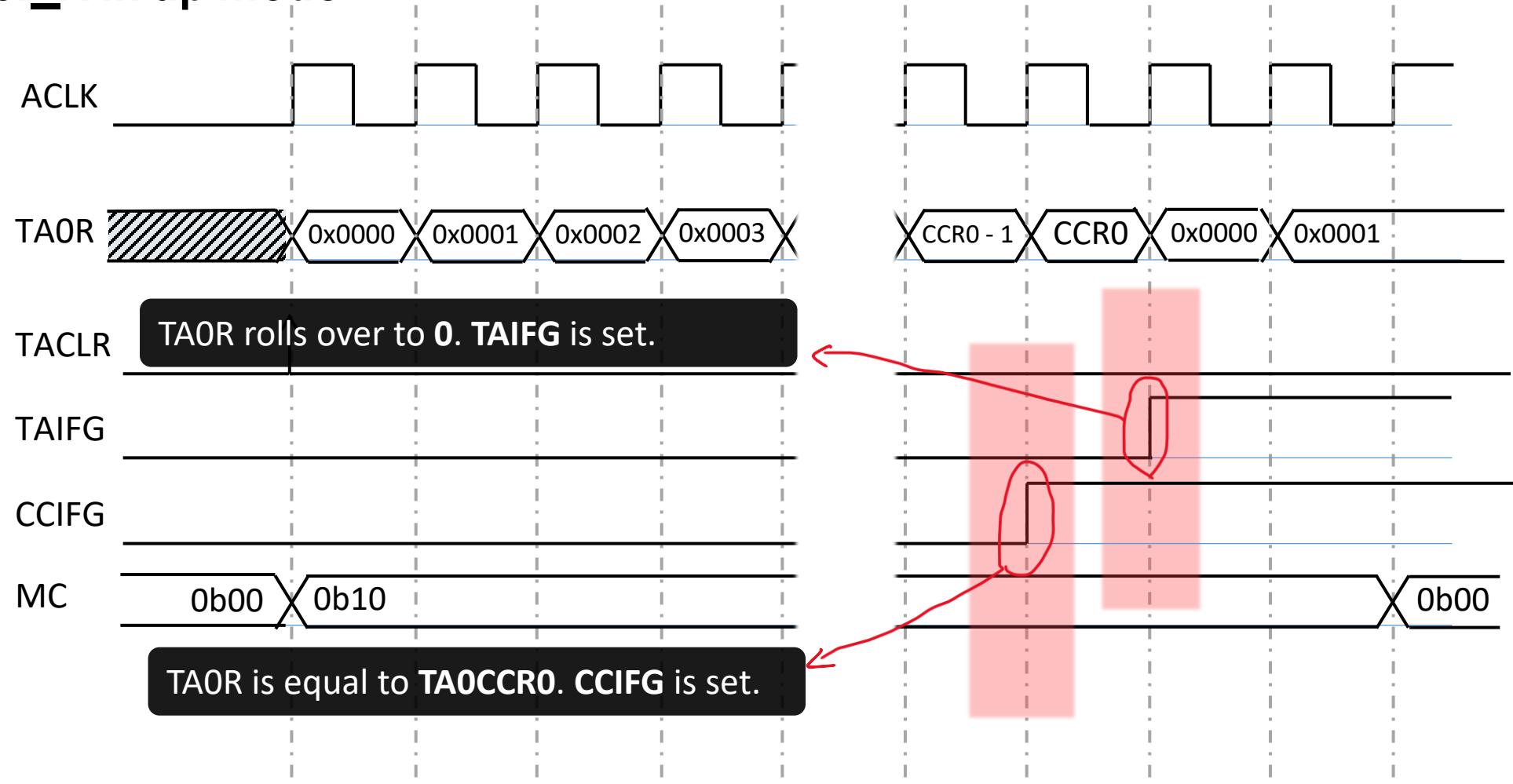
# Timer Interrupt in MSP430

- For Timer\_A in up Mode



# Timer Interrupt in MSP430

- For Timer\_A in up Mode

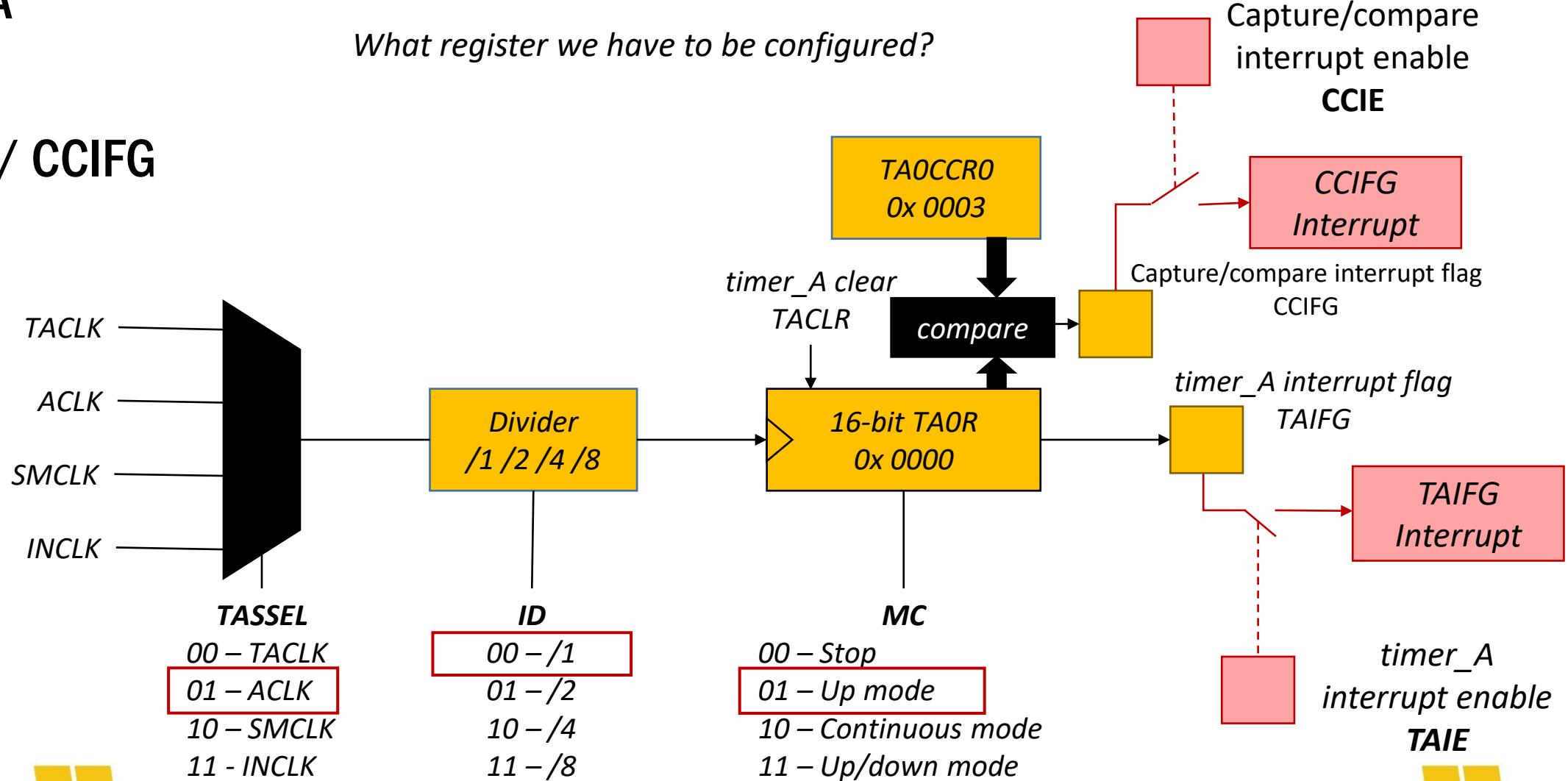


# Timer Interrupt in MSP430

- For Timer\_A

*What register we have to be configured?*

- Up mode w/ CCIFG

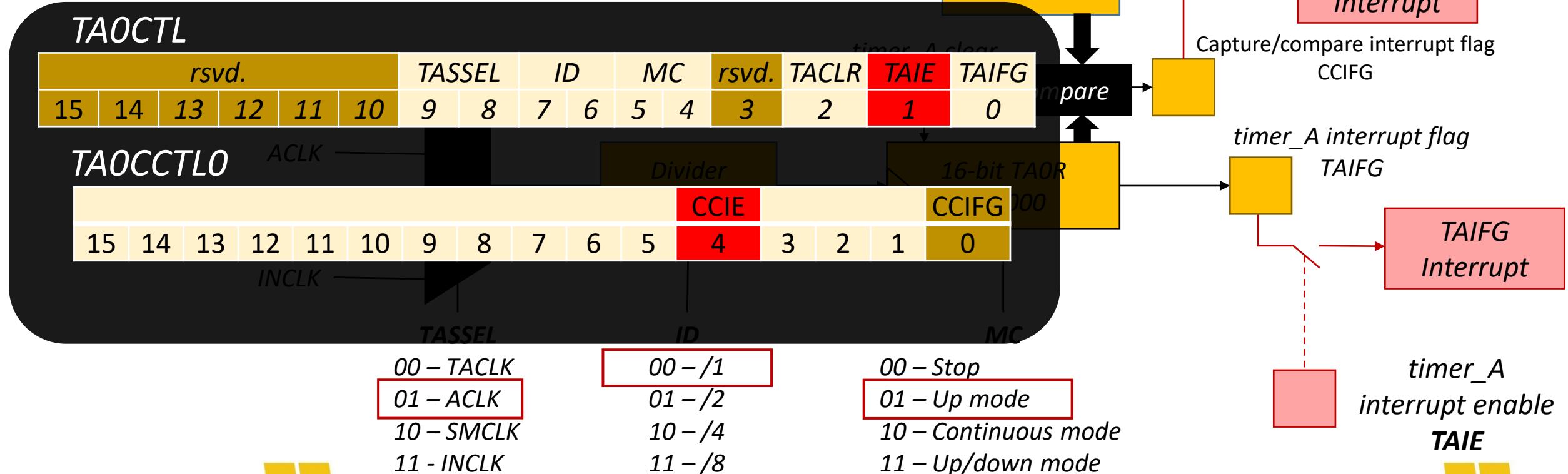


# Timer Interrupt in MSP430

- For Timer\_A

*What register we have to be configured?*

- Up mode w/ CCIFG





# Writing Code for Timer Interrupt

- For Timer\_A

rsvd.						TASSEL	ID	MC	rsvd.	TACLR	TAIE	TAIFG									CCIE	CCIFG									
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
TA0CTL										TA0CCTL0																					

- Up mode w/ CCIFG

Determining the config for up mode. (0.5 second)

No TAIE. Target for this example is CCIE

How many clock cycles does it take for the TAIFG flag to be raised?

How many clock cycles does it take for the CCIFG flag to be raised?

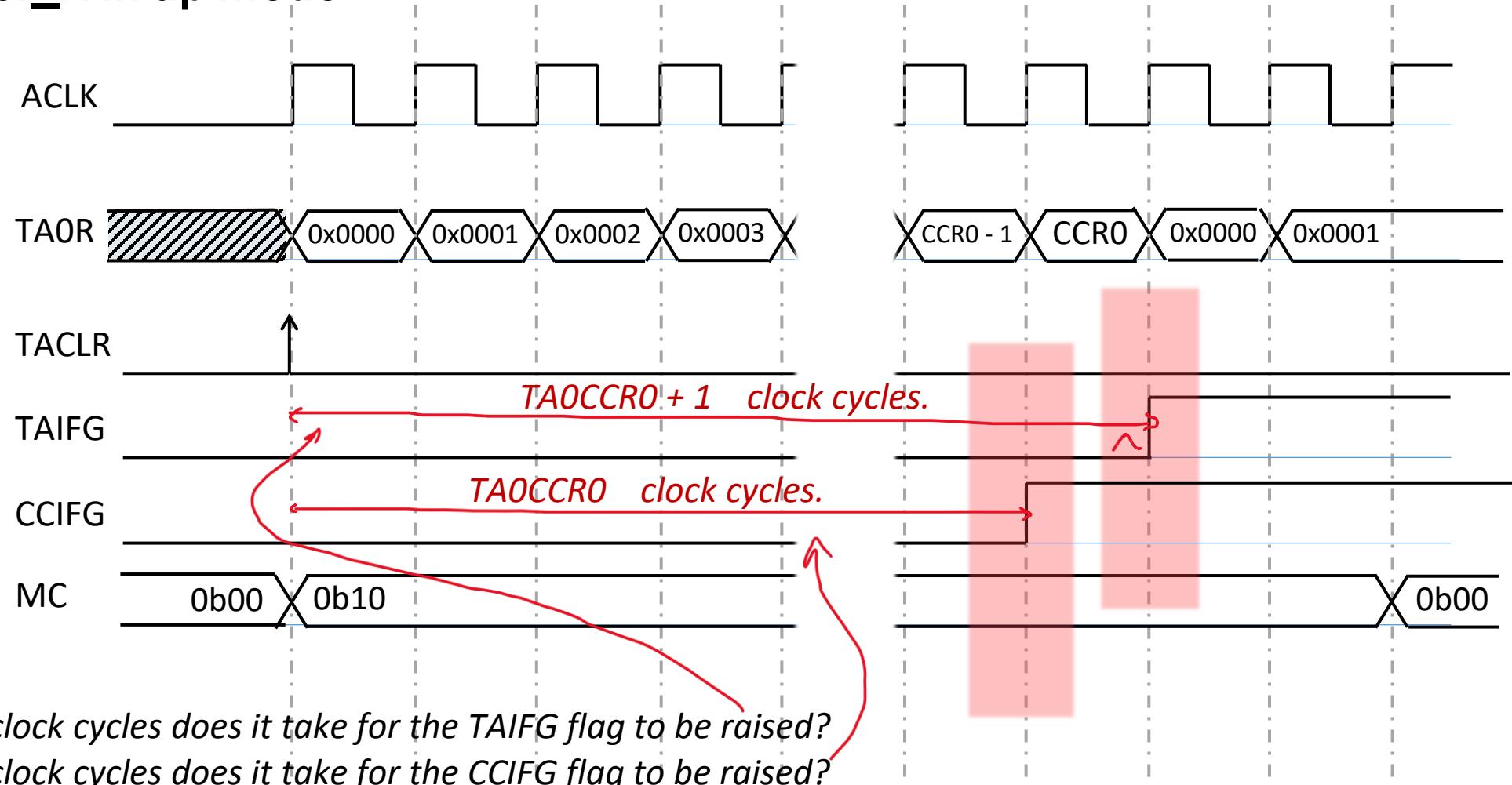
```
// Code that flashes the red LED using timer
#include <msp430fr6989.h>
#define redLED BIT0
void main(void)
{
    WDTCTL = WDTPW | WDTHOLD;           // Stop watchdog timer
    PM5CTL0 &= ~LOCKLPM5;             // Clear Low Power
    P1DIR |= LED;                     // Configure P1.0 to output mode
    P1OUT &= ~LED;                   // Turn off LED
    TA0CCR0 = 16384-1;                // Cycle of CCRO
    TA0CTL = TASSEL_1 | ID_0 | MC_2 | TACLR; TA0CCTL0 |= CCIE; // Configure timer, ACLK clock as source, divide by 1
                                                // continuous, clear TAOR, enable CCIE interrupt
    TA0CTL &= ~TAIFG;                // clear the TAIFG flag
    _enable_interrupts();            // enable GIE using intrinsic functions

    for(;;) {}                      // Infinite loop

#pragma vector = TIMERO_A1_VECTOR // preprocessor directive
__interrupt void blink_ISR(void) {
    TA0CCTL0 &= ~CCIFG;           // Clear the interrupt flag
    P1OUT ^= LED;                 // Toggle the LEDs
}
```

# Timer Interrupt in MSP430

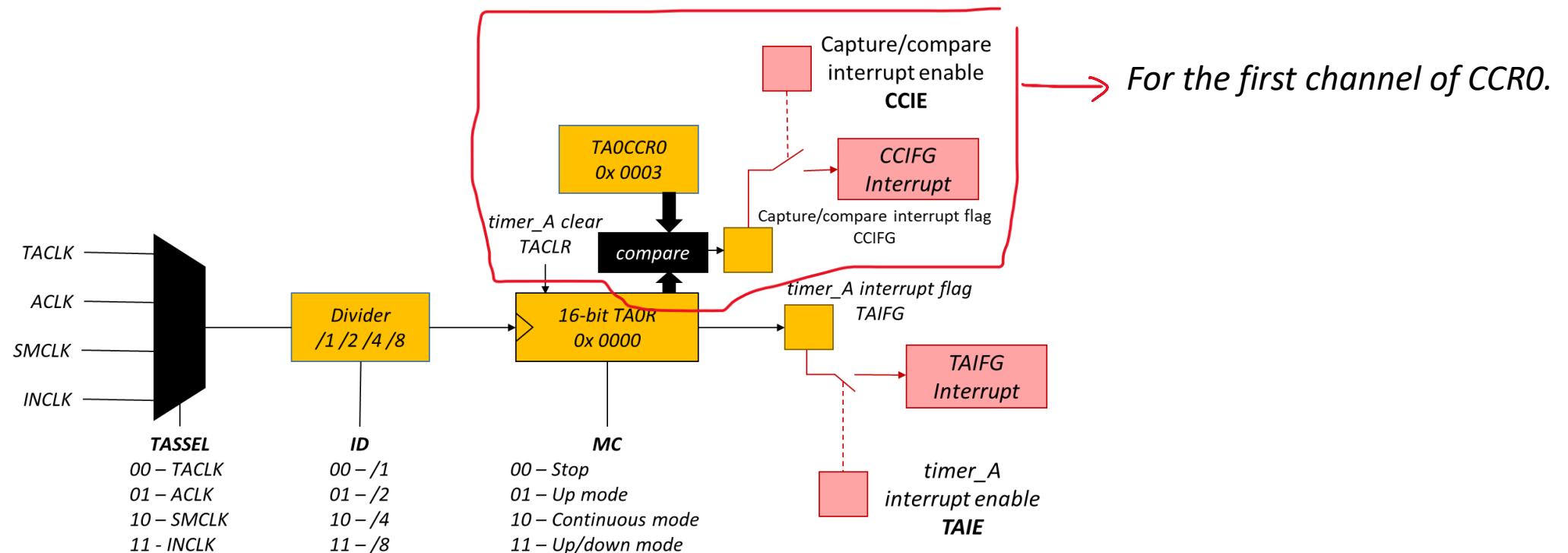
- For Timer\_A in up Mode



How many clock cycles does it take for the TAIFG flag to be raised?  
 How many clock cycles does it take for the CCIFG flag to be raised?

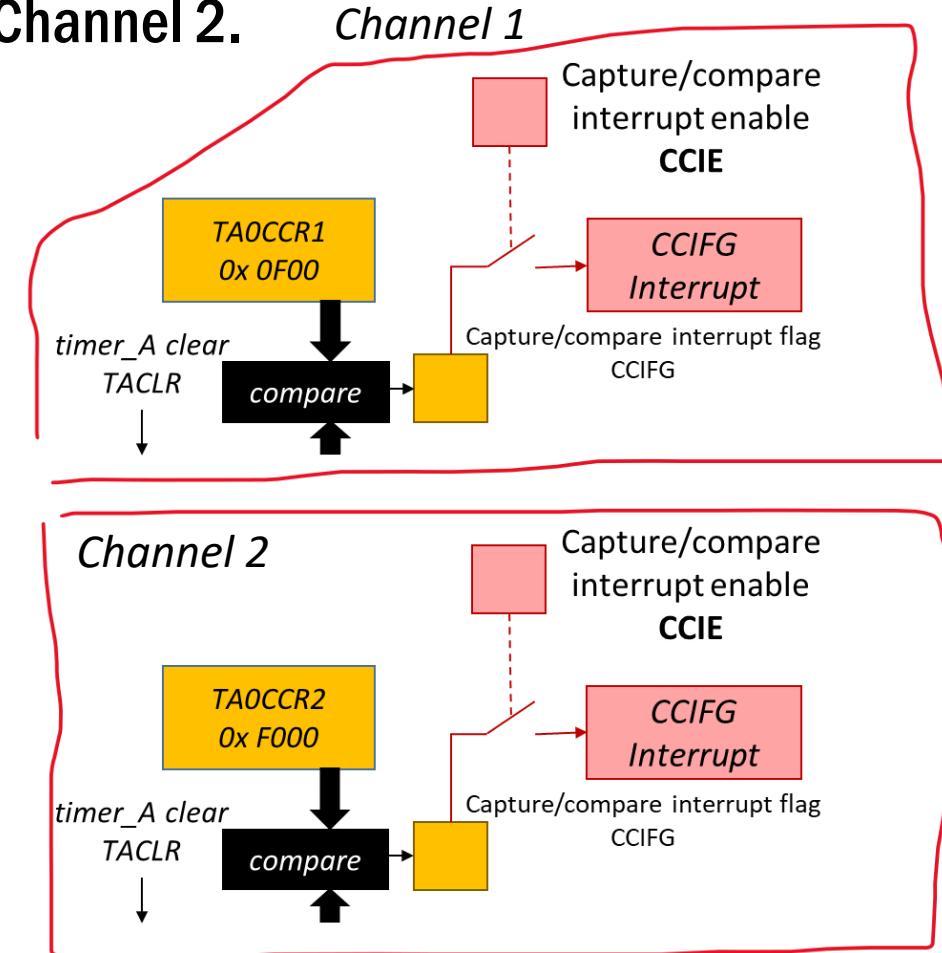
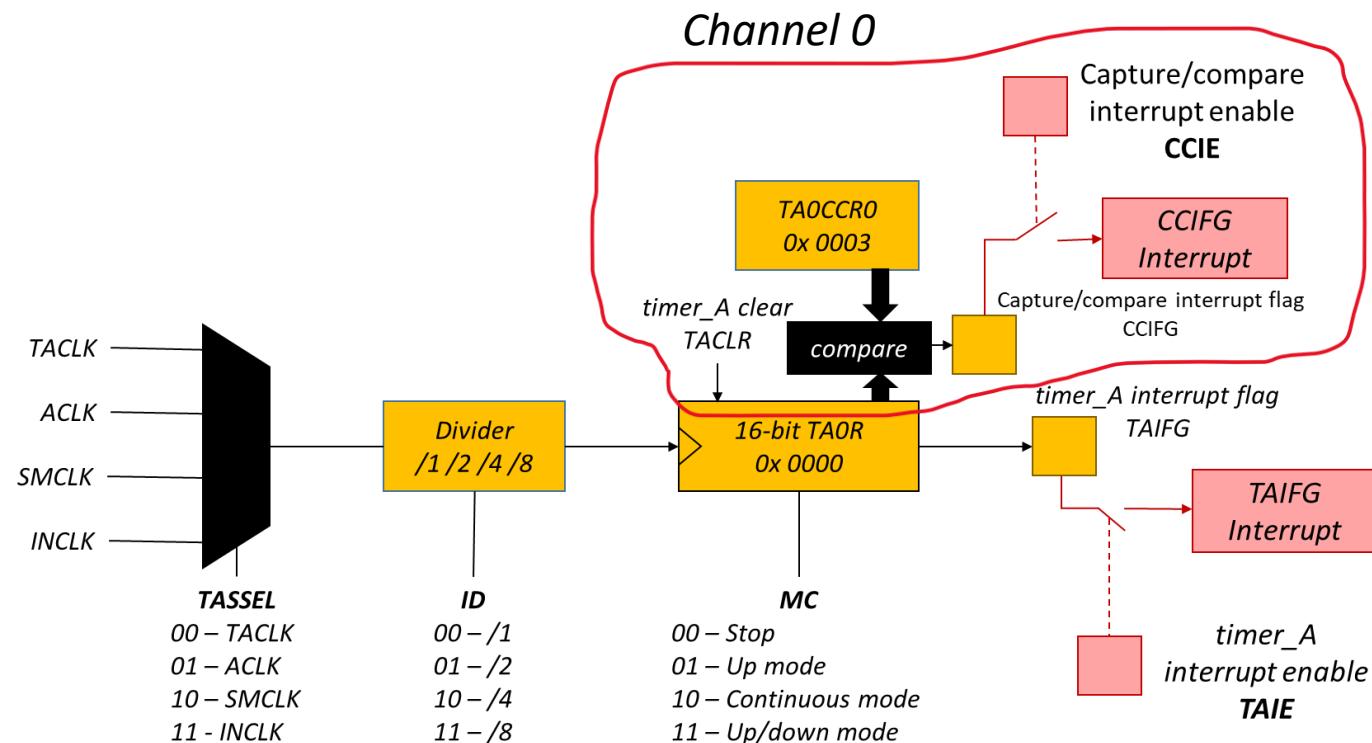
# Multiple Channels of Timer\_A0

- For Timer\_A
  - In Timer\_A0, similar to Channel 0, it has Channel1 and Channel 2.



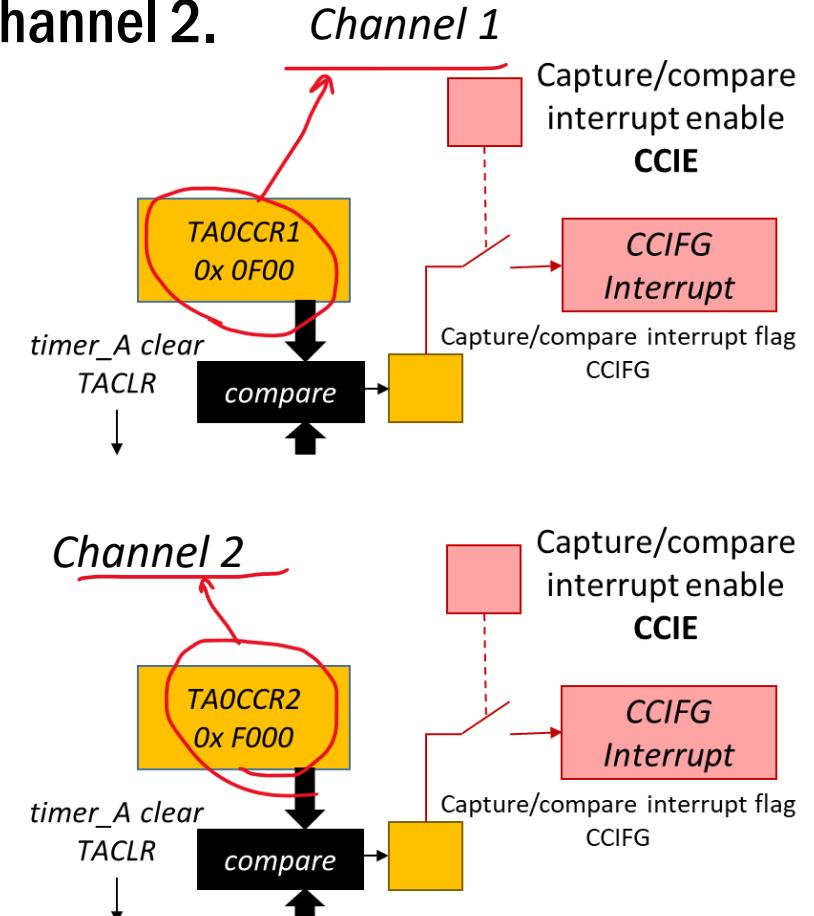
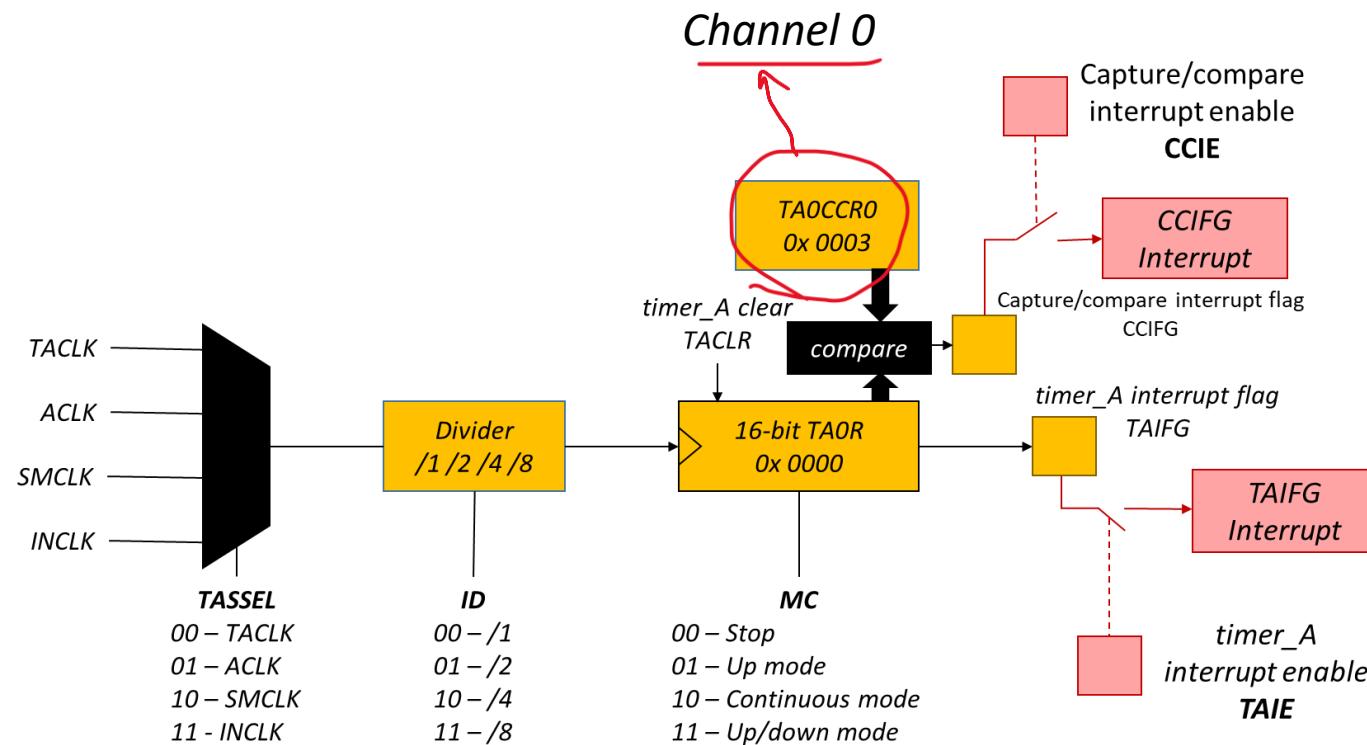
# Multiple Channels of Timer\_A0

- For Timer\_A
  - In Timer\_A0, similar to Channel 0, it has Channel1 and Channel 2.



# Multiple Channels of Timer\_A0

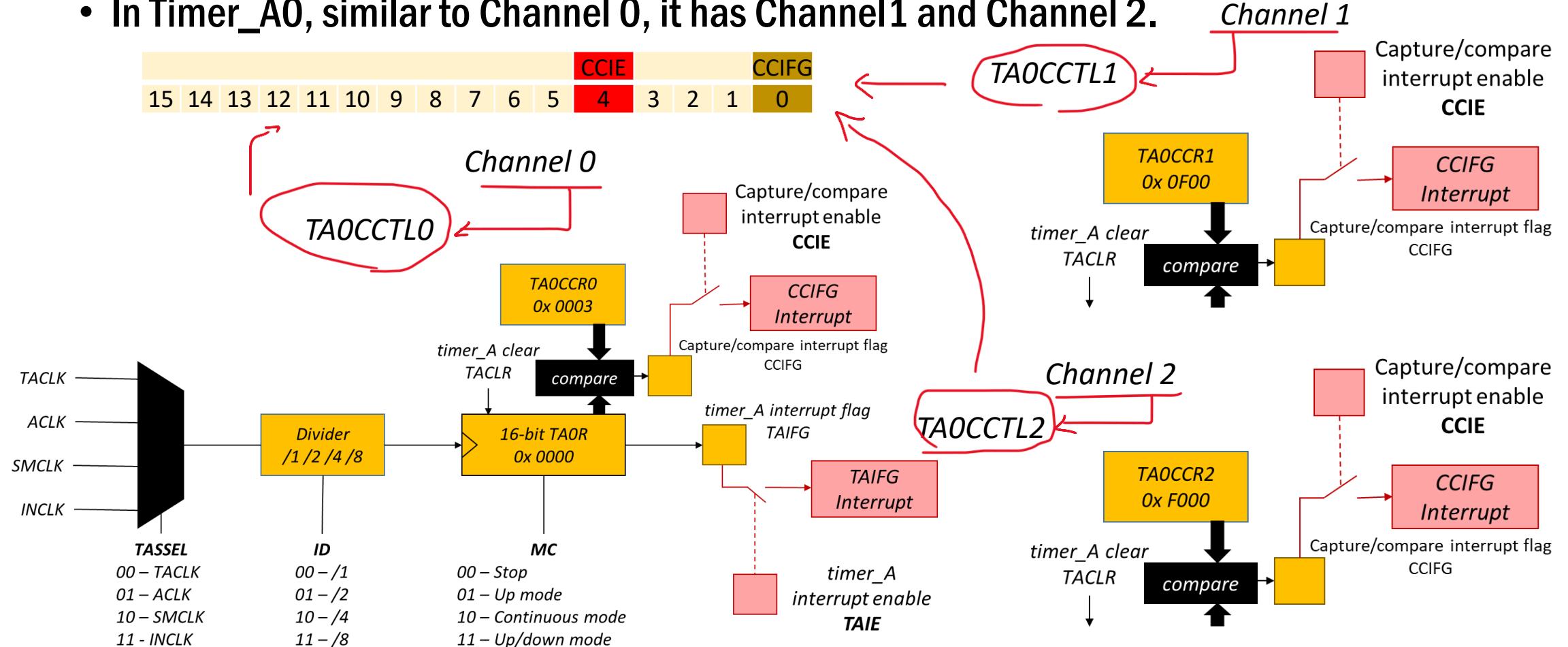
- For Timer\_A
  - In Timer\_A0, similar to Channel 0, it has Channel1 and Channel 2.



# Multiple Channels of Timer\_A0

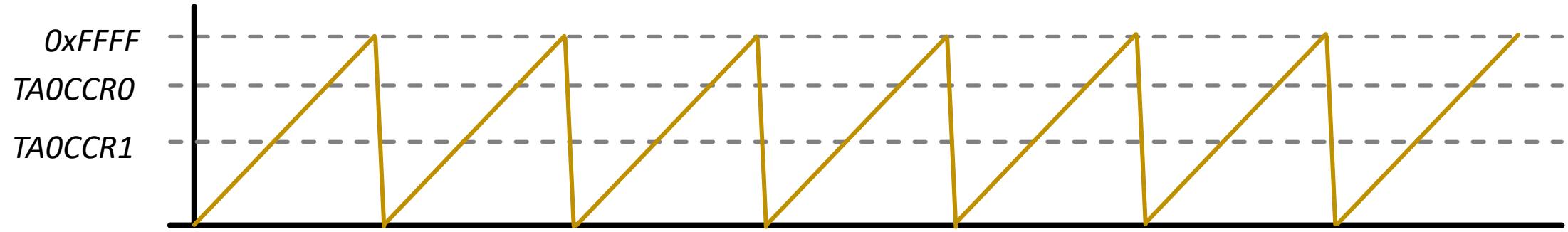
- For Timer\_A

- In Timer\_A0, similar to Channel 0, it has Channel1 and Channel 2.



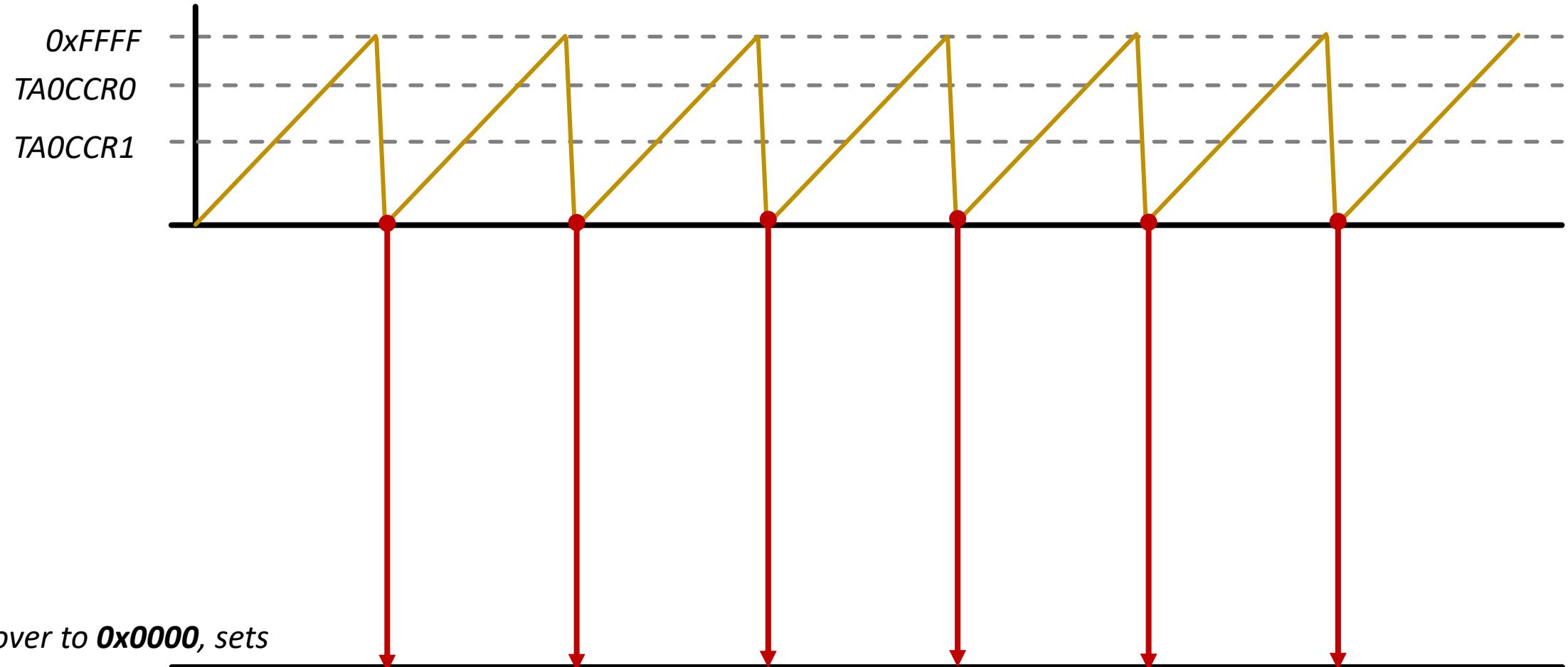
# Multiple Channels of Timer\_A0

- Multiple (TWO) channels, MC=2 (Continuous)



# Multiple Channels of Timer\_A0

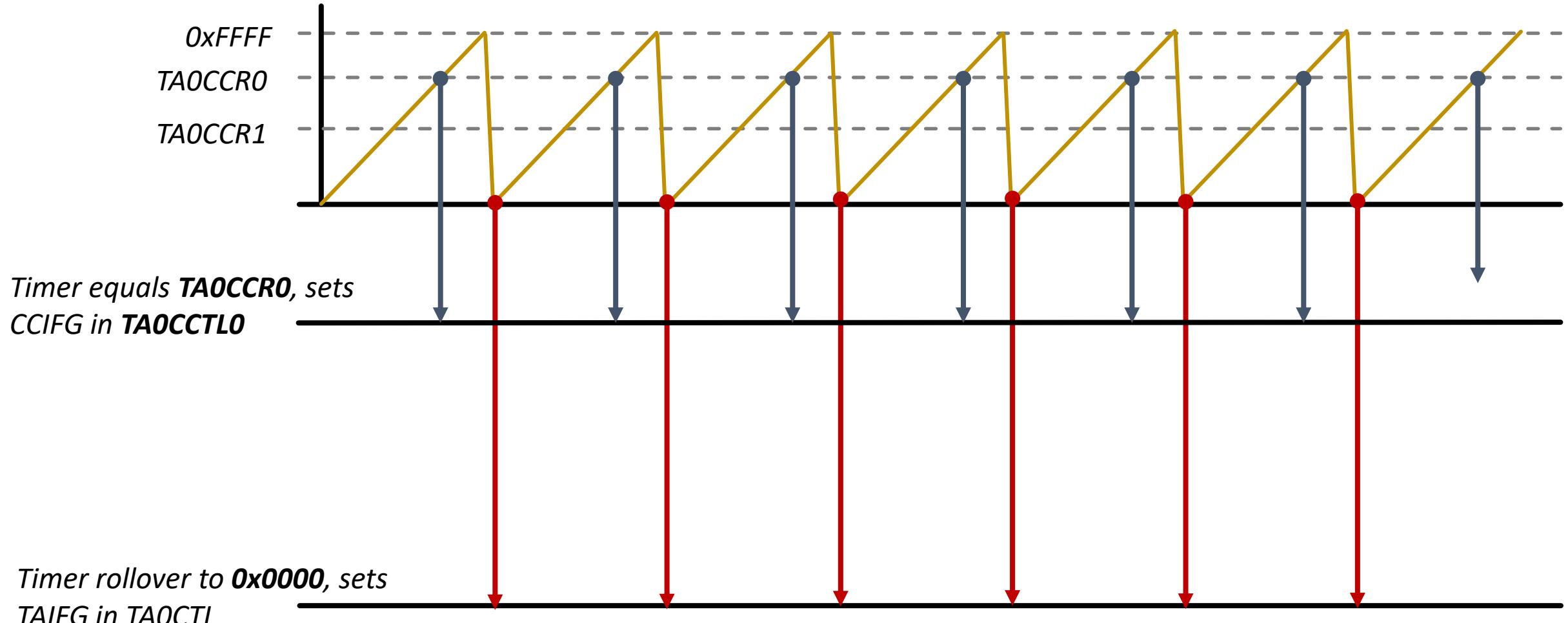
- Multiple (TWO) channels, MC=2 (Continuous)



Timer rollover to **0x0000**, sets  
TAIFG in TAOCTL

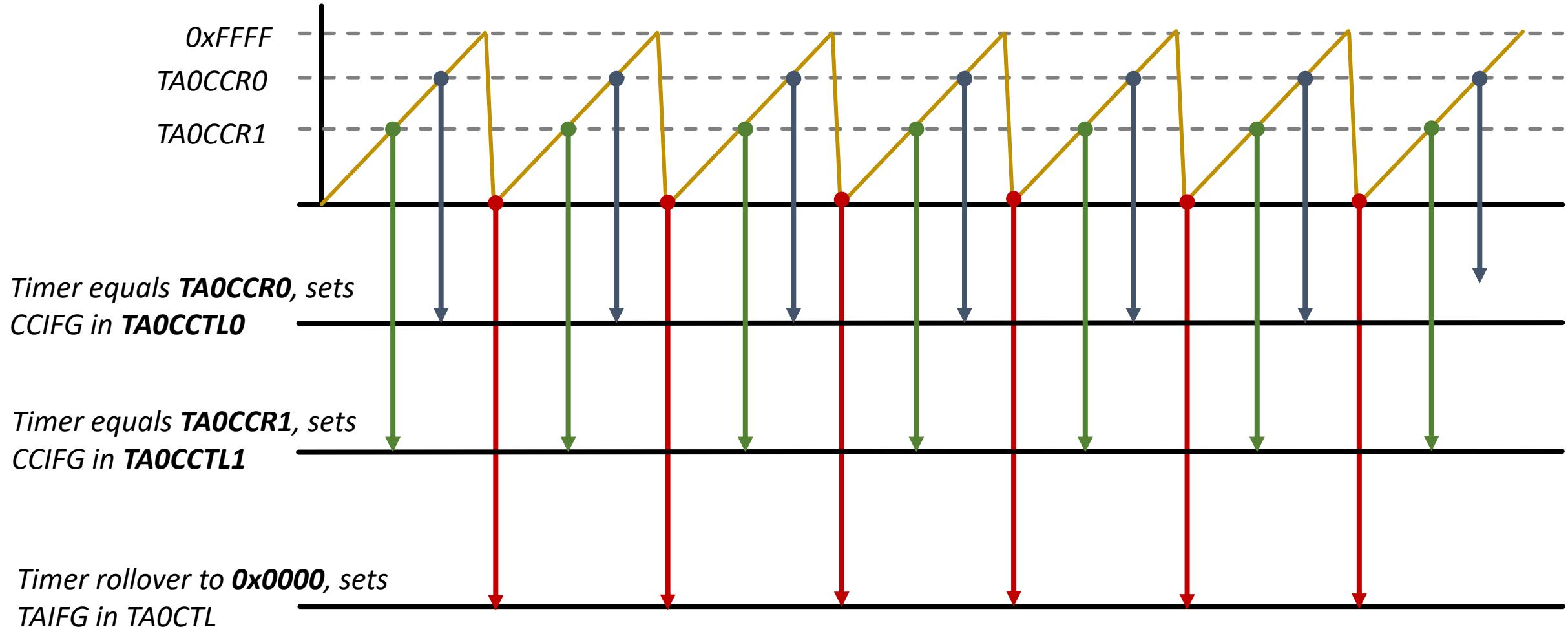
# Multiple Channels of Timer\_A0

- Multiple (TWO) channels, MC=2 (Continuous)



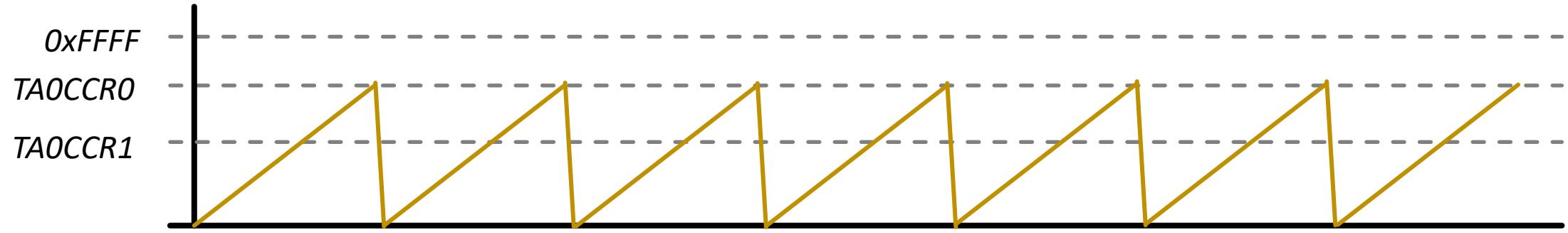
# Multiple Channels of Timer\_A0

- Multiple (TWO) channels, MC=2 (Continuous)



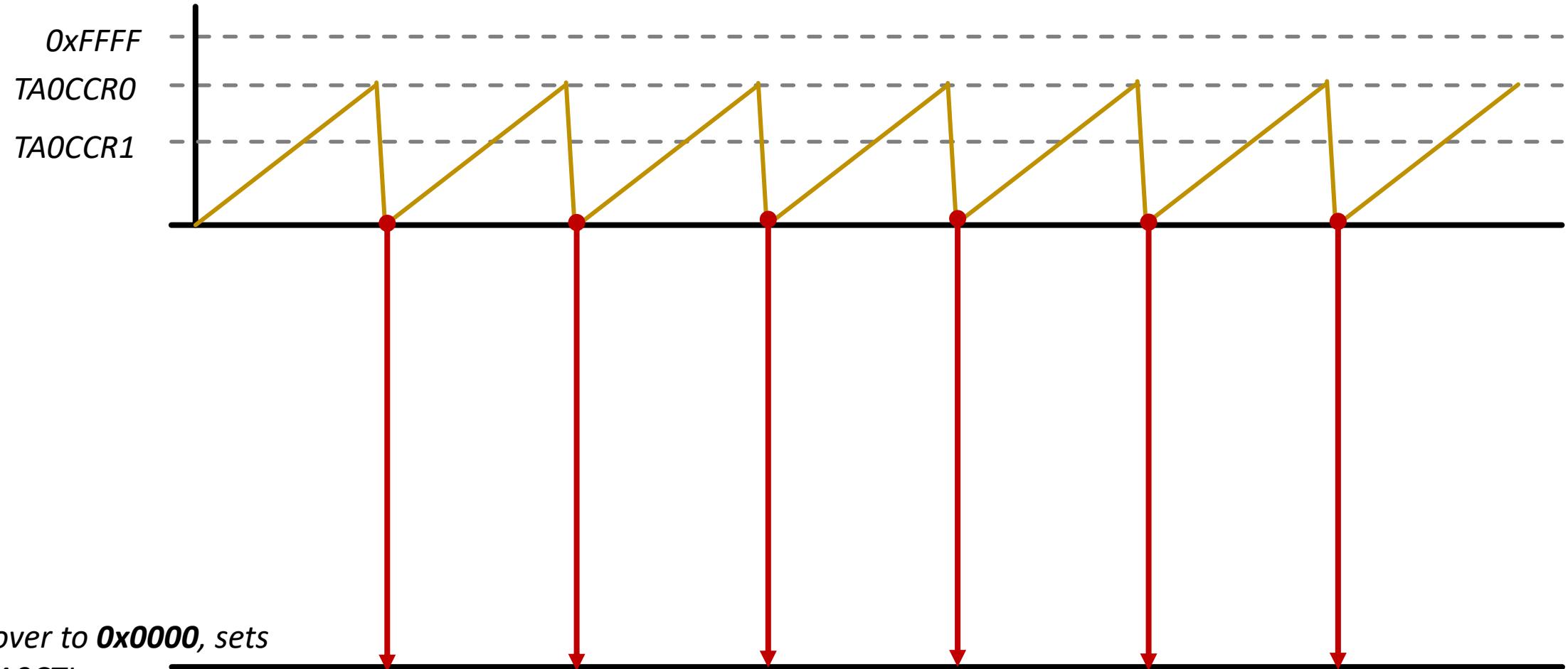
# Multiple Channels of Timer\_A0

- Multiple (TWO) channels, MC=1 (Up)



# Multiple Channels of Timer\_A0

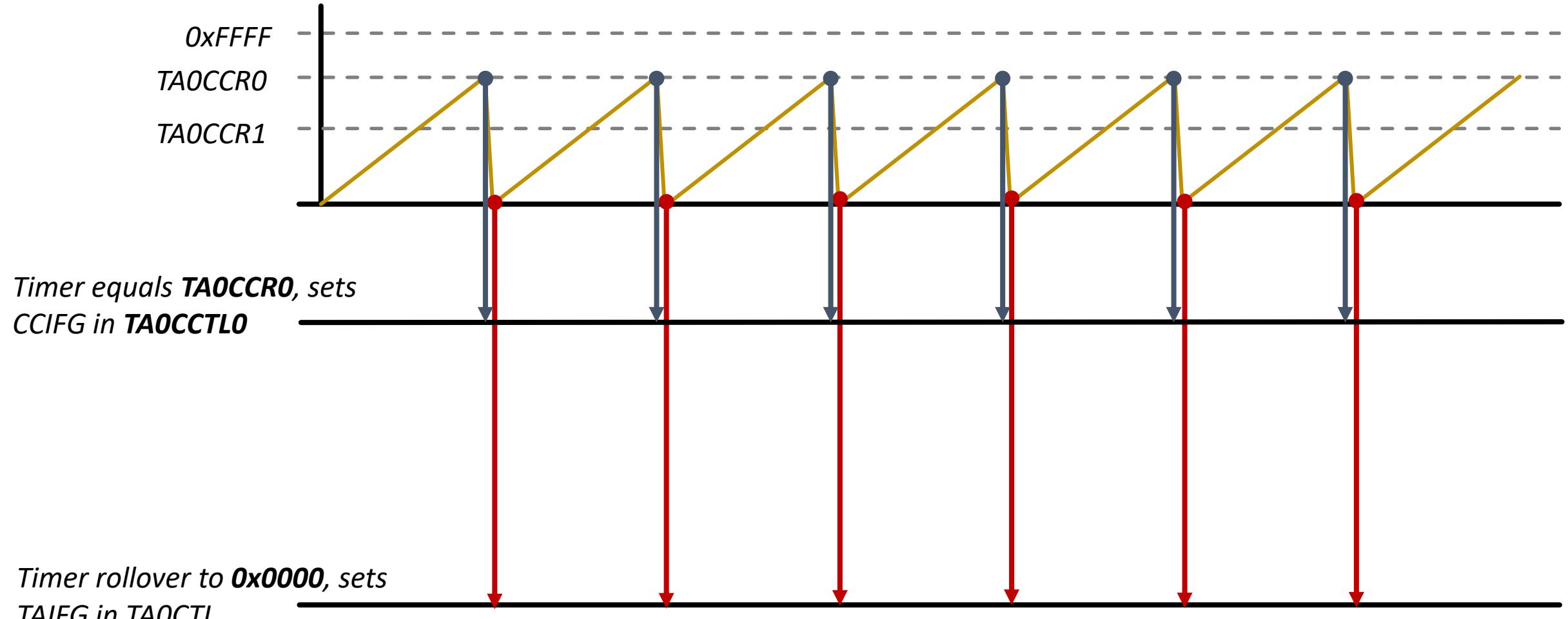
- Multiple (TWO) channels, MC=1 (Up)



Timer rollover to **0x0000**, sets  
TAIFG in TAOCTL

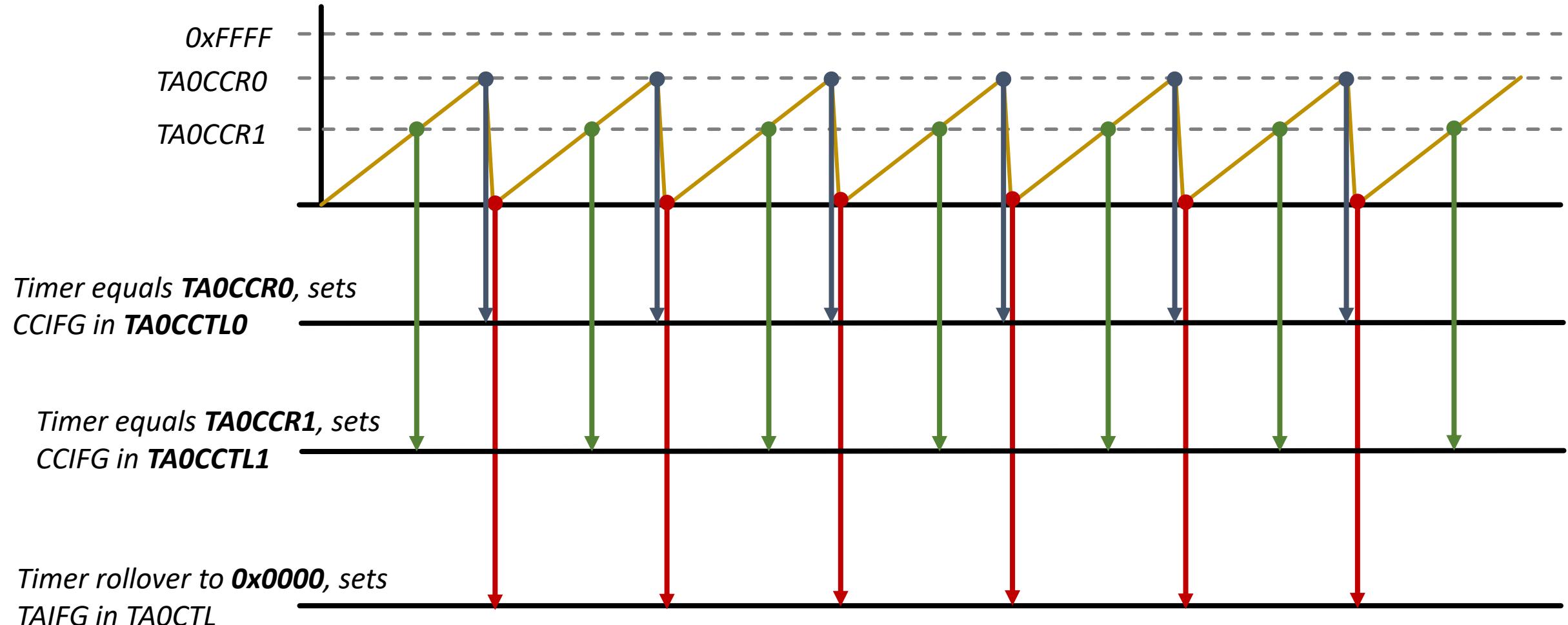
# Multiple Channels of Timer\_A0

- Multiple (TWO) channels, MC=1 (Up)



# Multiple Channels of Timer\_A0

- Multiple (TWO) Channels, MC=1 (Up)

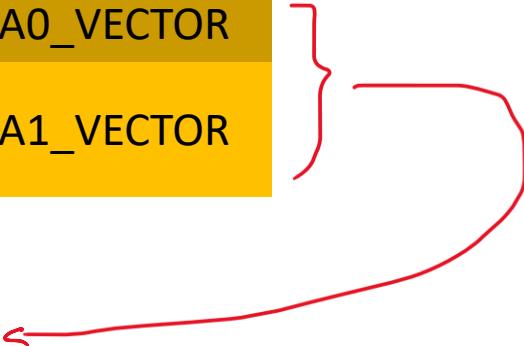




# Multiple Channels of Timer\_A0

- Interrupts per channel

Event	Interrupt enable	Interrupt flag	Interrupt vector
TAOR equals <b>TA0CCR0</b>	CCIE in <b>TA0CCTL0</b>	CCIFG in <b>TA0CCTL0</b>	TIMER0_A0_VECTOR
TAOR register rolls back to 0x0000	TAIE in <b>TA0CTL</b>	TAIFG in <b>TA0CTL</b>	TIMER0_A1_VECTOR



*For the #pragma used for defining ISR(). There are TWO different handler for each interrupt flag (One for TAIFG and one for CCIFG)!*



# Multiple Channels of Timer\_A0

- Interrupts per channel

Event	Interrupt enable	Interrupt flag	Interrupt vector
TAOR equals <b>TA0CCR0</b>	CCIE in <b>TA0CCTL0</b>	CCIFG in <b>TA0CCTL0</b>	<b>TIMER0_A0_VECTOR</b>
TAOR register rolls back to 0x0000	TAIE in <b>TA0CTL</b>	TAIFG in <b>TA0CTL</b>	<b>TIMER0_A1_VECTOR</b>
Event	Interrupt enable	Interrupt flag	Interrupt vector
TAOR equals <b>TA0CCR1</b>	CCIE in <b>TA0CCTL1</b>	CCIFG in <b>TA0CCTL1</b>	<b>TIMER0_A1_VECTOR</b>
TAOR register can roll back to 0x0000	TAIE in <b>TA0CTL</b>	TAIFG in <b>TA0CTL</b>	<b>TIMER0_A1_VECTOR</b>
Event	Interrupt enable	Interrupt flag	Interrupt vector
TAOR equals <b>TA0CCR2</b>	CCIE in <b>TA0CCTL2</b>	CCIFG in <b>TA0CCTL2</b>	<b>TIMER0_A1_VECTOR</b>
TAOR register can roll back to 0x0000	TAIE in <b>TA0CTL</b>	TAIFG in <b>TA0CTL</b>	<b>TIMER0_A1_VECTOR</b>

Shared ISR  
(one function  
for all)

# Multiple Channels of Timer\_A0

- Interrupts per channel

*The flag and enabler are separated. So, each can be used separately (but with the same ISR).*

Event	Interrupt enable	Interrupt flag	Interrupt vector
TAOR equals <b>TA0CCR0</b>	CCIE in <b>TA0CCTL0</b>	CCIFG in <b>TA0CCTL0</b>	TIMER0_A0_VECTOR
TAOR register rolls back to 0x0000	TAIE in <b>TA0CTL</b>	TAIFG in <b>TA0CTL</b>	<b>TIMER0_A1_VECTOR</b>
Event	Interrupt enable	Interrupt flag	Interrupt vector
TAOR equals <b>TA0CCR1</b>	CCIE in <b>TA0CCTL1</b>	CCIFG in <b>TA0CCTL1</b>	TIMER0_A1_VECTOR
TAOR register can roll back to 0x0000	TAIE in <b>TA0CTL</b>	TAIFG in <b>TA0CTL</b>	<b>TIMER0_A1_VECTOR</b>
Event	Interrupt enable	Interrupt flag	Interrupt vector
TAOR equals <b>TA0CCR2</b>	CCIE in <b>TA0CCTL2</b>	CCIFG in <b>TA0CCTL2</b>	TIMER0_A1_VECTOR
TAOR register can roll back to 0x0000	TAIE in <b>TA0CTL</b>	TAIFG in <b>TA0CTL</b>	<b>TIMER0_A1_VECTOR</b>

*Shared ISR (one function for all)*

# Multiple Channels of Timer\_A0

- Interrupts per channel

Event	Interrupt enable	Interrupt flag	Interrupt vector
TAOR equals <b>TA0CCR0</b>	CCIE in <b>TA0CCTL0</b>	CCIFG in <b>TA0CCTL0</b>	TIMER0_A0_VECTOR
<b>(Q) How can we handle individual requests, if they are sharing a same ISR (function)?</b>			
TAOR equals <b>TA0CCR1</b>	CCIE in <b>TA0CCTL1</b>	CCIFG in <b>TA0CCTL1</b>	TIMER0_A1_VECTOR
TAOR register can roll back to 0x0000	TAIE in <b>TA0CTL</b>	TAIFG in <b>TA0CTL</b>	TIMER0_A1_VECTOR
TAOR equals <b>TA0CCR2</b>	CCIE in <b>TA0CCTL2</b>	CCIFG in <b>TA0CCTL2</b>	TIMER0_A1_VECTOR
TAOR register can roll back to 0x0000	TAIE in <b>TA0CTL</b>	TAIFG in <b>TA0CTL</b>	TIMER0_A1_VECTOR

The flag and enabler are separated. So, each can be used separately (but with the same ISR).

Shared ISR (one function for all)

# Multiple Channels of Timer\_A0

- Interrupts per channel

Event	Interrupt enable	Interrupt flag	Interrupt vector
TAOR equals <b>TA0CCR0</b>	CCIE in <b>TA0CCTL0</b>	CCIFG in <b>TA0CCTL0</b>	TIMER0_A0_VECTOR
<b>(Q) How can we handle individual requests, if they are sharing a same ISR (function)?</b>			
TAOR register rolls back to 0x0000	TAIE in <b>TA0CTL</b>	TAIFG in <b>TA0CTL</b>	TIMER0_A1_VECTOR
<b>(A) The flags are set and they are different. So the flags can be checked inside the code.</b>			
TAOR equals <b>TA0CCR2</b> TAOR register can roll back to 0x0000	CCIE in <b>TA0CCTL2</b>	CCIFG in <b>TA0CCTL2</b>	TIMER0_A1_VECTOR
	TAIE in <b>TA0CTL</b>	TAIFG in <b>TA0CTL</b>	TIMER0_A1_VECTOR

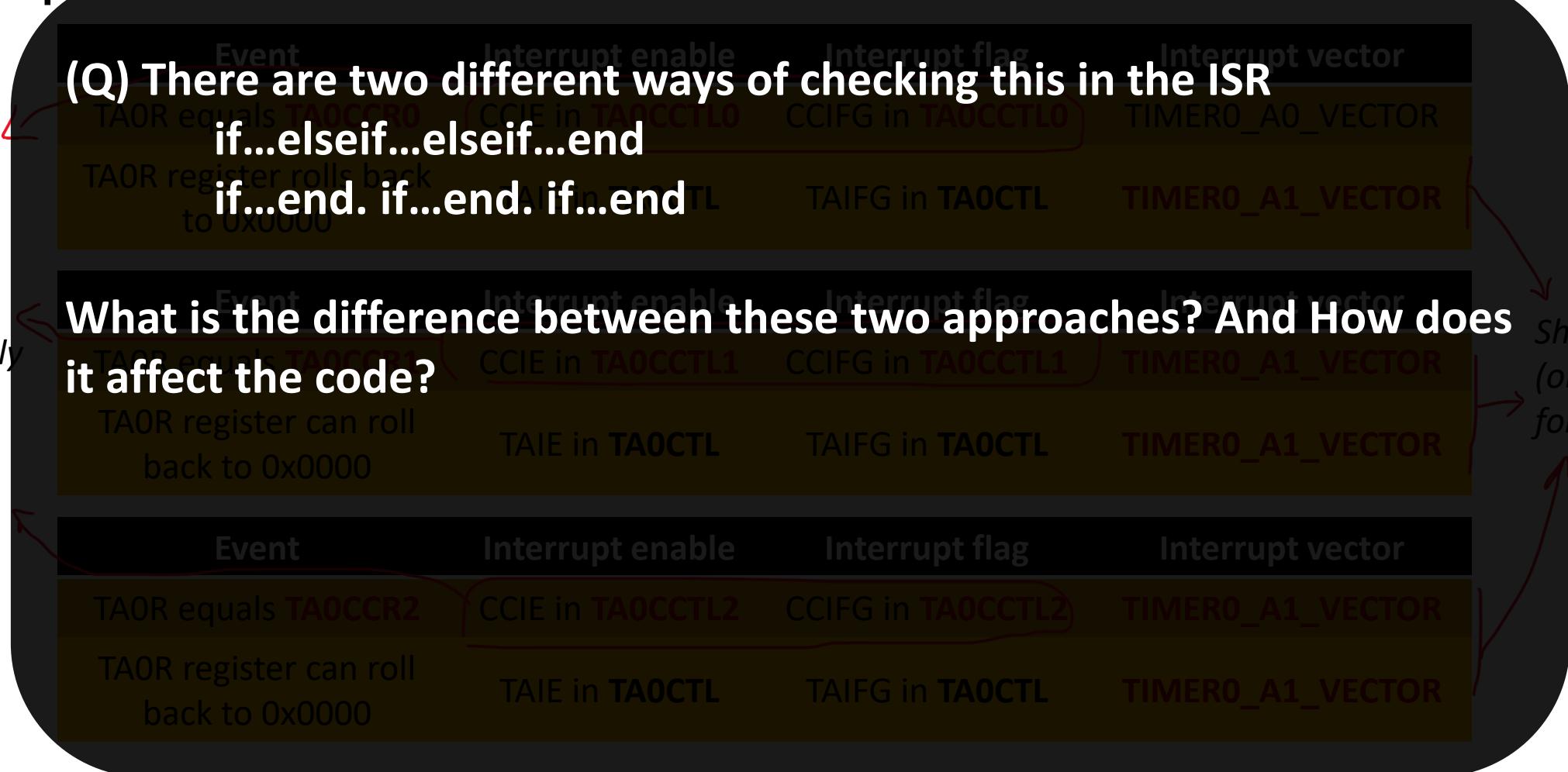
The flag and enabler are separated. So, each can be used separately (but with the same ISR).

Shared ISR (one function for all)

# Multiple Channels of Timer\_A0

- Interrupts per channel

*The flag and enabler are separated. So, each can be used separately (but with the same ISR).*



# Multiple Channels of Timer\_A0

- Interrupts per channel

The flag and enabler are separated. So, each can be used separately (but with the same ISR).

**(Q) There are two different ways of checking this in the ISR**

if...elseif...elseif...end  
if...end. if...end. if...end

**What is the difference between these two approaches? And How does it affect the code?**

**(A) In the first method, only one will get executed per ISR call. In the second, everything can be executed. Prioritization also can happen in the second approach i.e. the order in which the flags needs to be checked.**

Shared ISR  
(one function for all)

# Multiple Channels of Timer\_A0

- Interrupts per channel

Event	Interrupt enable	Interrupt flag	Interrupt vector
TAOR equals TA0CCR0	CCIE in TA0CCTL0	CCIFG in TA0CCTL0	TIMER0_A0_VECTOR
TAOR register rolls back	TAIE in TA0CTL	TAIFG in TA0CTL	TIMER0_A1_VECTOR
(Q) Which interrupt flag needs to be cleared during the time shared ISR is called? And who will be responsible for clearing the flag?	TAIE in TA0CTL	TAIFG in TA0CTL	TIMER0_A1_VECTOR
TAOR equals TA0CCR1	CCIE in TA0CCTL1	CCIFG in TA0CCTL1	TIMER0_A1_VECTOR
TAOR register can roll back to 0x0000	TAIE in TA0CTL	TAIFG in TA0CTL	TIMER0_A1_VECTOR
Event	Interrupt enable	Interrupt flag	Interrupt vector
TAOR equals TA0CCR2	CCIE in TA0CCTL2	CCIFG in TA0CCTL2	TIMER0_A1_VECTOR
TAOR register can roll back to 0x0000	TAIE in TA0CTL	TAIFG in TA0CTL	TIMER0_A1_VECTOR

The flag and enabler are separated. So, each can be used separately (but with the same ISR).

Shared ISR (one function for all)

# Multiple Channels of Timer\_A0

- Interrupts per channel

Event	Interrupt enable	Interrupt flag	Interrupt vector
TAOR equals TA0CCR0	CCIE in TA0CCTL0	CCIFG in TA0CCTL0	TIMER0_A0_VECTOR
TAOR register rolls back back to 0x0000	TAIE in TA0CTL	TAIFG in TA0CTL	TIMER0_A1_VECTOR
TAOR equals TA0CCR1	CCIE in TA0CCTL1	CCIFG in TA0CCTL1	TIMER0_A1_VECTOR
TAOR register can roll back to 0x0000	TAIE in TA0CTL	TAIFG in TA0CTL	TIMER0_A1_VECTOR
TAOR equals TA0CCR2	CCIE in TA0CCTL2	CCIFG in TA0CCTL2	TIMER0_A1_VECTOR
TAOR register can roll back to 0x0000	TAIE in TA0CTL	TAIFG in TA0CTL	TIMER0_A1_VECTOR

The flag and enabler are separated. So, each can be used separately (but with the same ISR).

(Q) Which interrupt flag needs to be cleared during the time shared ISR is called? And who will be responsible for clearing the flag?

(A) User/Program, and it should be done based on the flag raised.

Shared ISR (one function for all)



# Multiple Channels of Timer\_A0

- Interrupts per channel

```
// include header files
void main(void)
{
    // initial watchdog and LPM
    // Configure peripherals (LED), timer, and interrupts
    _enable_interrupts();           // enable GIE using intrinsic functions

    for(;;) {}                    // Infinite loop
}

#pragma vector = TIMER0_A1_VECTOR      // preprocessor directive
__interrupt void TOA1_ISR(void) {
    // Clear the interrupt flag
    // Perform the ISR routine
}

#pragma vector = TIMER0_A0_VECTOR      // preprocessor directive
__interrupt void TOA0_ISR(void) {
    // Clear the interrupt flag
    // Perform the ISR routine
}

// This ISR can be triggered by multiple events
__interrupt void T0A1_ISR(void) {
    if (CCIFG in TA0CCTL1 set) {
        // Clear the interrupt flag
        // Perform the ISR routine
    }

    if (CCIFG in TA0CCTL2 set) {
        // Clear the interrupt flag
        // Perform the ISR routine
    }

    if (TAIFG in TA0CTL set) {
        // Clear the interrupt flag
        // Perform the ISR routine
    }
}
```



# Multiple Channels of Timer\_A0

- Interrupts per channel

```
// include header files
void main(void)
{
    // Initialize timer A0
    // ...
}
```

Event	Interrupt enable	Interrupt flag	Interrupt vector
TA0R equals <b>TA0CCR0</b>	CCIE in <b>TA0CCTL0</b>	CCIFG in <b>TA0CCTL0</b>	<b>TIMERO_A0_VECTOR</b>
TA0R register rolls back to 0x0000	TAIE in <b>TA0CTL</b>	TAIFG in <b>TA0CTL</b>	<b>TIMERO_A1_VECTOR</b>
<pre>for(;;) {} // Infinite loop } // Perform the ISR routine</pre>			
Event	Interrupt enable	Interrupt flag	Interrupt vector
TA0R equals <b>TA0CCR1</b>	CCIE in <b>TA0CCTL1</b>	CCIFG in <b>TA0CCTL1</b>	<b>TIMERO_A1_VECTOR</b>
TA0R register can roll back to 0x0000	TAIE in <b>TA0CTL</b>	TAIFG in <b>TA0CTL</b>	<b>TIMERO_A1_VECTOR</b>
<pre>// Perform the ISR routine</pre>			
Event	Interrupt enable	Interrupt flag	Interrupt vector
TA0R equals <b>TA0CCR2</b>	CCIE in <b>TA0CCTL2</b>	CCIFG in <b>TA0CCTL2</b>	<b>TIMERO_A1_VECTOR</b>
TA0R register can roll back to 0x0000	TAIE in <b>TA0CTL</b>	TAIFG in <b>TA0CTL</b>	<b>TIMERO_A1_VECTOR</b>



# External Interrupts

- Interrupts per channel

```
// include header files
void main(void)
{
    // initial watchdog and LPM
    // Configure peripherals (LED), timer, and interrupts
    _enable_interrupts();      // enable GIE using intrinsic functions

    // Infinite loop
    for(;;) {
        if (button is pressed?)
            turn ON LED;
        else
            turn OFF LED;
    }
}
```



# External Interrupts

- Interrupts per channel

```
// include header files
void main(void)
{
    // initial watchdog and LPM
    // Configure peripherals (LED), timer, and interrupts
    _enable_interrupts();           // enable GIE using intrinsic functions

    // Infinite loop
    for(;;) {
        if (button is pressed?) {
            turn ON LED;
        } else
            turn OFF LED;
    }
}
```

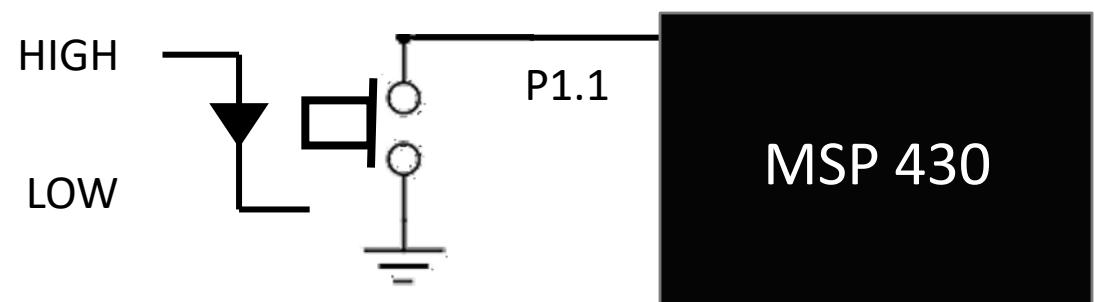
Polling?

*Using the '**polling**' technique to wait for events that happen very slow (~few Hz, relative to the processor frequency few MHz) is inefficient.*

*Interrupts are better at handling slow inputs.*

# External Interrupts

- Interrupts of Port P1
  - **P1IFG** flag is set when an external event occurs.
    - e.g., A button press causes the input to transition from high to low.
  - **P1IE** register is used to enable the interrupt on individual pins of Port 1.
  - **P1IES** register specifies the transition on which the interrupt flag is raised.
    - 1b – corresponding P1IFG flag is set during high-to-low transition
    - 0b – corresponding P1IFG flag is set during low-to-high transition



# External Interrupts

- Interrupts of Port P1

- **P1IFG** flag is set when an external event occurs.

- e.g., A button press causes the input to transition from high to low.

*P1IFG – has all the flags*

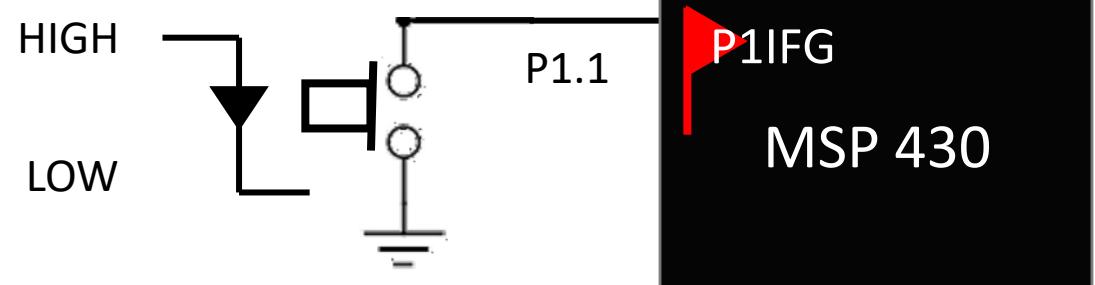
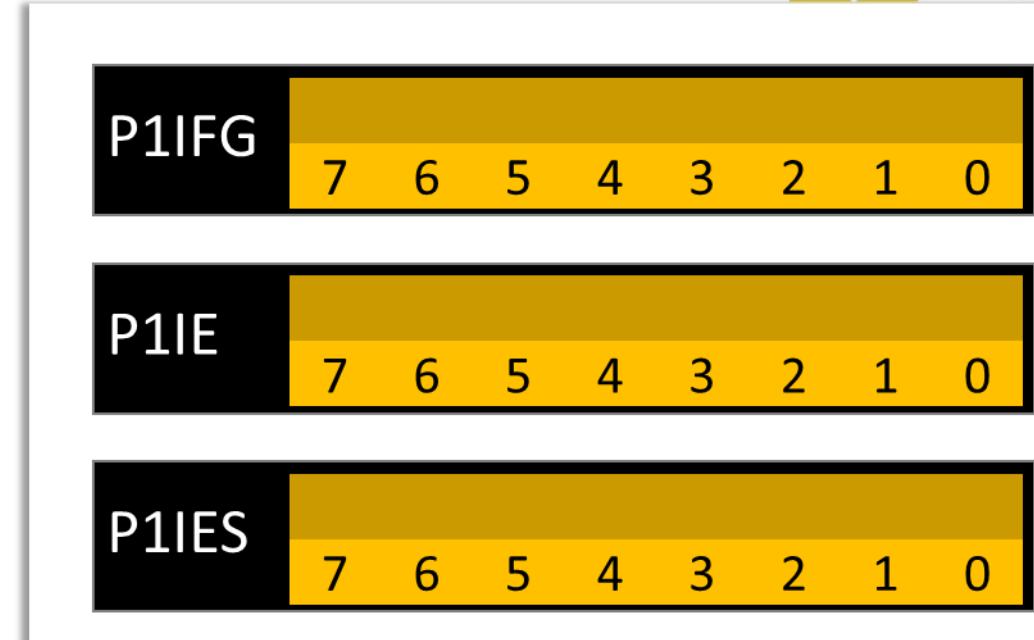
- **P1IE** register is used to enable the interrupt on individual pins of Port 1.

*P1IE – enable interrupts on each pin*

- **P1IES** register specifies the transition on which the interrupt flag is raised.

- 1b – corresponding P1IFG flag is set during high-to-low transition
- 0b – corresponding P1IFG flag is set during low-to-high transition

*P1IES – sets which transition must trigger the interrupt – high to low / low to high*





# External Interrupts

- Port 1 external interrupt vector

Event	Interrupt enable	Interrupt flag	Interrupt vector
External event on P1.0	BIT0 in P1IE	BIT0 in P1IFG	
External event on P1.1	BIT1 in P1IE	BIT1 in P1IFG	PORT1_VECTOR???
...	...	...	Refer family user guide and header file
External event on P1.7	BIT7 in P1IE	BIT7 in P1IFG	

`#pragma vector = PORT1_VECTOR`

*All interrupts in Port 1 share the same interrupt vector.  
Therefore, they share the same interrupt service routine.*



# External Interrupts

- Handling multiple interrupts in Port 1
  - e.g., Toggle Red LED when Push button 1 is pressed, toggle Green LED when Push button 2 is pressed.

```
// include header files
void main(void)
{
    // initial watchdog and LPM
    // Configure peripherals (LED), push buttons, timer, and interrupts
    _enable_interrupts();           // enable GIE using intrinsic functions

    for(;;) {}                    // Infinite loop
}

#pragma vector = PORT1_VECTOR      // preprocessor directive??
__interrupt void Port1_ISR(void) { // This ISR can be triggered by multiple events
    if (push button 1?) {
        // Clear pin0 flag
        // Toggle red LED
    }
    if (push button 2?) {          // P1IFG.2??
        // Clear pin1 flag
        // Toggle green LED
    }
    ...
}
```



# Example 1

- Timer and Interrupt

- What is this code for?

```
#include <msp430.h>

void main(void)
{
    WDTCTL = WDTPW | WDTHOLD;      // Stop watchdog timer

    // Set the clock source to SMCLK and configure Timer_A in continuous mode
    TACTL = TASSEL_2 + MC_2 + TAIE;  // SMCLK, continuous mode, enable overflow interrupt

    __enable_interrupt();          // Enable global interrupts

    while(1)
    {
        // Main loop does nothing, waits for interrupt
    }
}

// Timer_A Interrupt Service Routine (ISR) for Timer Overflow
#pragma vector = TIMER0_A1_VECTOR
__interrupt void Timer_A(void)
{
    // Check for the Timer overflow interrupt (TAIFG)
    if (TACTL & TAIFG)
    {
        TACTL &= ~TAIFG;    // Clear the overflow interrupt flag
        P1OUT ^= BIT0;     // Toggle LED connected to P1.0
    }
}
```



# Example 1

- Timer and Interrupt

- What is this code for?

*we configure the Timer\_A module to generate an interrupt when it overflows*

```
#include <msp430.h>

void main(void)
{
    WDTCTL = WDTPW | WDTHOLD;      // Stop watchdog timer

    // Set the clock source to SMCLK and configure Timer_A in continuous mode
    TACTL = TASSEL_2 + MC_2 + TAIE; // SMCLK, continuous mode, enable overflow interrupt

    __enable_interrupt();          // Enable global interrupts

    while(1)
    {
        // Main loop does nothing, waits for interrupt
    }
}

// Timer_A Interrupt Service Routine (ISR) for Timer Overflow
#pragma vector = TIMER0_A1_VECTOR
__interrupt void Timer_A(void)
{
    // Check for the Timer overflow interrupt (TAIFG)
    if (TACTL & TAIFG)
    {
        TACTL &= ~TAIFG;    // Clear the overflow interrupt flag
        P1OUT ^= BIT0;     // Toggle LED connected to P1.0
    }
}
```



# Example 2

- Timer and Interrupt

- Is there any issue with this code?

```
#include <msp430.h>

void main(void)
{
    WDTCTL = WDTPW | WDTHOLD; // Stop watchdog timer

    TACTL = TASSEL_2 + MC_1; // SMCLK, up mode
    TACCTL0 = CCIE; // Enable CCR0 interrupt
    TACCR0 = 1000 - 1; // Set CCR0 for a 1ms delay assuming 1 MHz clock

    while(1)
    {
        // Main loop does nothing, waits for interrupt
    }
}

// Timer_A CCR0 Interrupt Service Routine (ISR)
#pragma vector = TIMER0_A0_VECTOR
__interrupt void Timer_A0(void)
{
    P1OUT ^= BIT0; // Toggle LED connected to P1.0
}
```



# Example 2

- Timer and Interrupt

- Is there any issue with this code?

*The global interrupts are not enabled.*

*Without enabling global interrupts, the CPU will not respond to any interrupts even though the interrupt is set up correctly.*

`__enable_interrupt()`

```
#include <msp430.h>

void main(void)
{
    WDTCTL = WDTPW | WDTHOLD; // Stop watchdog timer

    TACTL = TASSEL_2 + MC_1; // SMCLK, up mode
    TACCTL0 = CCIE;          // Enable CCR0 interrupt
    TACCR0 = 1000 - 1;        // Set CCR0 for a 1ms delay assuming 1 MHz clock

    while(1)
    {
        // Main loop does nothing, waits for interrupt
    }

    // Timer_A CCR0 Interrupt Service Routine (ISR)
    #pragma vector = TIMER0_A0_VECTOR
    __interrupt void Timer_A0(void)
    {
        P1OUT ^= BIT0; // Toggle LED connected to P1.0
    }
}
```



# Example 3

- Timer and Interrupt

- Is there any issue with this code?

```
void main(void)
{
    WDTCTL = WDTPW | WDTHOLD; // Stop watchdog timer

    TACTL = TASSEL_2 + MC_1; // SMCLK, up mode
    TACCTL0 = CCIE; // Enable CCR0 interrupt
    TACCR0 = 1000 - 1; // Set CCR0 for a 1ms delay assuming 1 MHz clock

    __enable_interrupt(); // Enable global interrupts

    while(1)
    {
        // Main loop does nothing, waits for interrupt
    }
}

// Timer_A CCR0 Interrupt Service Routine (ISR)
#pragma vector = TIMER0_A0_VECTOR
__interrupt void Timer_A0(void)
{
    if (TACTL & TAIFG) // Interrupt flag check
    {
        P1OUT ^= BIT0; // Toggle LED connected to P1.0
        TACTL &= ~TAIFG; // Clear interrupt flag
    }
}
```

# Example 3

- Timer and Interrupt

- Is there any issue with this code?

*The ISR is supposed to handle the CCR0 interrupt, but the TAIFG flag is being checked instead.*

```

void main(void)
{
    WDTCTL = WDTPW | WDTHOLD; // Stop watchdog timer

    TACTL = TASSEL_2 + MC_1; // SMCLK, up mode
    TACCTL0 = CCIE; // Enable CCR0 interrupt
    TACCR0 = 1000 - 1; // Set CCR0 for a 1ms delay assuming 1 MHz clock

    __enable_interrupt(); // Enable global interrupts

    while(1)
    {
        // Main loop does nothing, waits for interrupt
    }
}

// Timer_A CCR0 Interrupt Service Routine (ISR)
#pragma vector = TIMER0_A0_VECTOR
__interrupt void Timer_A0(void)
{
    if (TACTL & TAIFG) // Interrupt flag check
    {
        P1OUT ^= BIT0; // Toggle LED connected to P1.0
        TACTL &= ~TAIFG; // Clear interrupt flag
    }
}

```



# Example 4

- Timer and Interrupt

- Is there any issue with this code?

```
void main(void)
{
    WDTCTL = WDTPW | WDTHOLD; // Stop watchdog timer

    TACTL = TASSEL_2 + MC_1 + TAIE; // SMCLK, up mode, enable overflow interrupt
    TACCTL0 = CCIE; // Enable CCR0 interrupt
    TACCR0 = 1000 - 1; // Set CCR0 for a 1ms delay assuming 1 MHz clock

    __enable_interrupt(); // Enable global interrupts

    while(1)
    {
        // Main loop does nothing, waits for interrupt
    }
}

// Timer_A CCR0 Interrupt Service Routine (ISR)
#pragma vector = TIMER0_A0_VECTOR
__interrupt void Timer_A0(void)
{
    P1OUT ^= BIT0; // Toggle LED connected to P1.0
    __enable_interrupt(); // Incorrectly enabling interrupts within the ISR
}
```



# Example 4

- Timer and Interrupt

- Is there any issue with this code?

*Calling \_\_enable\_interrupt() inside an ISR can lead to nested interrupts, meaning other interrupts might be serviced while one interrupt is still being handled. This can lead to unpredictable behavior, especially if the system isn't designed for nested interrupts.*

```
void main(void)
{
    WDTCTL = WDTPW | WDTHOLD; // Stop watchdog timer

    TACTL = TASSEL_2 + MC_1 + TAIE; // SMCLK, up mode, enable overflow interrupt
    TACCTL0 = CCIE; // Enable CCR0 interrupt
    TACCR0 = 1000 - 1; // Set CCR0 for a 1ms delay assuming 1 MHz clock

    __enable_interrupt(); // Enable global interrupts

    while(1)
    {
        // Main loop does nothing, waits for interrupt
    }
}

// Timer_A CCR0 Interrupt Service Routine (ISR)
#pragma vector = TIMER0_A0_VECTOR
__interrupt void Timer_A0(void)
{
    P1OUT ^= BIT0; // Toggle LED connected to P1.0
    __enable_interrupt(); // Incorrectly enabling interrupts within the ISR
}
```



# Example 5

- Port 1 and Interrupt

- What is this code for?

```
#include <msp430.h>

void main(void)
{
    WDTCTL = WDTPW | WDTHOLD; // Stop watchdog timer

    P1DIR |= BIT0;           // Set P1.0 (LED) as output
    P1OUT &= ~BIT0;          // Ensure P1.0 starts low (LED off)

    P1DIR &= ~BIT3;          // Set P1.3 (button) as input
    P1REN |= BIT3;           // Enable pull-up resistor on P1.3
    P1OUT |= BIT3;           // Set pull-up resistor (button is active low)

    P1IE |= BIT3;            // Enable interrupt on P1.3
    P1IES |= BIT3;           // Interrupt on falling edge (high-to-low transition)
    P1IFG &= ~BIT3;           // Clear interrupt flag for P1.3

    __enable_interrupt();     // Enable global interrupts

    while(1)
    {
        // Main loop does nothing, waits for interrupt
    }
}

// Port 1 ISR
#pragma vector = PORT1_VECTOR
_interrupt void Port_1(void)
{
    if (P1IFG & BIT3) // Check if the interrupt was generated by P1.3
    {
        P1OUT ^= BIT0; // Toggle LED on P1.0
        P1IFG &= ~BIT3; // Clear the interrupt flag for P1.3
    }
}
```



# Example 5

- Port 1 and Interrupt

- What is this code for?

*a button connected to P1.3 generates an interrupt on a falling edge, and the interrupt toggles an LED on P1.0.*

```
// Port 1 ISR
#pragma vector = PORT1_VECTOR
_interrupt void Port_1(void)
{
    if (P1IFG & BIT3) // Check if the interrupt was generated by P1.3
    {
        P1OUT ^= BIT0; // Toggle LED on P1.0
        P1IFG &= ~BIT3; // Clear the interrupt flag for P1.3
    }
}
```

```
#include <msp430.h>

void main(void)
{
    WDTCTL = WDTPW | WDTHOLD; // Stop watchdog timer

    P1DIR |= BIT0; // Set P1.0 (LED) as output
    P1OUT &= ~BIT0; // Ensure P1.0 starts low (LED off)

    P1DIR &= ~BIT3; // Set P1.3 (button) as input
    P1REN |= BIT3; // Enable pull-up resistor on P1.3
    P1OUT |= BIT3; // Set pull-up resistor (button is active low)

    P1IE |= BIT3; // Enable interrupt on P1.3
    P1IES |= BIT3; // Interrupt on falling edge (high-to-low transition)
    P1IFG &= ~BIT3; // Clear interrupt flag for P1.3

    __enable_interrupt(); // Enable global interrupts

    while(1)
    {
        // Main loop does nothing, waits for interrupt
    }
}
```



# Example 6

- Port 1 and Interrupt

- What is this code for?

```
// Port 1 ISR
#pragma vector = PORT1_VECTOR
_interrupt void Port_1(void)
{
    if (P1IFG & BIT1) // Check if the interrupt was generated by P1.1 (rising edge)
    {
        P1OUT ^= BIT0; // Toggle LED on P1.0
        P1IFG &= ~BIT1; // Clear interrupt flag for P1.1
    }

    if (P1IFG & BIT2) // Check if the interrupt was generated by P1.2 (falling edge)
    {
        P1OUT ^= BIT6; // Toggle LED on P1.6
        P1IFG &= ~BIT2; // Clear interrupt flag for P1.2
    }
}
```

```
#include <msp430.h>

void main(void)
{
    WDTCTL = WDTPW | WDTHOLD; // Stop watchdog timer

    P1DIR |= BIT0 + BIT6; // Set P1.0 and P1.6 (LEDs) as outputs
    P1OUT &= ~(BIT0 + BIT6); // Ensure LEDs start off

    P1DIR &= ~(BIT1 + BIT2); // Set P1.1 and P1.2 as inputs (buttons)
    P1REN |= BIT1 + BIT2; // Enable pull-up resistors on P1.1 and P1.2
    P1OUT |= BIT1 + BIT2; // Set pull-up resistors (active low buttons)

    P1IE |= BIT1 + BIT2; // Enable interrupts on P1.1 and P1.2
    P1IES &= ~BIT1; // Interrupt on rising edge for P1.1
    P1IES |= BIT2; // Interrupt on falling edge for P1.2
    P1IFG &= ~(BIT1 + BIT2); // Clear interrupt flags for P1.1 and P1.2

    __enable_interrupt(); // Enable global interrupts

    while(1)
    {
        // Main loop does nothing, waits for interrupt
    }
}
```



# Example 6

- Port 1 and Interrupt

- What is this code for?

Interrupts on two different pins (P1.1 and P1.2). One button on P1.1 triggers on a rising edge, and another on P1.2 triggers on a falling edge, each toggling a different LED.

```
// Port 1 ISR
#pragma vector = PORT1_VECTOR
_interrupt void Port_1(void)
{
    if (P1IFG & BIT1) // Check if the interrupt was generated by P1.1 (rising edge)
    {
        P1OUT ^= BIT0; // Toggle LED on P1.0
        P1IFG &= ~BIT1; // Clear interrupt flag for P1.1
    }

    if (P1IFG & BIT2) // Check if the interrupt was generated by P1.2 (falling edge)
    {
        P1OUT ^= BIT6; // Toggle LED on P1.6
        P1IFG &= ~BIT2; // Clear interrupt flag for P1.2
    }
}
```

```
#include <msp430.h>

void main(void)
{
    WDTCTL = WDTPW | WDTHOLD; // Stop watchdog timer

    P1DIR |= BIT0 + BIT6; // Set P1.0 and P1.6 (LEDs) as outputs
    P1OUT &= ~(BIT0 + BIT6); // Ensure LEDs start off

    P1DIR &= ~(BIT1 + BIT2); // Set P1.1 and P1.2 as inputs (buttons)
    P1REN |= BIT1 + BIT2; // Enable pull-up resistors on P1.1 and P1.2
    P1OUT |= BIT1 + BIT2; // Set pull-up resistors (active low buttons)

    P1IE |= BIT1 + BIT2; // Enable interrupts on P1.1 and P1.2
    P1IES &= ~BIT1; // Interrupt on rising edge for P1.1
    P1IES |= BIT2; // Interrupt on falling edge for P1.2
    P1IFG &= ~(BIT1 + BIT2); // Clear interrupt flags for P1.1 and P1.2

    __enable_interrupt(); // Enable global interrupts

    while(1)
    {
        // Main loop does nothing, waits for interrupt
    }
}
```



# Example 6

- Port 1 and Interrupt

- What is this code for?

Interrupts on two different pins (P1.1 and P1.2). One button on P1.1 triggers on a rising edge, and another on P1.2 triggers on a falling edge, each toggling a different LED.

```
// Port 1 ISR
#pragma vector = PORT1_VECTOR
__interrupt void Port_1(void)
{
    if (P1IFG & BIT1) // Check if the interrupt was generated by P1.1 (rising edge)
    {
        P1OUT ^= BIT0; // Toggle LED on P1.0
        P1IFG &= ~BIT1; // Clear interrupt flag for P1.1
    }

    if (P1IFG & BIT2) // Check if the interrupt was generated by P1.2 (falling edge)
    {
        P1OUT ^= BIT6; // Toggle LED on P1.6
        P1IFG &= ~BIT2; // Clear interrupt flag for P1.2
    }
}
```

```
#include <msp430.h>

void main(void)
{
    WDTCTL = WDTPW | WDTHOLD; // Stop watchdog timer

    P1DIR |= BIT0 + BIT6; // Set P1.0 and P1.6 (LEDs) as outputs
    P1OUT &= ~(BIT0 + BIT6); // Ensure LEDs start off

    P1DIR &= ~(BIT1 + BIT2); // Set P1.1 and P1.2 as inputs (buttons)
    P1REN |= BIT1 + BIT2; // Enable pull-up resistors on P1.1 and P1.2
    P1OUT |= BIT1 + BIT2; // Set pull-up resistors (active low buttons)

    P1IE |= BIT1 + BIT2; // Enable interrupts on P1.1 and P1.2
    P1IES &= ~BIT1; // Interrupt on rising edge for P1.1
    P1IES |= BIT2; // Interrupt on falling edge for P1.2
    P1IFG &= ~(BIT1 + BIT2); // Clear interrupt flags for P1.1 and P1.2

    __enable_interrupt(); // Enable global interrupts

    while(1)
    {
        // Main loop does nothing, waits for interrupt
    }
}
```

# Thank You!

## Questions?

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