Impromptu meeting

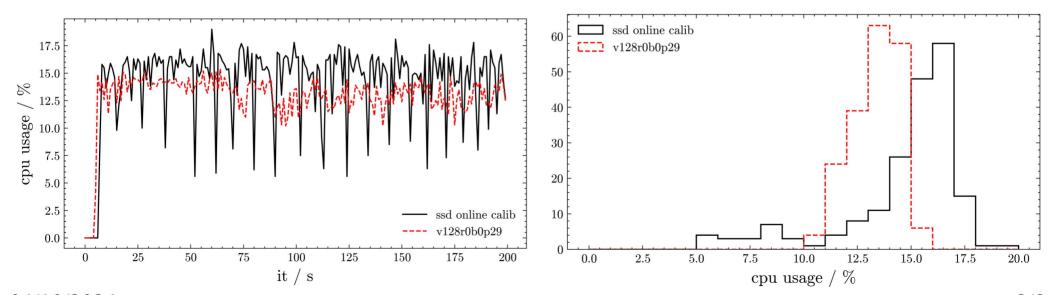
Name 1, Name 2, Name 3

Outline

- Possible FPGA trigger/counter
- Status of ssd online calibration
- Need for other testing station
- Calib dependence on WCD trigger
- Summary and Outlook

Possible FPGA trigger/counter

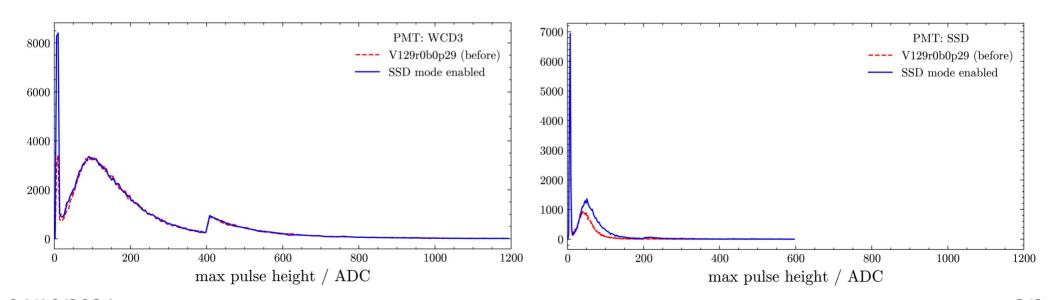
- Move resource intensive tasks to FPGA
 - Have counter that gives # of T70 events on FPGA-level
 - 3-fold coinc. of 1.75 VEM + 1-fold coinc. of 2.5 VEM (WCD)
 - 1-fold coinc. of 2.64 MIP (SSD)



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Status of SSD online calibration

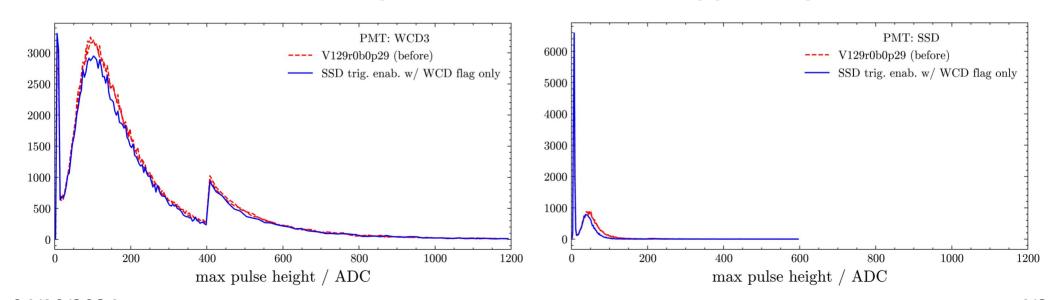
- Changes since SD/UMD calibration meeting
 - disentangle histo calculations from rate counting/threshold setting
 - Enable SSD trigger mode in FPGA muon buffer



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Status of SSD online calibration

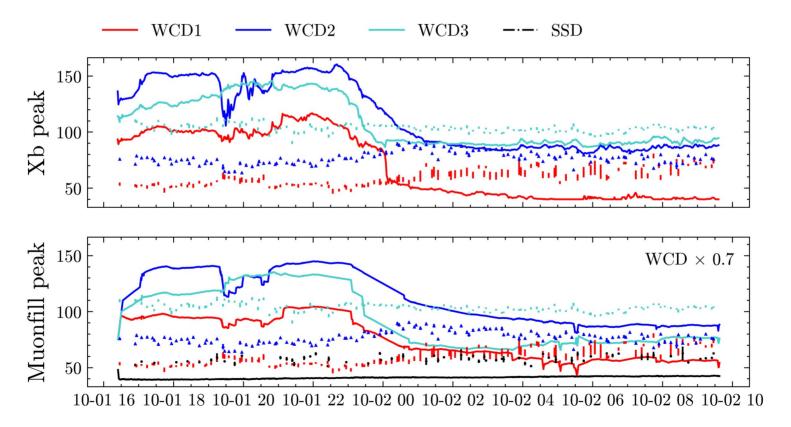
- Changes since SD/UMD calibration meeting
 - disentangle histo calculations from rate counting/threshold setting
 - Enable SSD trigger mode in FPGA muon buffer
 - Add events to histogram based on PMT trigger flags



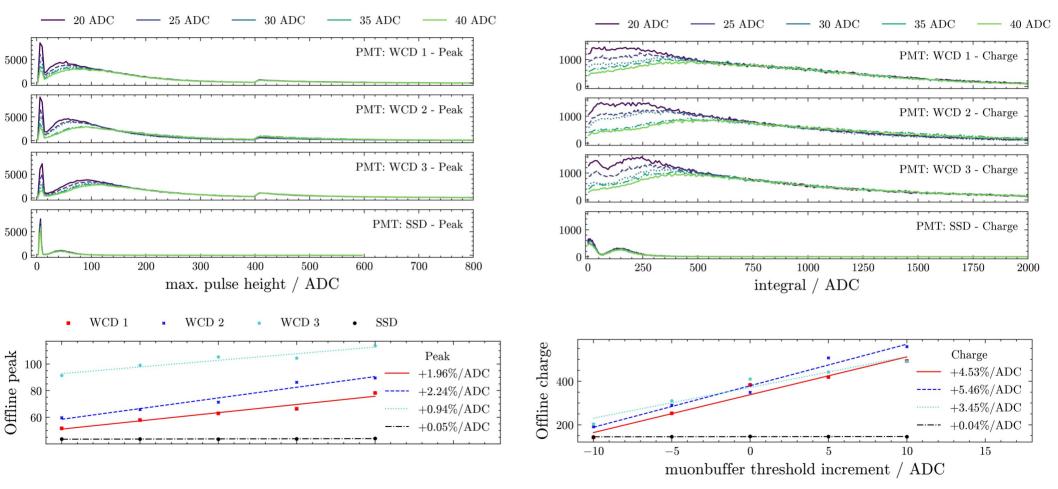
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Need for other testing station

Hard to compare Xb, Muonfill: none reproduce Offline peak!



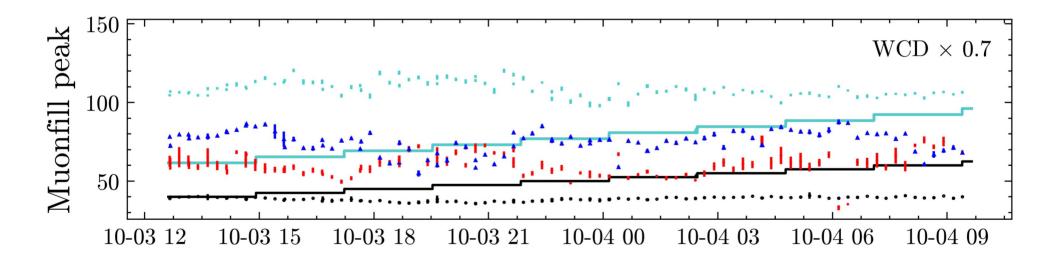
WCD calib dependence on trigger threshold



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Upcoming

- Characterise fluctuations in T70 rates
 - Seem larger than Poissonian in at least 1 PMT, why?
 - Unique to Didi?



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Backup