

Vivado Version 2015.1 and Later Board File Installation

Board Files: Download (<https://github.com/Digilent/vivado-boards/archive/master.zip>)

Description

This guide will help you obtain Vivado Board Files for the Nexys 4, Nexys 4 DDR, Basys 3, Arty, Nexys Video, Zedboard and Zybo FPGA Boards.

Vivado 2014.3

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New Project

Default Part

Choose a default Xilinx part or board for your project. This can be changed later.

Select: ☐ Parts ☒ Boards

Filter

Vendor: All

Display Name: All

Board Rev: Latest

Reset All Filters

Search:

Display Name	Vendor	Board Rev	Part	I/O Pin Count	File Version	Available IOBs	LUT Elements	FlipFlops	Block RAMs	DCI
Basys3 Artix-7 FPGA Board	digilentinc.com	1.1	xc7a35t0pg236-1	236	1.1	106	20800	41600	50	90
Nexys4 FPGA Board	digilentinc.com	1.1	xc7a100t0cg324-1	324	1.1	210	63400	126800	135	24
Nexys4 DDR	digilentinc.com	1.1	xc7a100t0cg324-1	324	1.1	210	63400	126800	135	24
MicroZed Board	em.avnet.com	f	xc7z010d0g400-1	400	1.1	100	17600	35200	60	80
ZedBoard Zynq Evaluation and Development Kit	em.avnet.com	d	xc7z020d0g484-1	484	1.2	200	53200	106400	140	22
Artix-7 AC701 Evaluation Platform	xilinx.com	1.1	xc7a200t0bg676-2	676	1.1	400	134600	269200	365	74
Kintex-7 KC705 Evaluation Platform	xilinx.com	1.1	xc7k325t0fg900-2	900	1.1	500	203800	407600	445	84
Virtex-7 VC707 Evaluation Platform	xilinx.com	1.1	xc7vx485t0fg1761-2	1,761	1.1	700	303600	607200	1030	28
Virtex-7 VC709 Evaluation Platform	xilinx.com	1.0	xc7vx690t0fg1761-2	1,761	1.4	850	433200	866400	1470	36
Virtex UltraScale VCU107 Evaluation Board	xilinx.com	A	xcvu095-fv0d1924-2-e-es1	1,924	1.2	832	537600	1075200	1728	76
ZYNQ-7 ZC702 Evaluation Board	xilinx.com	1.0	xc7z020d0g484-1	484	1.1	200	53200	106400	140	22
ZYNQ-7 ZC706 Evaluation Board	xilinx.com	1.1	xc7z045t0fg900-2	900	1.1	362	218600	437200	545	90

< Back Next > Finish Cancel

What you need before proceeding with this guide

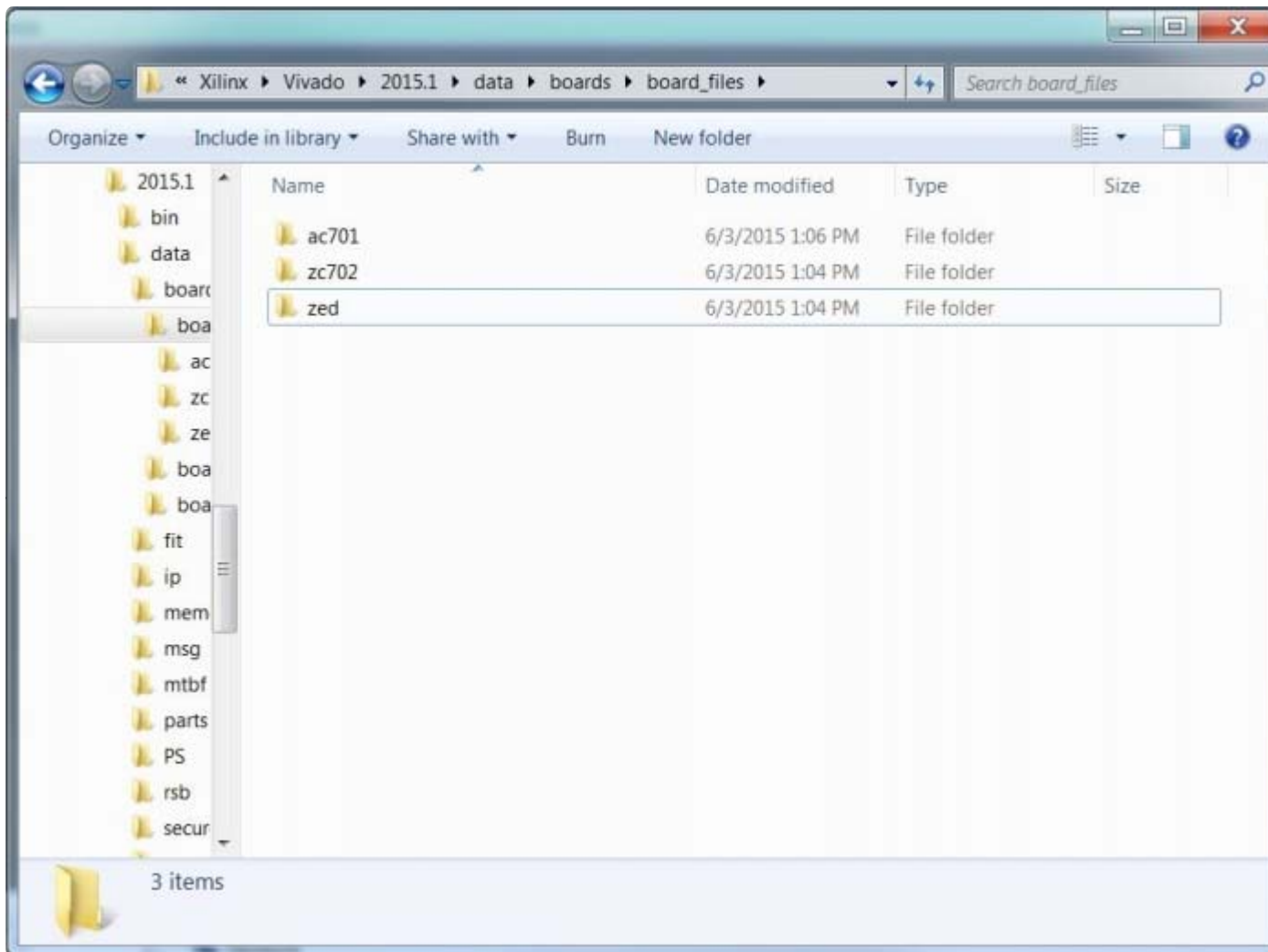
- Xilinx Vivado 2015.1 or later.

Follow this Wiki guide on how to install and activate Vivado on your PC. [Installing Vivado \(https://reference.digilentinc.com/vivado/installation\)](https://reference.digilentinc.com/vivado/installation)

What are Board Files and why do you need them

After installing Vivado, the default installation directory on your drive will contain a folder called **board_files**. If Vivado is installed in the C drive (usually recommended), then the **board_files** folder can be found here: **C:\Xilinx\Vivado\2015.1\data\boards**.

By default this folder contains XML files for different FPGA boards manufactured by Xilinx.



(https://reference.digilentinc.com/_detail/vivado

[/boardfiles2.jpg?id=reference%3Asoftware%3Avivado%3Aboard-files](#))

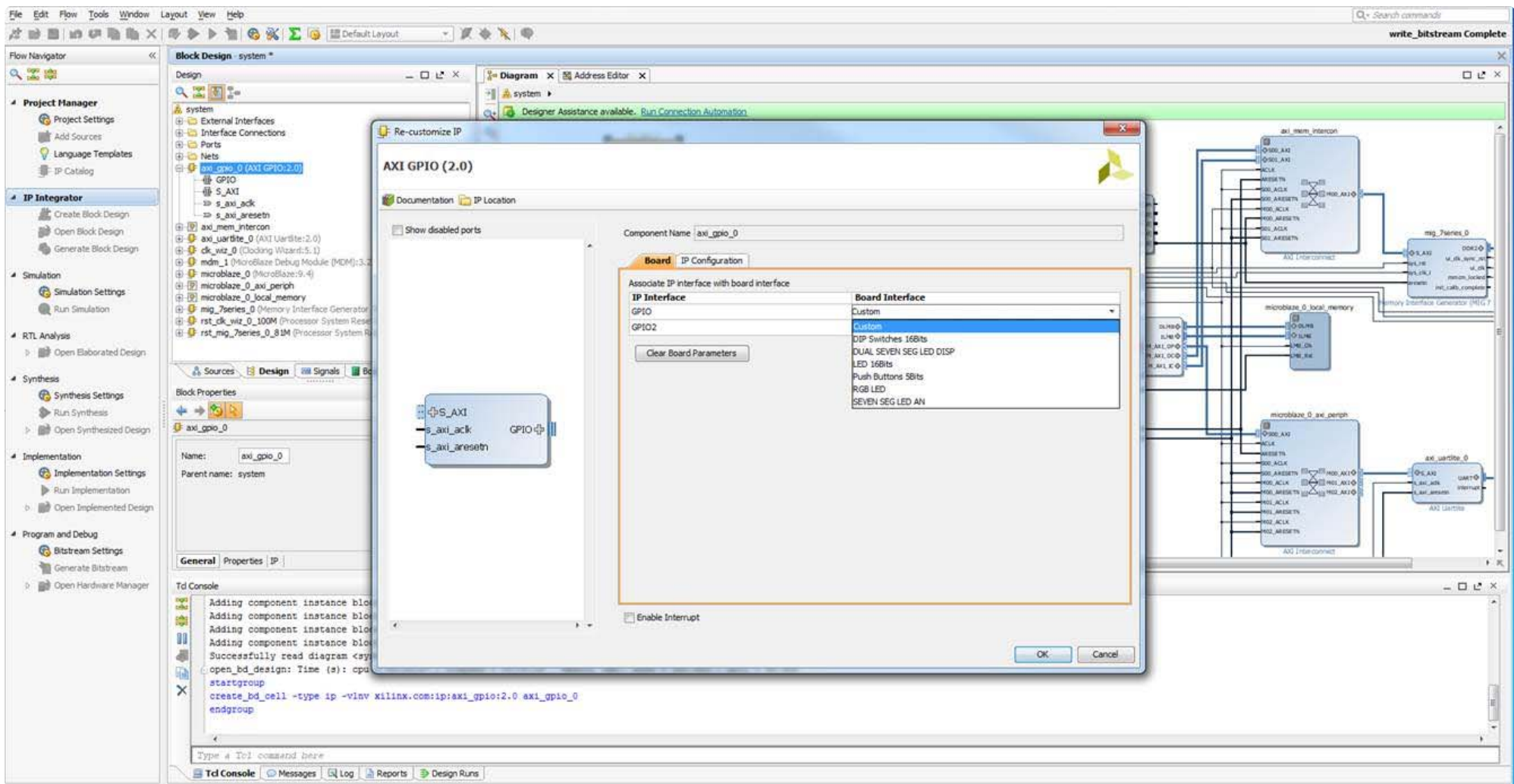
XML files define different interfaces on the board. Interfaces such as Slide Switches, Push Buttons, LEDs, USB-UART, DDR Memory, Ethernet etc.

Digilent has created XML files for the Artix 7 FPGA boards:

- Basys 3
- Nexys 4
- Nexys 4 DDR
- Arty
- Nexys Video

- Zybo
- Zedboard

In the AXI GPIO 0 IP customization window, you will be able to assign different interfaces that are available on your selected board to a specific GPIO 0 IP block.

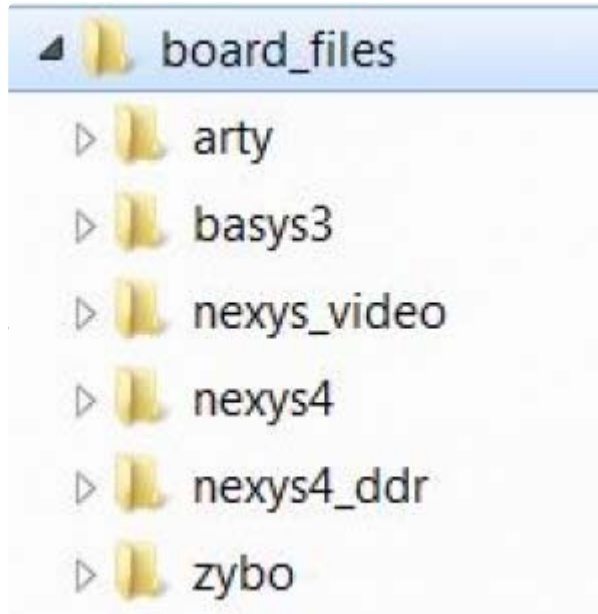


(https://reference.digilentinc.com/_detail/vivado/vivado_bsp_files_6.jpg?id=reference%3Asoftware%3Avivado%3Aboard-files)

Installation

Download and extract this Zip file: <https://github.com/Digilent/vivado-boards/archive/master.zip> (<https://github.com/Digilent/vivado-boards/archive/master.zip>)
You can also check out the repository for the board files on github located [here](https://github.com/Digilent/vivado-boards/) (<https://github.com/Digilent/vivado-boards/>)

This zip file will contain a folder called *new/board_files*. Save this in your user documents folder. We will copy this *board_files* folder, navigate to the **board_files** folder inside the Vivado Installation directory, and merge them both.



(https://reference.digilentinc.com/_detail/vivado/boardfiles1.jpg?id=reference%3Asoftware%3Avivado%3Aboard-files)

- Copy the contents of the *board_files* folder
- Navigate to the **board_files** folder in the Vivado Installation directory (C:\Xilinx\Vivado\2015.1\data\boards\board_files)
- Paste the contents into the board_files folder
- Restart Vivado

The newly added files will each contain a sub-folder for the current board revision. This sub-folder contains the respective XML files for each FPGA board.

The sub-folder for the **nexys4_dds** will contain an additional file called **mig.prj** which is the Xilinx Memory Interface Generator description file for customizing the DDR2 component on the **Nexys 4 DDR**.

You are now ready to start a new IP Integrator based Vivado project for the Digilent Nexys 4, Nexys 4 DDR, Zybo, Zedboard and Basys 3 FPGA Boards.

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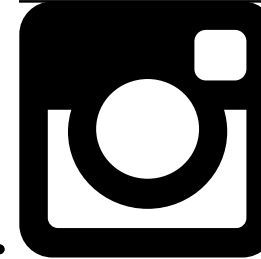
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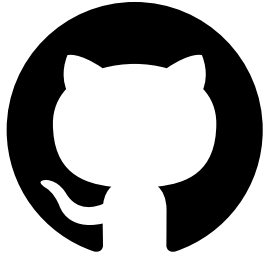
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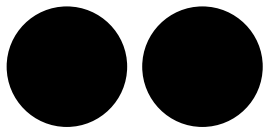
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