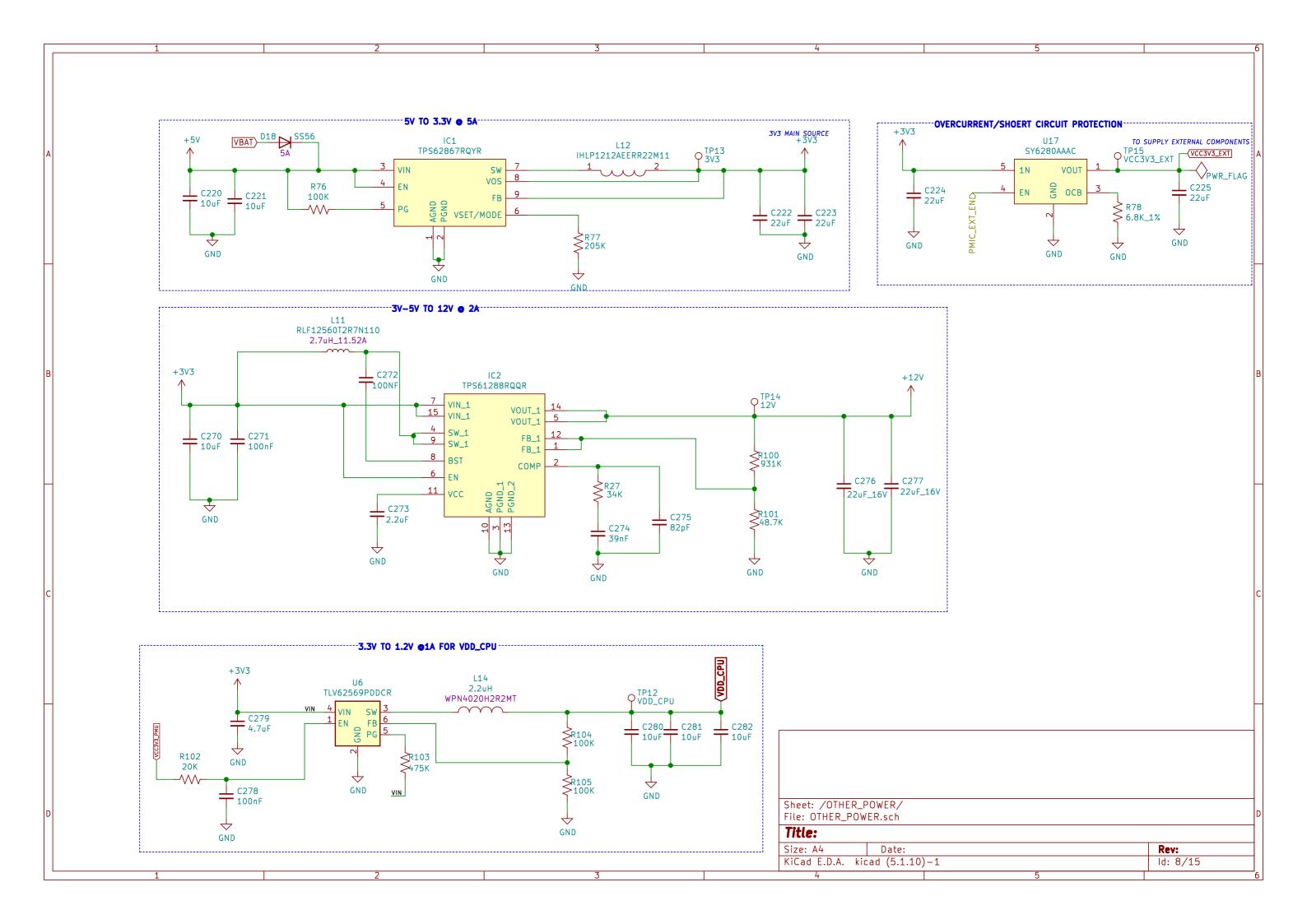
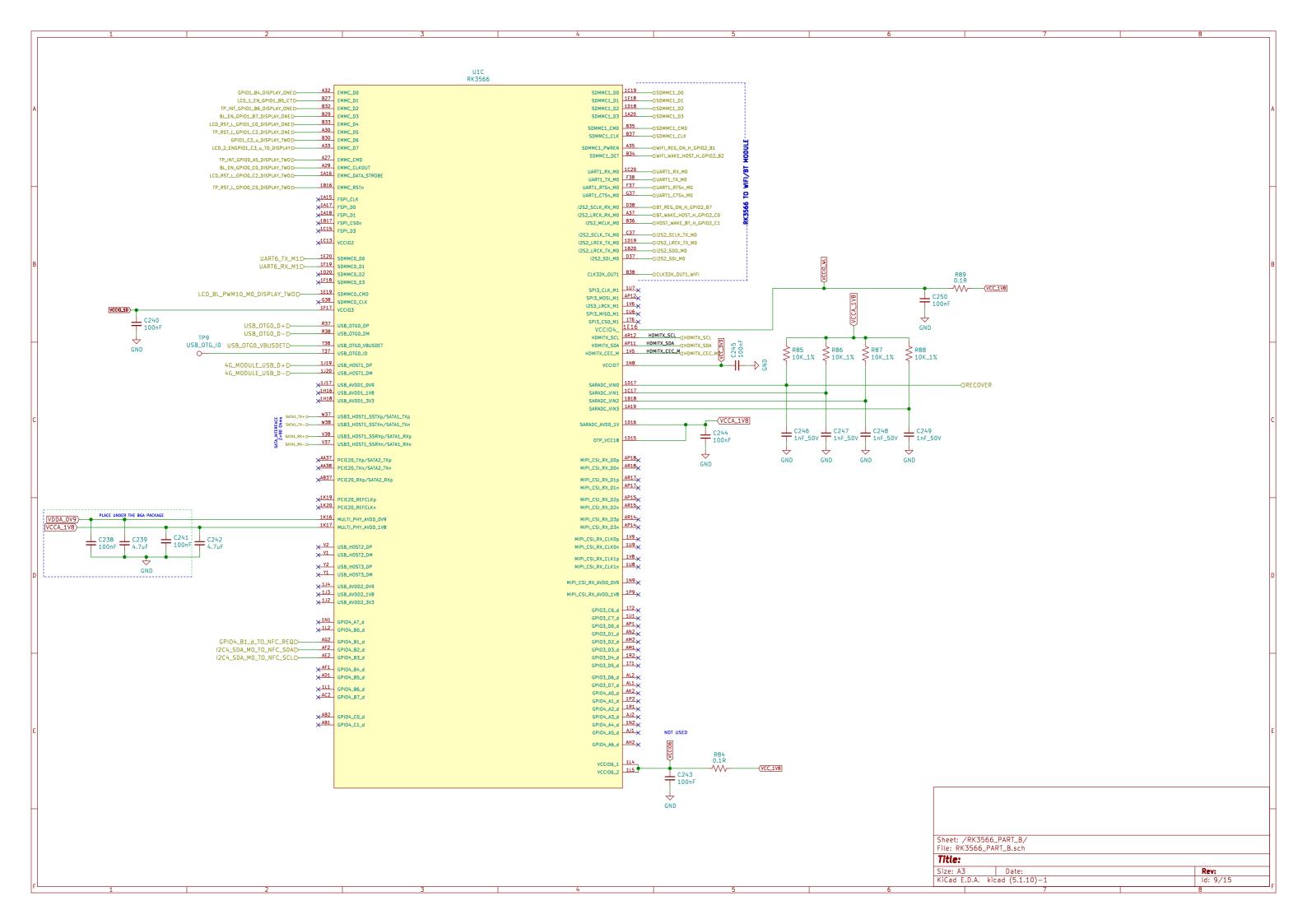
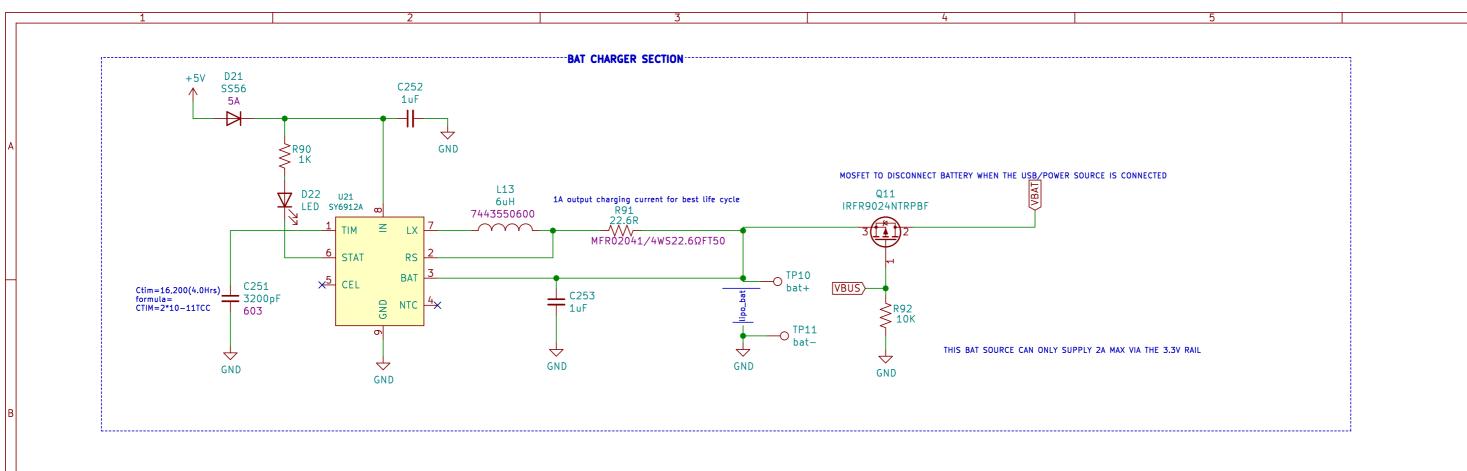
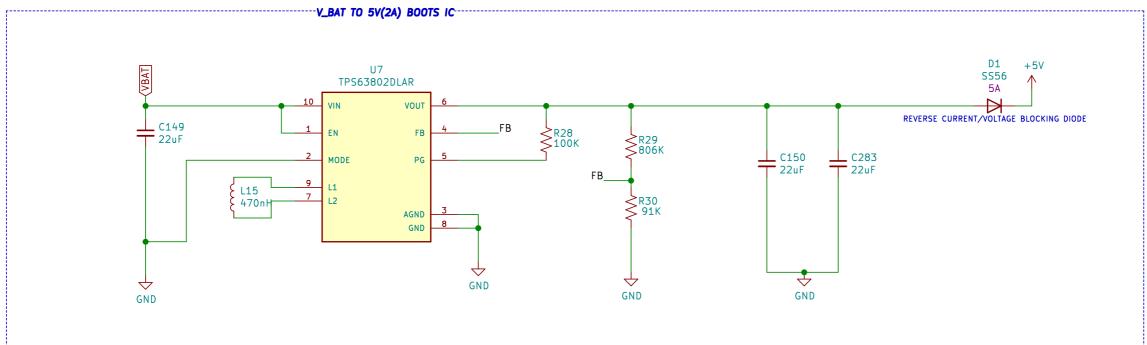


LPDDR3_RAM POWER F1 VDD1_1 F12 VDD1_2 G4 VDD1_3 G9 VDD1_4 T9 VDD1_5 U1 VDD1_7 VDD1_8 VCC1V8_DDR VSS_1 A3 VSS_2 A10 VSS_3 C1 VSS_3 C5 VSS_6 C8 VSS_6 C12 VSS_7 D2 VSS_8 D9 VSS_10 E1 VSS_11 E1 VSS_12 E5 VSS_13 E12 VSS_14 G3 VSS_16 G3 VSS_16 G3 VSS_16 G3 VSS_16 G3 VSS_17 G5 VSS_18 G10 VSS_19 G10 VSS_19 G10 VSS_19 G10 VSS_10 G12 VSS_20 J11 VSS_22 J3 VSS_23 J112 VSS_26 K2 VSS_27 K2 VSS_28 K11 VSS_26 K4 VSS_27 K9 VSS_28 VSS_26 VSS_26 VSS_29 VSS_26 V1 VSS_49 VSS_50 VSS_49 VSS_50 AB3 VSS_56 AB3 VSS_56 AB3 VSS_56 AB8 VSS_57 AB8 VCC_DDR DECOUPLING CAPACITORS C101 22uF C104 C109 C109 D8 VDDQ_7 D12 VDDQ_8 F3 VDDQ_9 F10 VDDQ_10 U3 VDDQ_11 U10 VDDQ_12 W1 VDDQ_13 W5 VDDQ_14 W8 VDDQ_15 VDQ_15 AA3 VDDQ_15 AA5 VDDQ_18 AA6 VDDQ_19 AA10 VDDQ_20 VCC_DDR DECOUPLING CAPACITORS VCCOV6_DDR C138 C140 C142 100nF 100nF 100nF C144 C147 C103 C100 C102 C102 A4 VDD2_1 A9 VDD2_2 F5 VDD2_3 F8 VDD2_3 H1 VDD2_5 H5 VDD2_6 VDD2_6 H8 VDD2_7 VCC_DDR) VCC1V8_DDR H8 VDD2_7 VDD2_8 H12 VDD2_9 K10 VDD2_11 K10 VDD2_11 N1 VDD2_12 N1 VDD2_13 VDD2_15 N12 VDD2_15 N12 VDD2_16 R1 VDD2_16 R8 VDD2_19 R12 VDD2_18 R8 VDD2_19 R12 VDD2_20 UDS VDD2_21 UB AB4 VDD2_23 AB9 VDD2_24 C106 C131 C260 C261 C262 C263 C264 C265 T00nF T00nF T00nF T00nF C266 C267 C131 3.3V TO 0.6V@2A GND +3V3 L10 2.2uH WPN4020H2R2MT U5 TLV62569PDDCR ______ C107 4.7uF C122 C124 22uF 22uF VCCA1V8_PMU GND R25 20K DNU_7 DNU_8 AA12 AA12 AB12 AB12 DNU_10 DNU_11 AB12 DNU_12 ×A1 ×A2 ×A11 DNU_1 DNU_2 ×A11 DNU_3 ×A12 DNU_4 ×B1 DNU_5 DNU_6 C105 100nF GND GND Sheet: /RK3566_LPDDR3/LPDDR4_POWER/ File: LPDDR4_POWER.sch Title: Size: A3 Date: KiCad E.D.A. kicad (5.1.10)-1









Sheet: /BATTERY_CHARGER/
File: BATTERY CHARGER.sch

Title:	

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