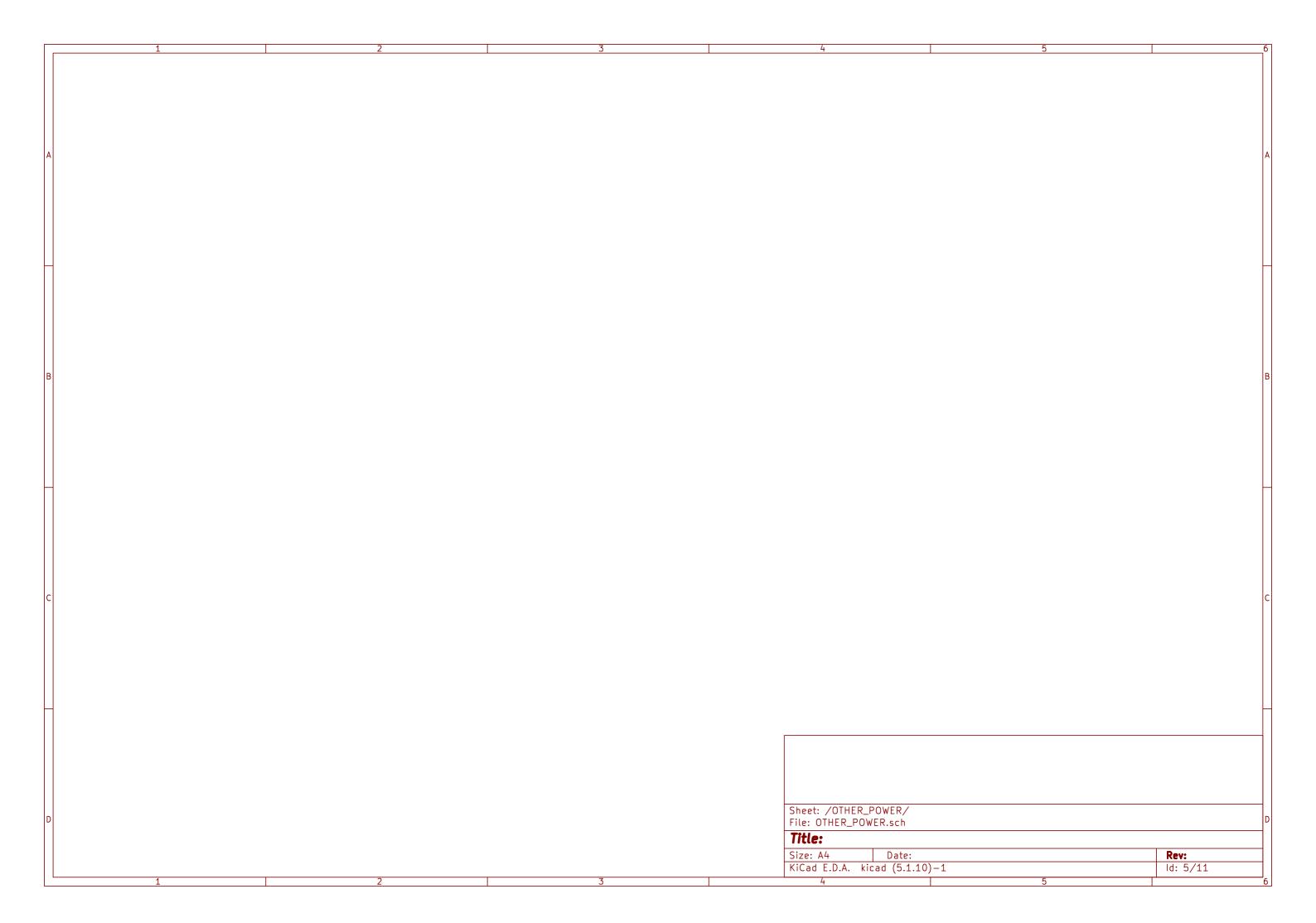
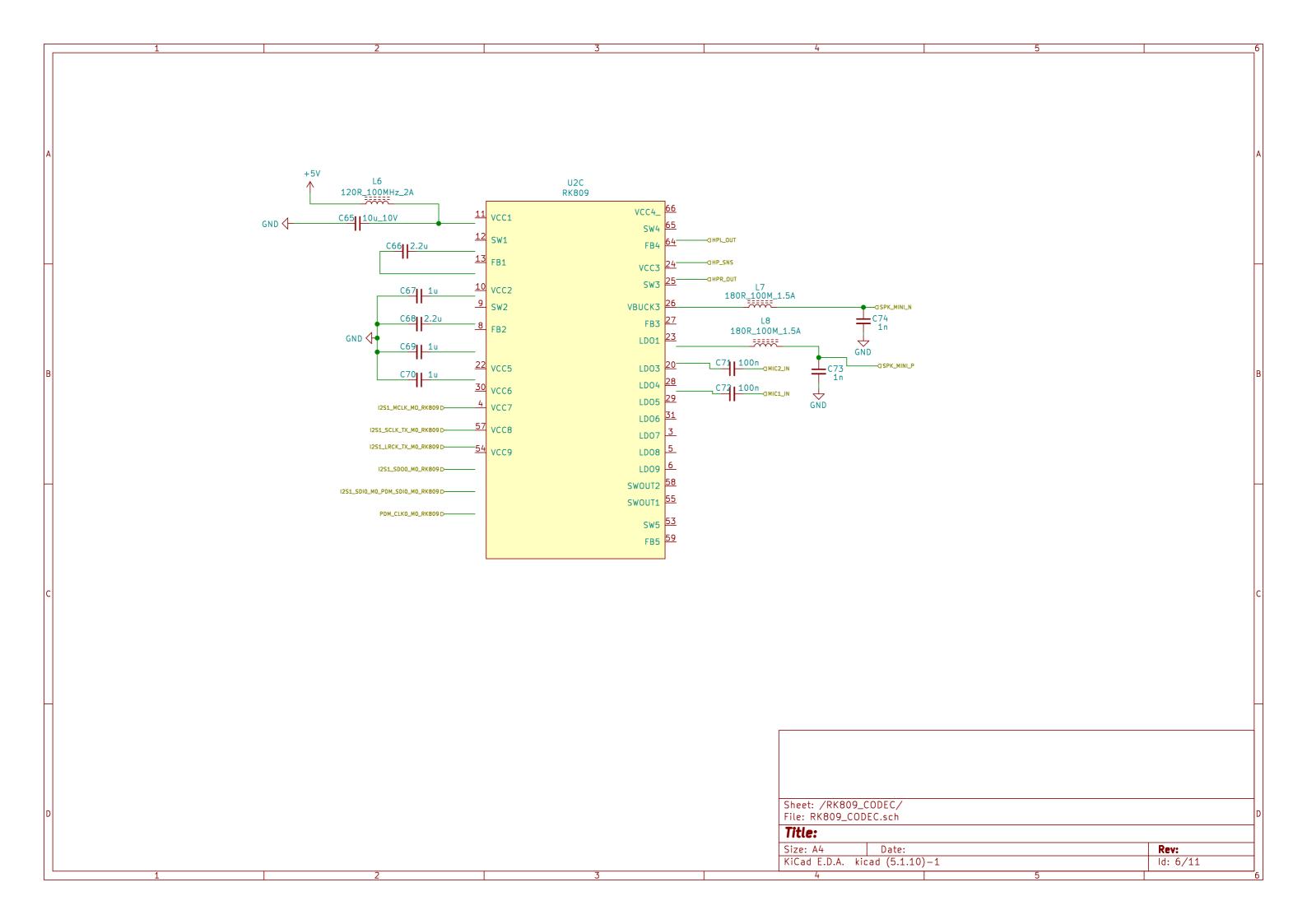
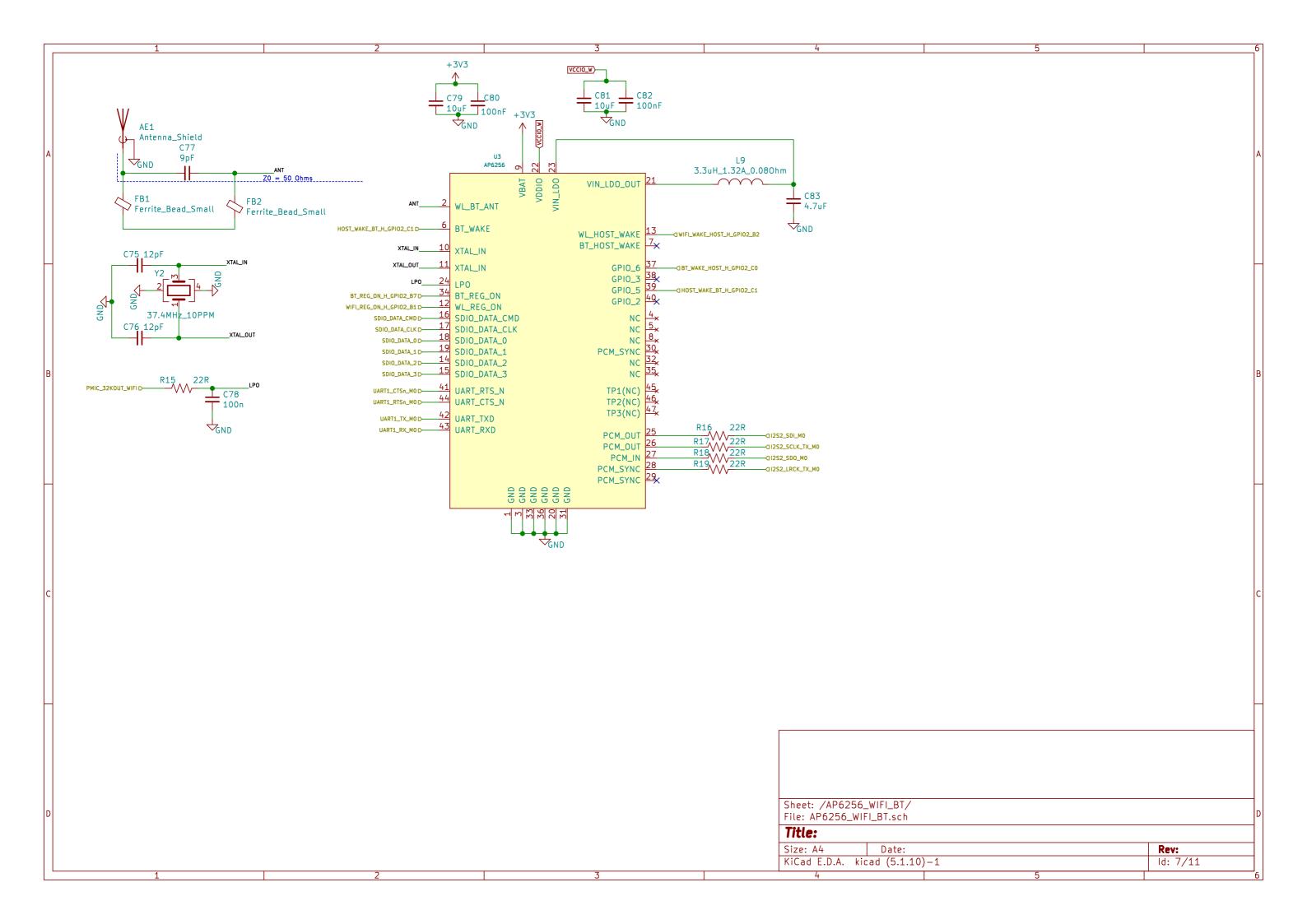


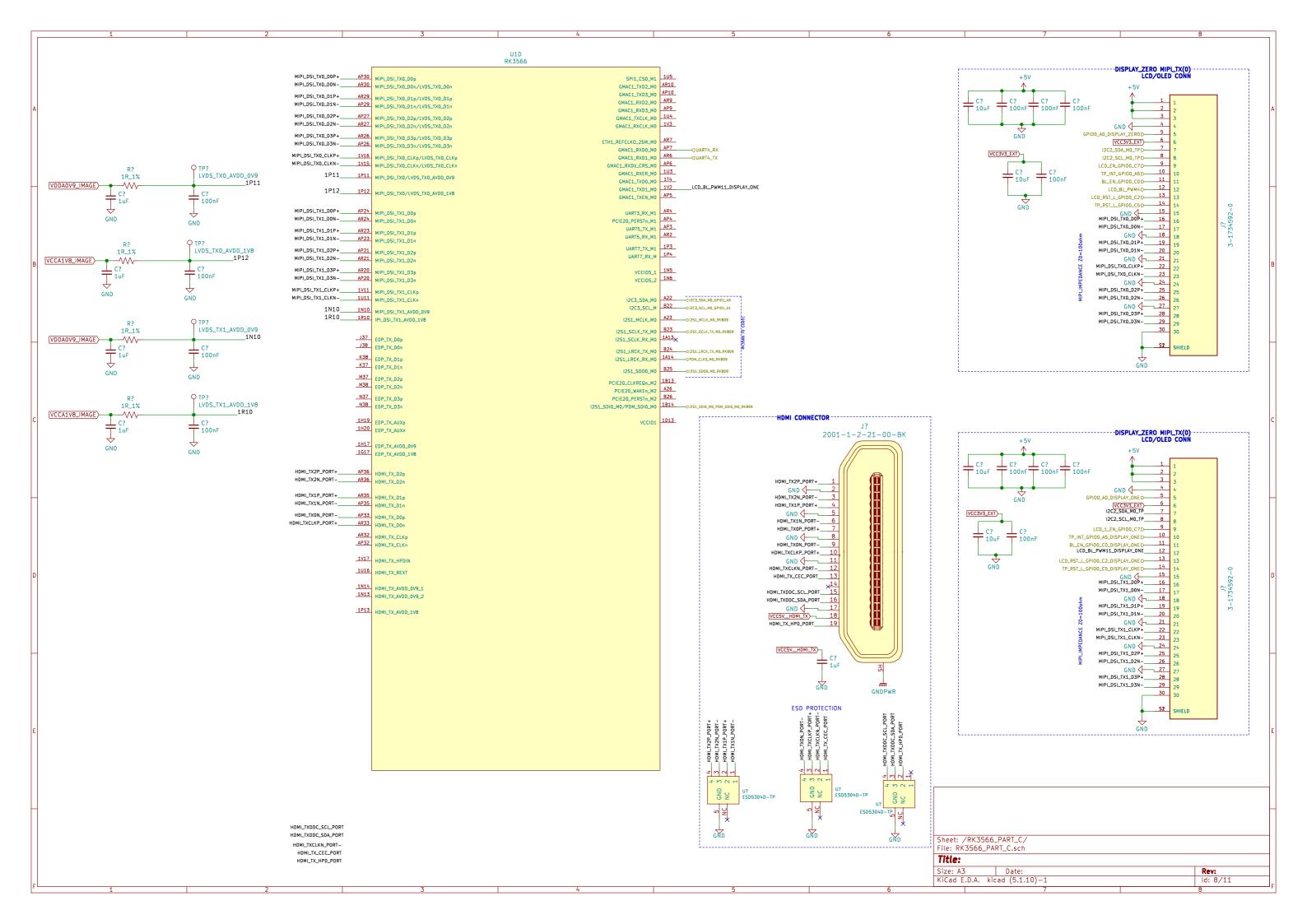
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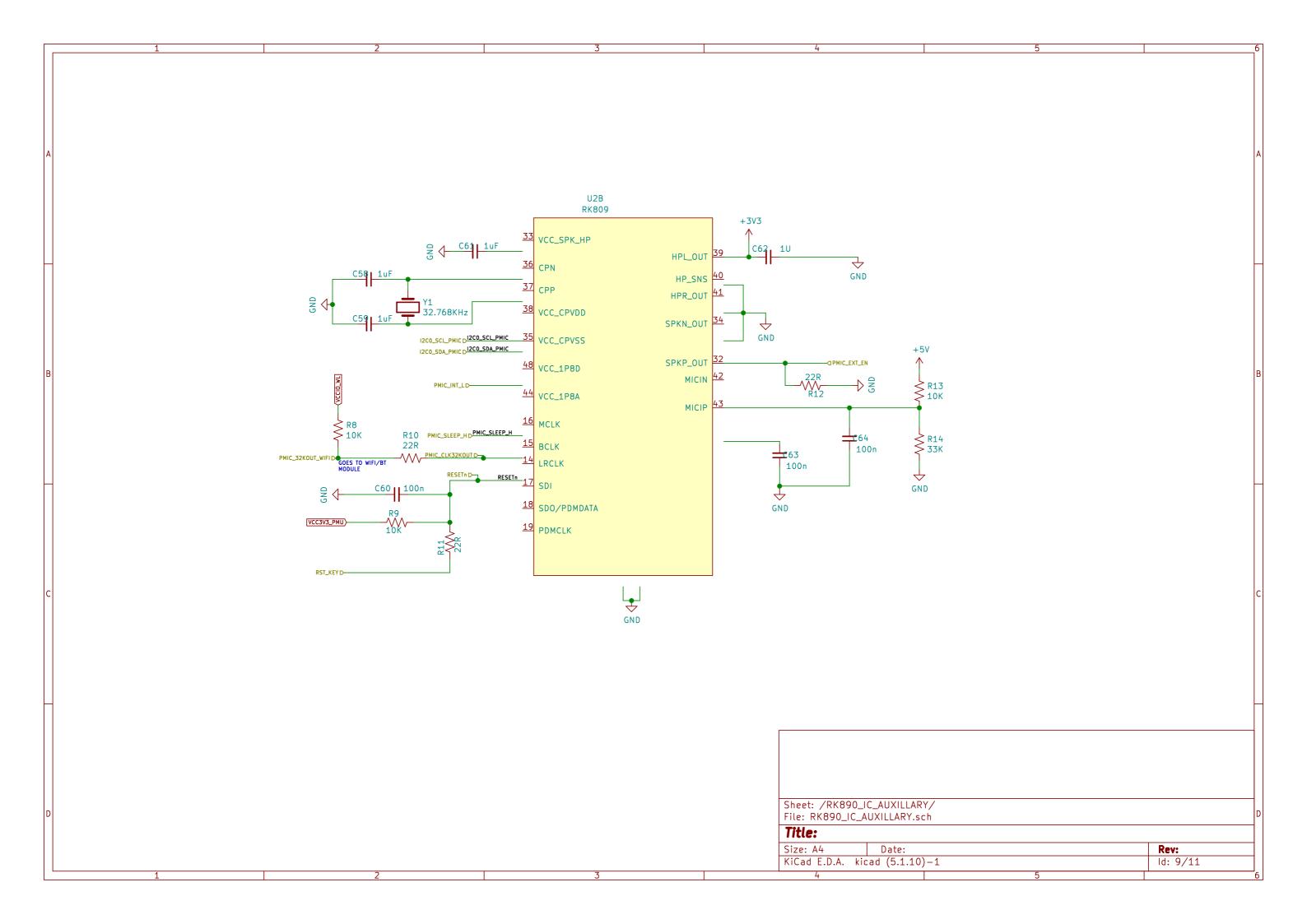
		U1C R\$3566					
	CDION DV DICTUM ONLY	A32		50,000	1C19	######################################	
	GPIO1_B4_DISPLAY_ONED	B27	EMMC_D0 EMMC_D1	SDMMC1_D0 SDMMC1_D1	1E18	—GSDMMC1_D0 —GSDMMC1_D1	
١.	LCD_1_EN_GPI01_B5_C7D	B32					
l٩	TP_INT_GPIO1_B6_DISPLAY_ONE D	B29	EMMC_D2	SDMMC1_D2	4	—QSDMMC1_D2	
	BL_EN_GPI01_B7_DISPLAY_ONED	B33	EMMC_D3	SDMMC1_D3	INEU	——QSDMMC1_D3	
	LCD_RST_L_GPI01_C0_DISPLAY_ONED	A30	EMMC_D4	SDMMC1_CMD	B35	GSDMMC1_CMD	
	TP_RST_L_GPI01_C2_DISPLAY_ONE D	B30	EMMC_D5	SDMMC1_CLK	B37		
	eMMC_DO_FLASH_DO D	A33	EMMC_D6		A35	"	
	eMMC_D1_FLASH_D1 D		EMMC_D7	SDMMC1_PWREN	B34	—	
	eMMC_D2_FLASH_D2_D	_A27	EMMC_CMD	SDMMC1_DET	551	—	
	eMMC_D3_FLASH_D3.D	A29	EMMC_CLKOUT		1C20		
	eMMC_D4_FLASH_D4D	1A16	EMMC_DATA_STROBE	UART1_RX_M0		—UART1_RX_M0	
	eMMC_D5_FLASH_D5.D	1B16	THUS DOT	UART1_TX_M0		—	
Г	eMMC_D6_FLASH_D6 D		EMMC_RSTn	UART1_RTSn_M0 UART1_CTSn_M0	G37		
	eMMC_D7_FLASH_D7 D	1A15	FSPI_CLK	UARII_CISh_MU			
	eMMC_CMD_FLASH_WRn D	1A17	FSPI_D0	I2S2_SCLK_RX_M0		— □ BT_REG_ON_H_GPI02_B7 99 99 99 99 99 99 99 99 99 99 99 99 99	
		1A18	FSPI_D1	I2S2_LRCK_RX_M0		—⟨JBT_WAKE_HOST_H_GPI02_C0	
		_1B17	FSPI_CS0n	I2S2_MCLK_M0	B36	—∕□HOST_WAKE_BT_H_GPI02_C1	
		1C15	FSPI_D3	I2S2_SCLK_TX_M0	C37	—	
		1C13	VCCIO2	I2S2_LRCK_TX_M0		— ☐1252_LRCK_TX_M0	
			1002	I2S2_LRCK_TX_M0		──☐1252_SD0_M0	
		1E20	SDMMCO_DO	1252_ERCK_TX_M0		—□1252_SDI_M0	
В		1F19	SDMMCO_D1	1232_301_110		G1232_30_H0	
		1D20	SDMMCO_D2	CLK32K_OUT1	B38	—⟨JCLK32K_OUT1_WIFI	
		1F18	SDMMCO_D3	CERSZR_0011		GCERSZR_0011_WIII	
				SPI3_CLK_M1	107		
		1E19	SDMMCO_CMD	SPI3_MOSI_M1	AP12		
		G38	SDMMCO_CLK	12S3_LRCK_M1	176		
		1F17	VCCI03	SPI3_MISO_M1	106		
		R37		SPI3_CS0_M1	176		
			USB_OTGO_DP				
		R38	USB_OTGO_DM	HDMITX_SCL	AR12		
H		_T38	USB_OTGO_VBUSDET	HDMITX_SDA	AP11		
		T37	USB_OTGO_ID	HDMITX_CEC_M	1V5		
		1J19			1N8		
		1J20	USB_HOST1_DP USB_HOST1_DM	VCCI07	1110		
		1J17	USB_AVDD1_0V9	SARADC_VINO	1017		
		1H16	USB_AVDD1_1V8	SARADC_VIN1	1D17		
		1H18	USB_AVDD1_3V3	SARADC_VIN2 SARADC_VIN3	1818		
		W37		SARADC_VIN3	1017		
c	SATA1_TX+ D	W38	USB3_HOST1_SSTXp/SATA1_TXp USB3_HOST1_SSTXn/SATA1_TXn	SARADC_AVDD_1V	1D16		
				SARADC_AVDD_IV			
	SATA1_RX+D—	V38	USB3_HOST1_SSRXp/SATA1_RXp	OTP_VCC18	1D15		
	SATA1_RX - D	V37	USB3_HOST1_SSRXn/SATA1_RXn	011_10010			
		AA37			AP18		
		AA38	PCIE20_TXp/SATA2_TXp	MIPI_CSI_RX_D0p	AR18		
			PCIE20_TXn/SATA2_TXn	MIPI_CSI_RX_D0n			
		AB37	PCIE20_RXp/SATA2_RXp	MIPI_CSI_RX_D1p	AR17		
		AB38	PCIE20_RXn/SATA2_RXn	MIPI_CSI_RX_D1n	AP17		
H		1K19	PCIE20_REFCLKp	MIPI_CSI_RX_D2p	AP15		
		1K20	PCIE20_REFCLKn	MIPI_CSI_RX_D2n	AR15		
		1K16	MULTI_PHY_AVDD_0V9	MIPI_CSI_RX_D3p	AR14		
		1K17	MULTI_PHY_AVDD_1V8	MIPI_CSI_RX_D3p	AP14		
			10010 1101002110		1/9		
		V2	USD USSTER OR	MIPI_CSI_RX_CLKOp	109		
		V1	USB_HOST2_DP	MIPI_CSI_RX_CLKOn			
			USB_HOST2_DM	MIPI_CSI_RX_CLK1p	1/8		
		Y2	USB_HOST3_DP	MIPI_CSI_RX_CLK1n	108		
D		Y1	USB_HOST3_DM		1N0		
		_1J4		'LCSI_RX_AVDD_0V9			
		_1J3	USB_AVDD2_1V8 MIF	I_CSI_RX_AVDD_1V8	1P9		
		1J2	USB_AVDD2_3V3				
				GPI03_C6_d			
		1N1	GPI04_A7_d	GPI03_C7_d			
		_1L2	GPI04_B0_d	GPI03_D0_d			
		AG2		GPI03_D1_d	l		
		AF2	GPI04_B1_d	GPI03_D2_d			
H		AE2	GPI04_B2_d	GPI03_D3_d			
			GPI04_B3_d	GPI03_D4_d	171		
		AF1	GPI04_B4_d	GPI03_D5_d	l		
1		AD1	GPI04_B5_d	GPI03_D6_d			
1		111	CDIO/, R6 d	GPI03_D7_d			
1		AC2	GPI04_B7_d	GPI04_A0_d			
				GPIO4_A1_d	191		
		AB2		GPI04_A2_d	AJ2		
		_AB1	GPI04_C0_d GPI04_C1_d	GPI04_A3_d GPI04_A4_d	1N2		
F			GF104_CI_0	GP104_A4_d GP104_A5_d	AJ1		
ľ							
				GPI04_A6_d	AH2		
				VCCI06_1			
				VCC106_2	1L5_		
					-		
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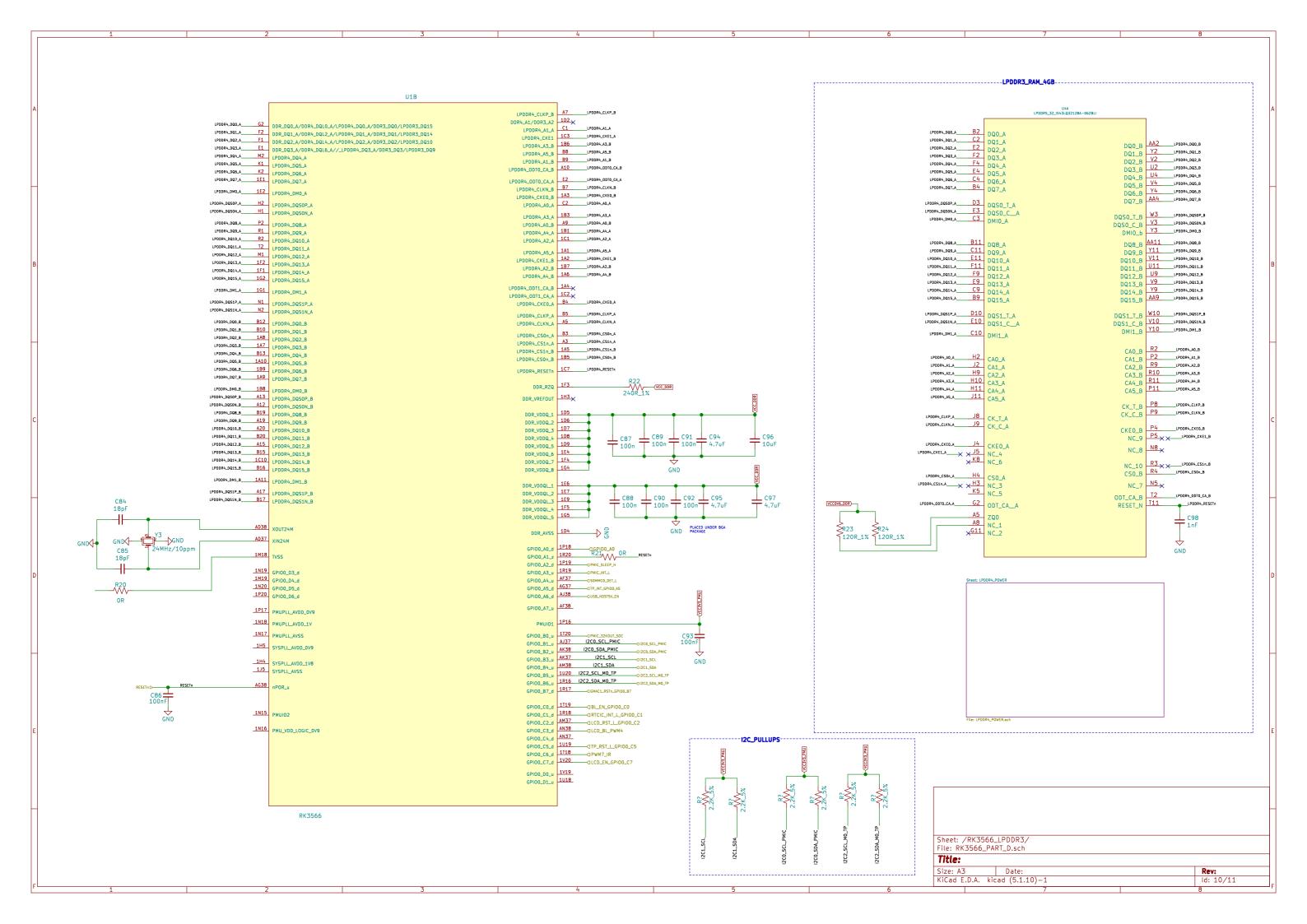












LPDDR3_RAM POWER F1 VDD1_1 F12 VDD1_2 G4 VDD1_3 G9 VDD1_4 T9 VDD1_5 U1 VDD1_7 VDD1_8 VCC1V8_DDR VSS_1 A3 A10 VSS_2 C1 VSS_3 C5 VSS_6 C8 VSS_5 D4 VSS_6 D9 VSS_10 D11 VSS_11 E1 VSS_12 E8 VSS_15 G3 VSS_14 C5 VSS_15 G6 VSS_17 G5 VSS_18 G6 VSS_19 G10 VSS_20 J1 VSS_21 J1 VSS_21 J1 VSS_22 J1 VSS_23 VSS_23 VSS_23 VSS_24 VSS_25 K2 VSS_26 VSS_27 VSS_28 K11 VSS_27 VSS_28 VSS_28 VSS_29 VSS_28 VSS_49 VSS_50 VSS_40 VSS_50 ABB C103 C106 TuF 100nF VCC_DDR DECOUPLING CAPACITORS VCC_DDR) C101 22uF C104 C109 C109 VCC_DDR DECOUPLING CAPACITORS VCCOV6_DDR C108 C110 C112 C114 C116 C118 C120 1UF 100nF 100nF 100nF 100nF C132 C134 C136 C138 C140 C142 C144 C100nF T100nF T100nF T100nF C100 C102 C102 C125 _ C127 C129 A4 VDD2_1 A9 VDD2_2 F5 VDD2_3 F8 VDD2_4 H1 VDD2_5 H5 VDD2_6 VDD2_6 H8 VDD2_7 H12 VDD2_7 VCC_DDR) 3.3V TO 1.8V@2A H8 VDD2_7 VDD2_8 H12 VDD2_9 K10 VDD2_11 K10 VDD2_11 N1 VDD2_12 N1 VDD2_13 VDD2_15 N12 VDD2_15 N12 VDD2_16 R1 VDD2_16 R8 VDD2_19 R12 VDD2_18 R8 VDD2_19 R12 VDD2_20 UDS VDD2_21 UB AB4 VDD2_23 AB9 VDD2_24 3.3V TO 0.6V@2A L11 2.2uH WPN4020H2R2MT +3V3 TLV62569DDC L10 2.2uH U5 TLV62569DDC WPN4020H2R2MT C146 C148 ₹R26 200K ± C107 +4.7uF GND GND R25 20K GND ₹R27 100K GND GND + C105 GND GND GND DNU_7 DNU_8 AA12 AA12 AB12 AB12 DNU_10 DNU_11 AB12 DNU_12 ×A1 ×A2 ×A11 DNU_1 DNU_2 ×A11 DNU_3 ×A12 DNU_4 ×B1 DNU_5 DNU_6 Sheet: /RK3566_LPDDR3/LPDDR4_POWER/ File: LPDDR4_POWER.sch Title: Size: A3 Date: KiCad E.D.A. kicad (5.1.10)-1