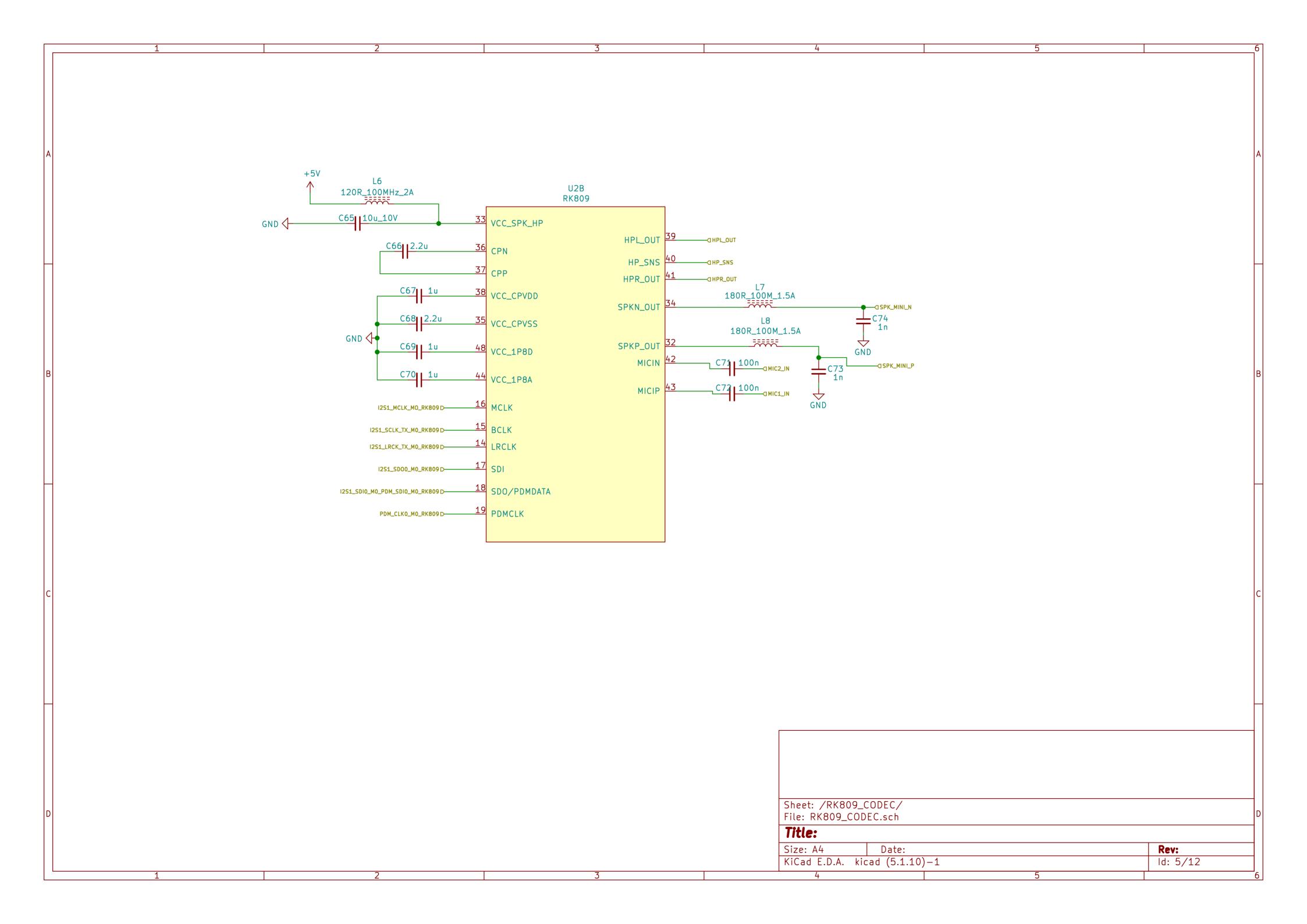
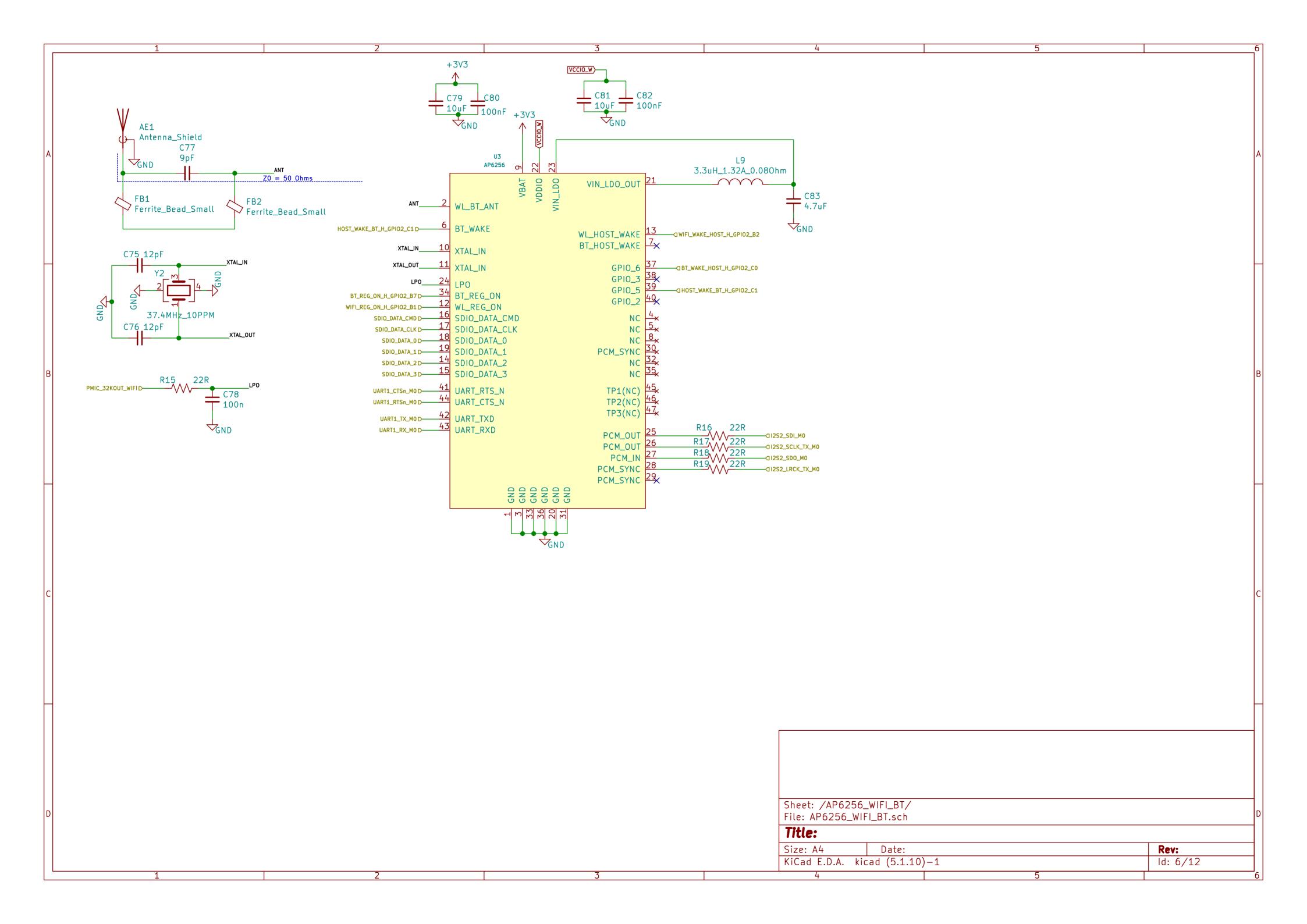
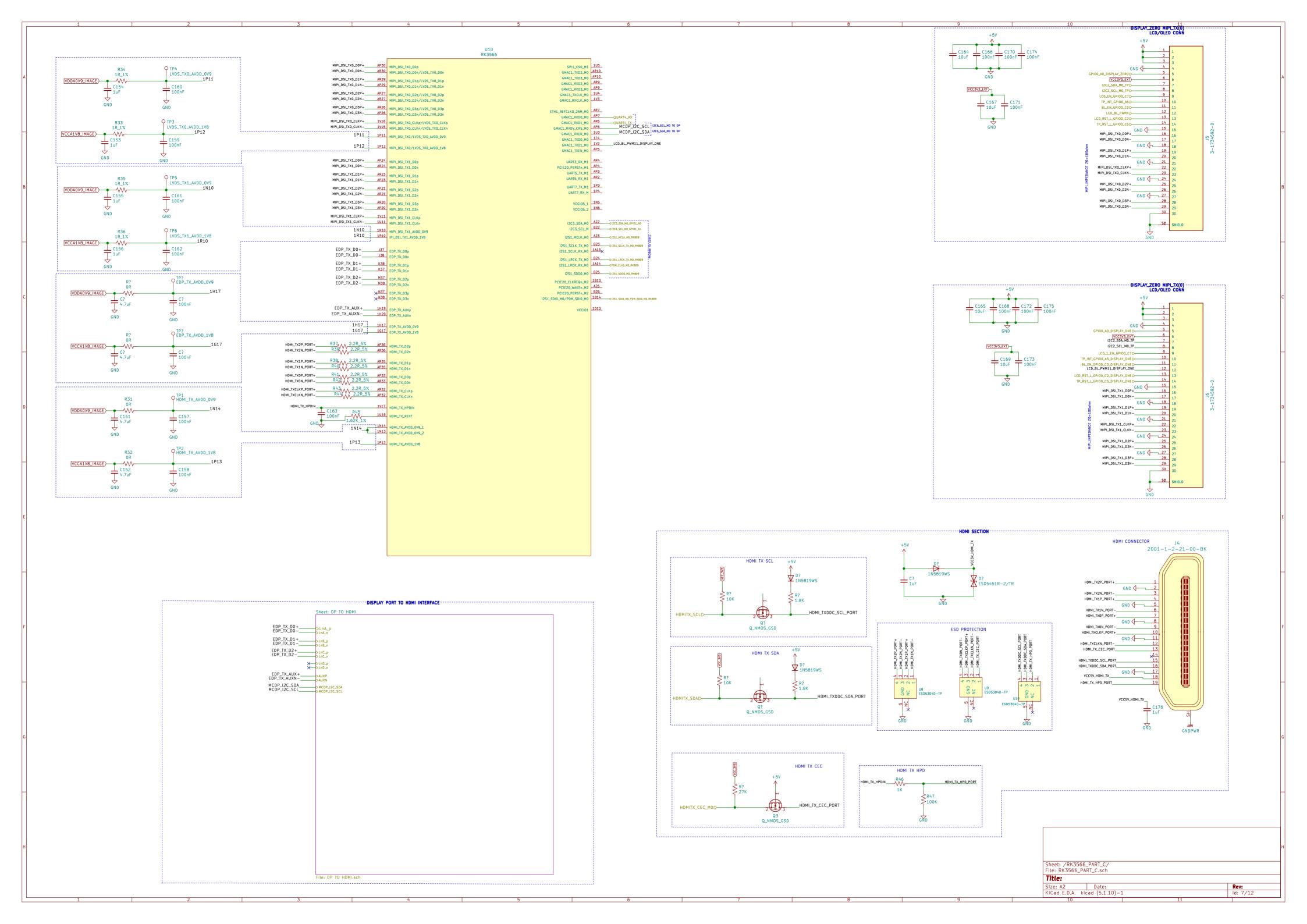


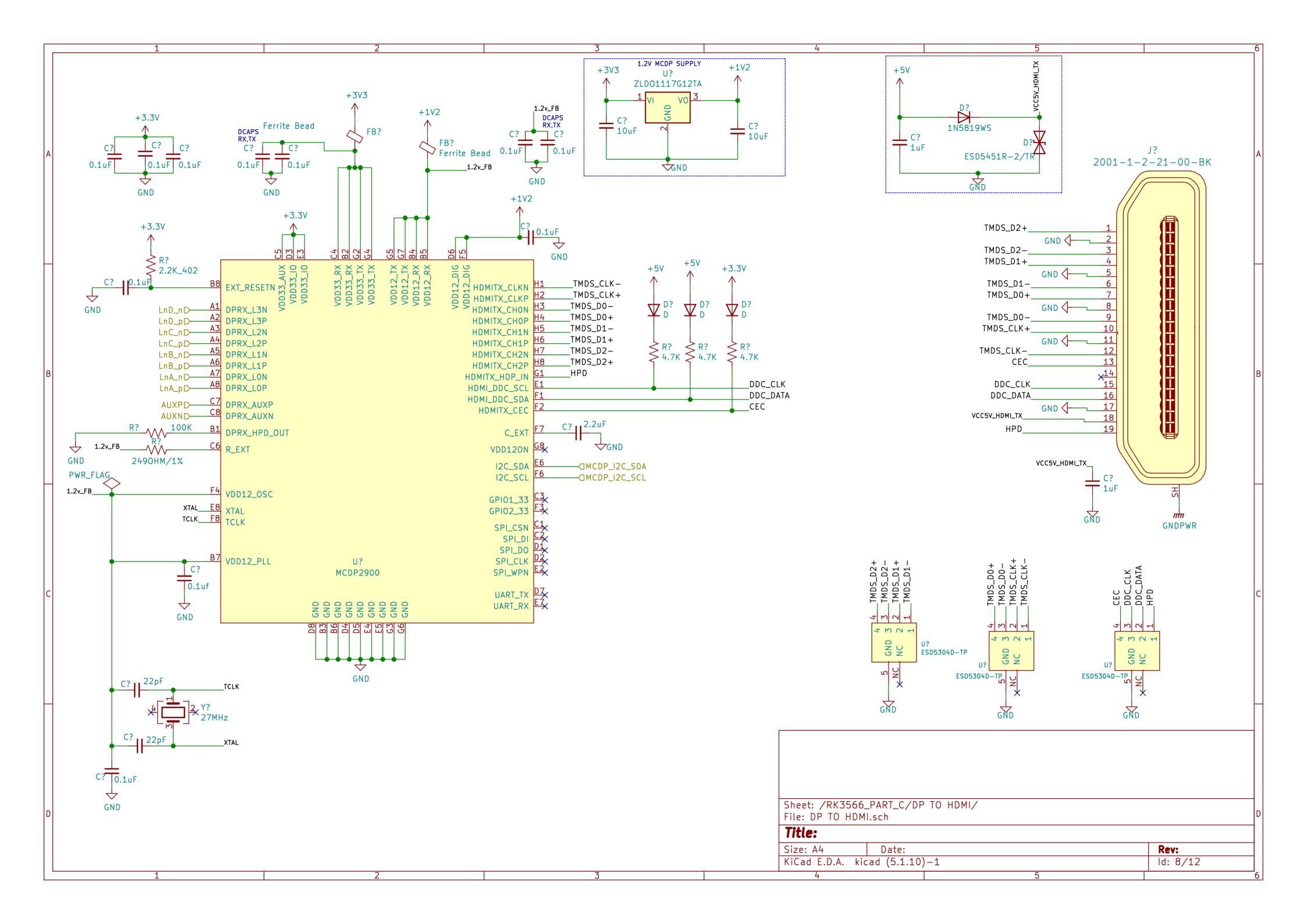
U1C RK3566

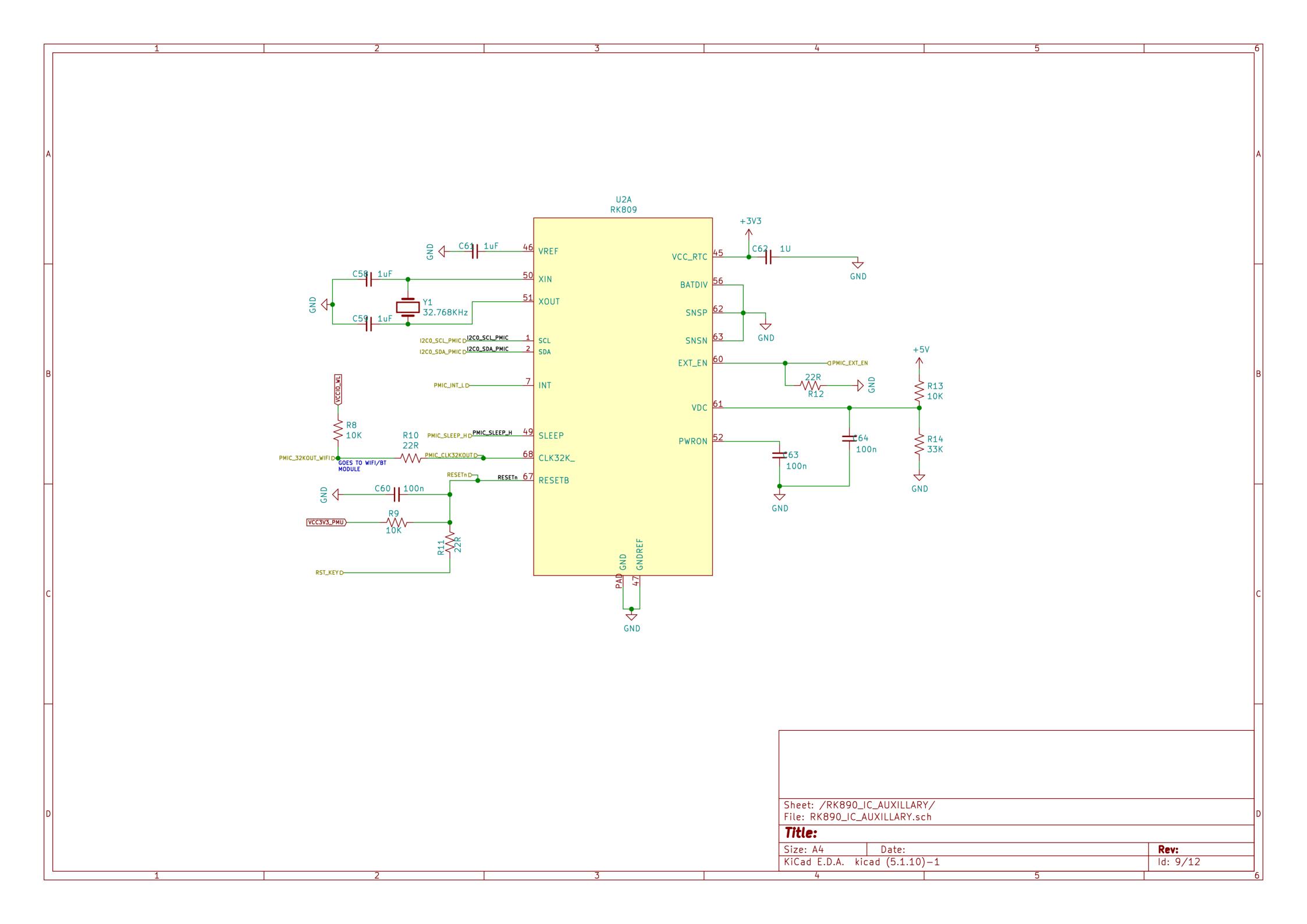
	RK3566	
GRIO1 R/ DISPLAY ONED A32	ENVICED CONTROL DO	1C19 GERMACA DO
GFIOT_B4_DISFLAT_ONED	- EMMC_D0	1519
ECD_T_EN_GFIOT_B3_C7D	EMMC_DI SUMMCI_DI SUMMCI_DI SUMMCI_DI	4040
TP_INT_GPI01_B6_DISPLAY_ONE D B32 BL_EN_GPI01_B7_DISPLAY_ONE D B29	EMMC_D3 SDMMC1_D3	1516 — QSDMMC1_D2 1A20 — QSDMMC1_D3
LCD_RST_L_GPI01_C0_DISPLAY_ONE D B33	EMMC_D3	J3DMMCI_D3
TP_RST_L_GPI01_C2_DISPLAY_ONE D A30	SDMMC1_CMD	B35 SDMMC1_CMD
B30	SDMMC1_CLK	B37 SDMMC1_CLK
eMMC_DO_FLASH_DO D	EMMC_D7 SDMMC1_PWREN	A35
emmC_D1_FLASH_D1 D	SDMMC1_DET	A35
eMMC_D2_FLASH_D2_D A27	EMMC_CMD	Z WII [_WARE_ITOST_IT_OF TOZ_DZ
eMMC_D3_FLASH_D3 D	EMMC_CLKOUT UART1_RX_MO	1C20 UART1_RX_M0
eMMC_D4_FLASH_D4 D	EMMC_DATA_STROBE UART1_TX_MO	1C20
eMMC_D5_FLASH_D5_D		F37 QUART1_RTSn_M0
eMMC_D6_FLASH_D6 D	LIART1 CTSn MO	G37 QUART1_CTSn_M0
eMMC_D7_FLASH_D7 D	FSPI_CLK	
eMMC_CMD_FLASH_WRn D 1A18	FSPI_D0 I2S2_SCLK_RX_M0	D38
	FSPI_D1 I2S2_LRCK_RX_M0	B36 GBT_WAKE_HOST_H_GPI02_C0
	FSPI_CSOn I2S2_MCLK_M0	B36 — THOST_WAKE_BT_H_GPI02_C1
<u></u>	FSPI_D3 12S2_SCLK_TX_M0	C37 G12S2_SCLK_TX_M0
<u>1C13</u>	VCC102 12S2_LRCK_TX_M0	1D19 GI2S2_LRCK_TX_M0
	12S2_LRCK_TX_M0	1B20
_1E20	SDMMC0_D0 12S2_SDI_M0	D37
_ <u>1F19</u>	SDMMCO_D1	
<u>_1D20</u>	SDMMCO_D2 CLK32K_OUT1	B38CLK32K_OUT1_WIFI
	SDMMC0_D3	
_1E19	SPI3 CIK M1	<u>1U7</u>
	SDMMCO_CMD SPI3 MOSI M1	AP12
	SDMMCO_CLK 1253 LRCK M1	1V6
	VCCIO3 SPI3_MISO_M1	106
_ <u>R37</u>	SPI3 CS0 M1	<u>1T6</u>
	USB_OTGO_DP	
	USB_OTGO_DM HDMITX_SCL	AR12 HDMITX_SCL
	- USB_OTGO_VBUSDET HDMITX_SDA	AP11 HDMITX_SDA
<u>T37</u>	USB_OTGO_ID HDMITX_CEC_M	1V5 HDMITX_CEC_MO
_1,119		1N8
	USB_HOST1_DP VCCIO7	110
	USB_HOST1_DM	
	USB_AVDD1_0V9 SARADC_VINO	<u>1017</u>
_1H16	USB_AVDD1_1V8 SARADC_VIN1	<u>1017</u>
_1H18	USB_AVDD1_3V3 SARADC_VIN2	<u>1B18</u>
	SARADC_VIN3	1A19
SATA1_TX+ D W37	USB3_HOST1_SSTXp/SATA1_TXp	
SATA1_TX-DW38	USB3_HOST1_SSTXn/SATA1_TXn SARADC_AVDD_1V	1D16_
SATA1 DV. D. V38		
SAIALLAATU	USB3_HOST1_SSRXp/SATA1_RXp OTP_VCC18	1D15
SATA1_RX-DV37	USB3_HOST1_SSRXn/SATA1_RXn	
_AA37	PCIE20_TXp/SATA2_TXp MIPI_CSI_RX_D0p	AP18_
_AA38		AR18
_AB37	PCIE20_RXp/SATA2_RXp MIPI_CSI_RX_D1p	
_AB38	PCIE20_RXn/SATA2_RXn MIPI_CSI_RX_D1n	AP17
_1K19	PCIE20_REFCLKp MIPI_CSI_RX_D2p	AP15
<u>1K20</u>	T CICZO_KET CEKP	AR15
	TO DE SOLUCION TO THE SOLUCION	
_1K16	MILITER TO LOS LINES DE LA COMPANION DE LA COM	AR14
_1K17	MULTI_PHY_AVDD_1V8 MIPI_CSI_RX_D3n	AP14
	MIPL_CSI_RX_CLK0p	1V9
<u></u>	- USB_HOST2_DP MIPL_CSI_RX_CLKOn	1U9
V1	USB HOST2 DM	1V8_
	MIPI_CSI_RX_CLK1p	4110
	USB_HOST3_DP MIPL_CSI_RX_CLK1n	
	USB_HOST3_DM	1N9_
114	USB_AVDD2_0V9 MIPI_CSI_RX_AVDD_0V9	
	USB_AVDD2_1V8 MIPI_CSI_RX_AVDD_1V8	<u>1P9</u>
	USB_AVDD2_3V3	
	GPI03_C6_d	172
_1N1	GPI03_C7_d	101
1L2	GPIO3_DO_d	AP1
	GPI03_D1_d	AN2
	GPI04_B1_d GPI03_D2_d	AM2
	GPI04_B2_d GPI03_D3_d	AM1_
_ <u>AE2</u>	GPI04_B3_d GPI03_D4_d	111
_ AF1	GPI03_D5_d	171
_AD1	GPI04_B5_d GPI03_D6_d	AL2
	CPIO3 D7 d	AL1_
	GPI04_B6_d	AK2_
ACZ	GPI04_B7_d GPI04_A1_d	1P2
	GPI04_A2_d	1R1_
_AB2	GPI04_C0_d GPI04_A3_d	AJ2
_AB1	GPI04_C1_d GPI04_A4_d	<u>1N2</u>
	GPI04_A5_d	AJ1_
		AH2_
	GP104_A6_d	
		l
	VCCI06_1	114
	VCC106_2	11.5
		1

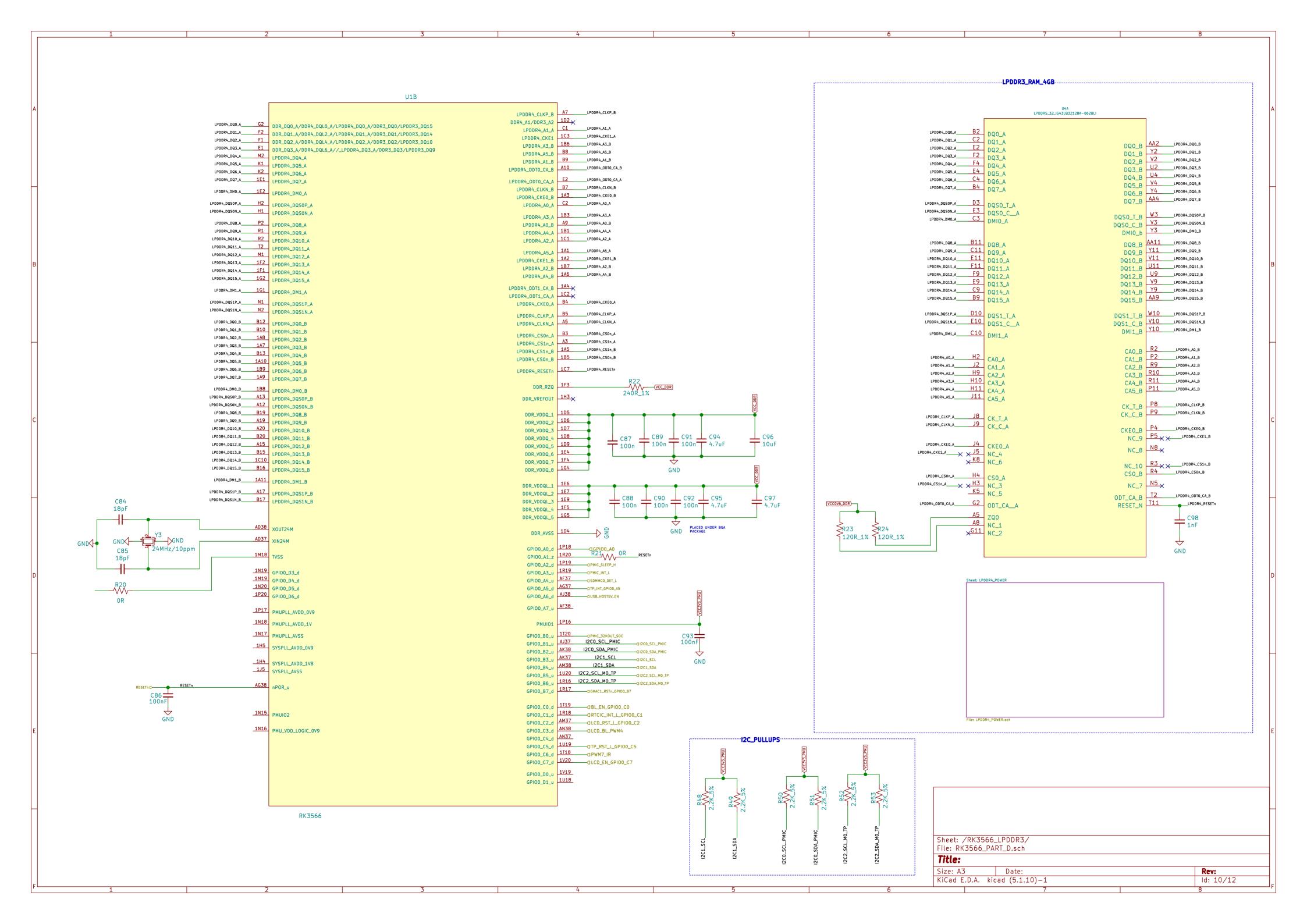












LPDDR3_RAM POWER U4B LPDDRS_32_IS43LQ32128A-062BLI F1 VDD1_1 VCC1V8_DDR F12 VDD1_1 F12 VDD1_2 G4 VDD1_3 G9 VDD1_4 T4 VDD1_5 T9 VDD1_6 VSS_1 VSS_2 VSS_3 VSS_4 VSS_5 VSS_6 VSS_6 VSS_7 VSS_8 D9 U1 VDD1_7 VDD1_8 C103 C106 VCC_DDR DECOUPLING CAPACITORS B3 VDDQ_1 VCCOV6_DDR B5 VDDQ_1 B5 VDDQ_2 B8 VDDQ_3 B10 VDDQ_4 VDDQ_5 D5 VDDQ_5 VSS_8 D4 VSS_9 D9 VSS_10 D11 VSS_11 E1 VSS_12 E5 VSS_13 E8 VSS_14 G1 VSS_15 G3 VSS_16 G3 VSS_17 G5 G8 VSS_18 G10 VSS_19 G10 VSS_20 J1 VSS_21 J1 VSS_22 J1 VSS_22 J10 VSS_24 J12 VCC_DDR C101 22uF C104 10uF C109 10uF C130 C133 C135 C137 C139 C141 C143 C145 100nF D5 VDDQ_6 D8 VDDQ 6 VDDQ 7 VDDQ 8 VDDQ 9 VDDQ 10 U3 VDDQ 11 GND U10 VDDQ_12 W1 VDDQ_13 W5 VDDQ_14 VDDQ_15 VCC_DDR DECOUPLING CAPACITORS W12 AA3 VDDQ_16 VDDQ_17 VDDQ_17 VDDQ_18 VSS_23 VSS_24 VSS_25 VSS_26 VSS_26 VSS_27 VSS_28 VSS_28 V1 VCCOV6_DDR AA8 VDDQ_19 AA10 VDDQ_20 C100 C102 C108 C110 C112 C114 C116 C118 C120 1uF 100nF 100nF 100nF ☐ C125 ☐ C127 ☐ C129 ☐ C132 ☐ C134 ☐ C136 ☐ C138 ☐ C140 ☐ C142 ☐ C144 ☐ C147 ☐ 100nF C99 22uF 1uF VSS_28 VSS_43 VSS_44 VSS_45 VSS_46 VSS_46 VSS_47 W2 GND VSS_48 W4 VSS_49 VSS_50 W11 3.3V TO 1.8V@2A H8 VDD2_7 VDD2_8 3.3V TO 0.6V@2A +3V3 K1 VDD2_9 K3 VDD2_10 2.2uH +3V3 TLV62569DDC WPN4020H2R2MT L10 K10 K10 K12 VDD2_11 VDD2_12 VDD2_13 VDD2_14 VDD2_15 2.2uH VIN SW TLV62569DDC C131 4.7uF WPN4020H2R2MT VSS_56 AB5 C146 C148 VSS_57 AB8 ₹R26 200K VSS_58 AB10 C122 C124 22uF 22uF \rightarrow GND N12 N12 VDD2_16 VDD2_17 VDD2_17 VDD2_18 R25 20K ₹R27 100K GND GND GND GND R8 VDD2_19 VDD2_20 VDD2_21 VDD2_21 VDD2_22 VDD2_22 VDD2_23 AB4 AB9 VDD2_23 L C105 GND 100nF \rightarrow AB9 VDD2_24 GND XA1 DNU_1 XA2 DNU_2 XA11 DNU_3 XA12 DNU_4 XB1 DNU_5 XB12 DNU_6 DNU_7 AA1 DNU_8 DNU_9 DNU_10 AB2 AB1 DNU_11 DNU_12 AB11 AB12 Sheet: /RK3566_LPDDR3/LPDDR4_POWER/ File: LPDDR4_POWER.sch Title: Size: A3 Date: KiCad E.D.A. kicad (5.1.10)-1 Rev: ld: 11/12

