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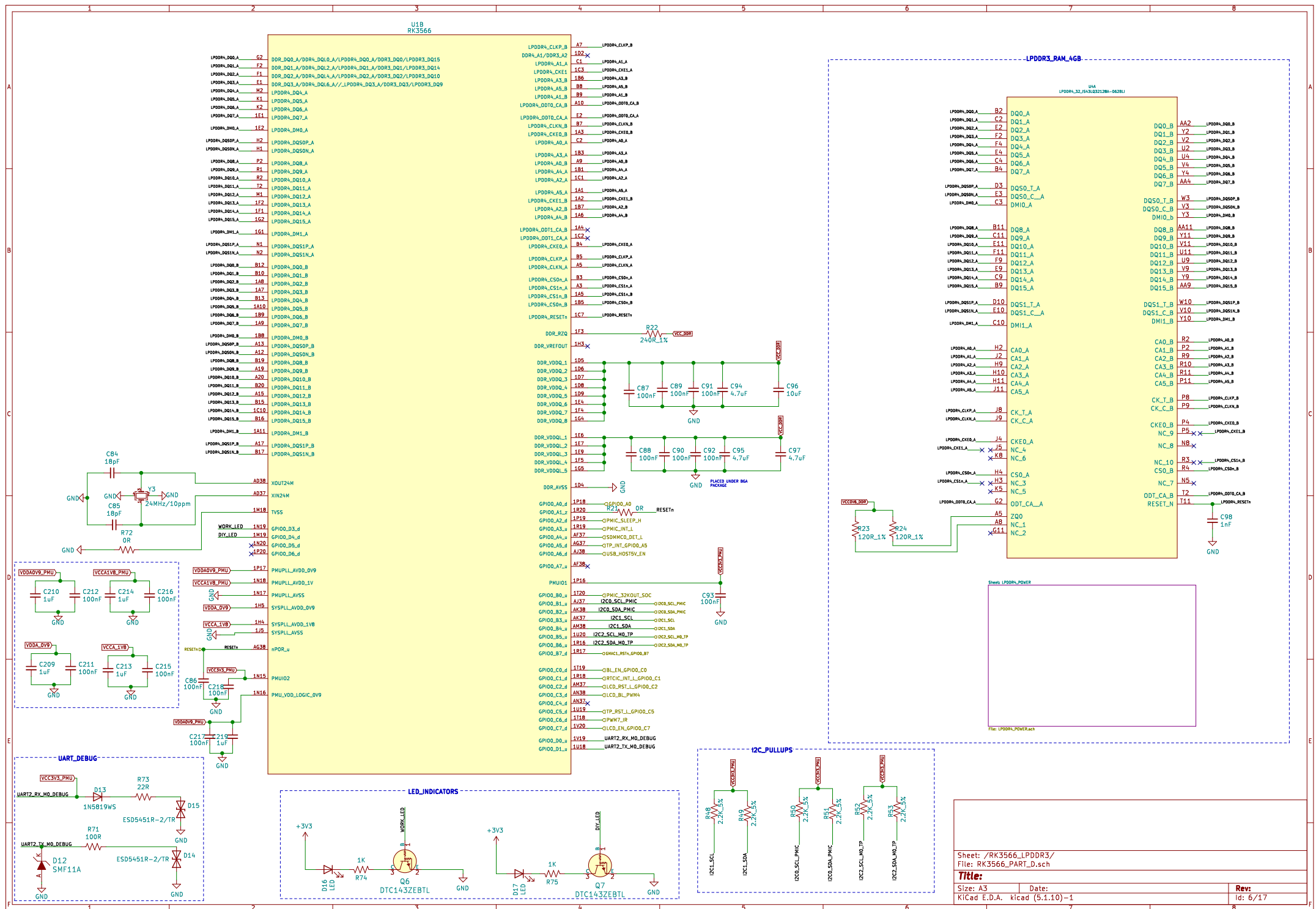
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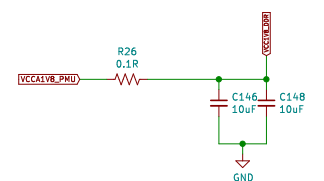
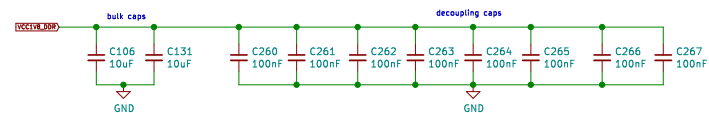
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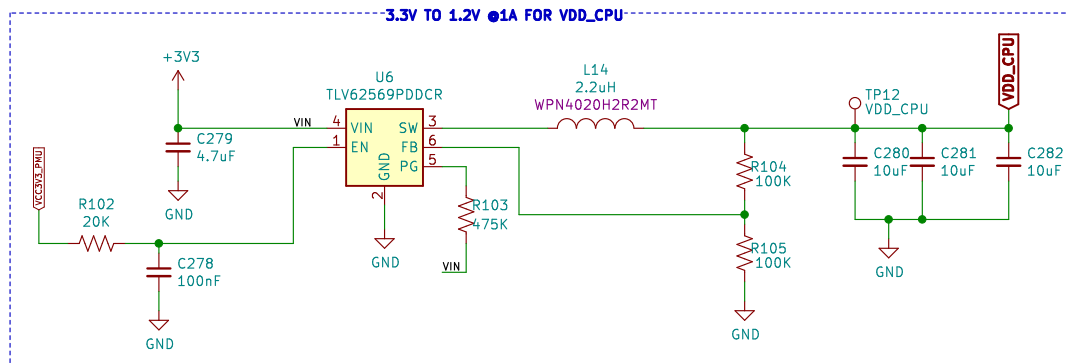
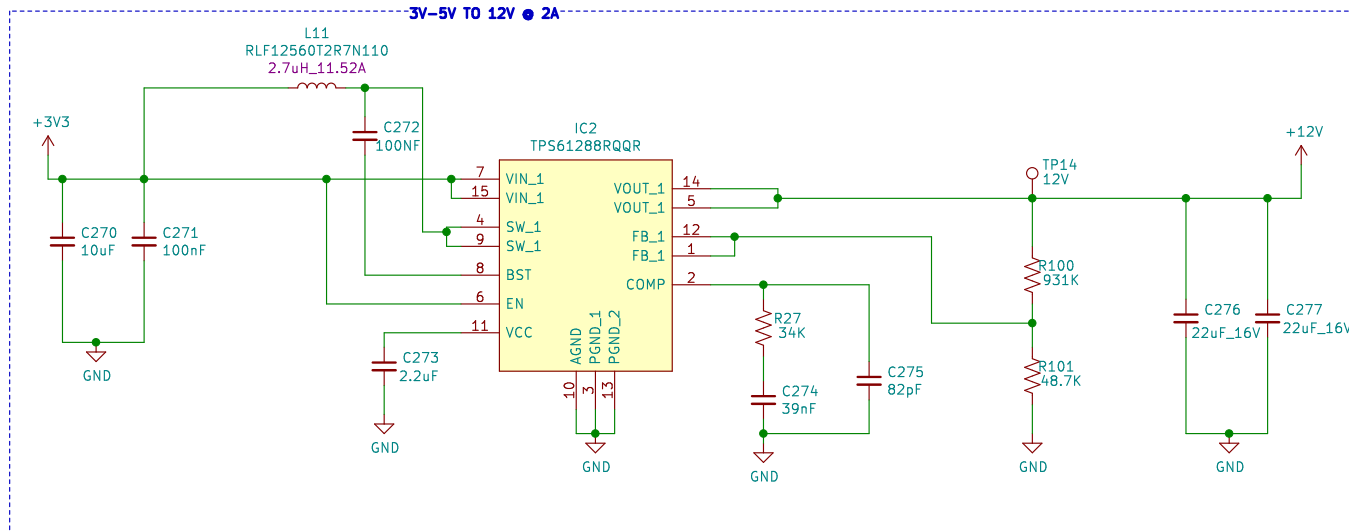
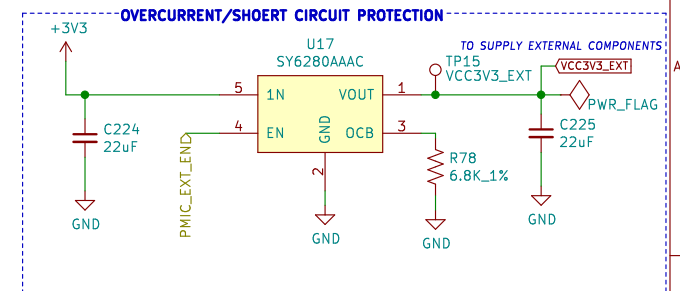
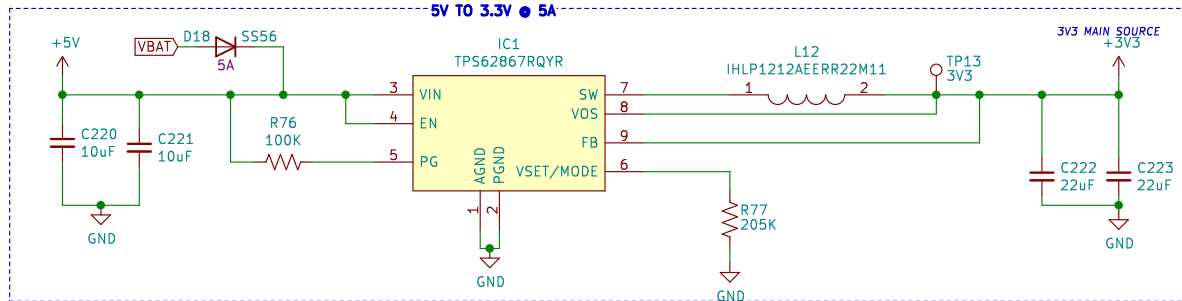
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Sheet: /OTHER_POWER/		
File: OTHER_POWER.sch		
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Size: A4	Date:	Rev:
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U1C
RK3566

GPIO1_B4_DISPLAY_ONED—A32
LCD_1_EN_GPIO1_B8_C7D—B27
TP_INT_GPIO1_B6_DISPLAY_ONED—B32
BL_EN_GPIO1_B7_DISPLAY_ONED—B29
LCD_RST_L_GPIO1_C0_DISPLAY_ONED—A30
TP_RST_L_GPIO1_C2_DISPLAY_ONED—A30
GPIO1_C2_u_DISPLAY_TWOD—B30
LCD_2_EN_GPIO1_C3_u_DISPLAY—A33
TP_INT_GPIO1_A5_DISPLAY_TWOD—A27
BL_EN_GPIO1_C0_DISPLAY_TWOD—A29
LCD_RST_L_GPIO1_C2_DISPLAY_TWOD—A16
TP_RST_L_GPIO1_C6_DISPLAY_TWOD—A16

EMMC_RSTn
FSPL_CLK
FSPL_D0
FSPL_D1
FSPL_C50n
FSPL_D3
VCCIO2

UART6_TX_M1D—1F20
UART6_RX_M1D—1F19
SDMMC0_D0
SDMMC0_D1
SDMMC0_D2
SDMMC0_D3

SDMMC0_CMD
SDMMC0_CLK
VCCIO3

USB_OTG0_D+D—B37
USB_OTG0_D—B38
USB_OTG0_DM—B38
USB_OTG0_DP—B37
USB_OTG0_VBUSDET—B38
USB_OTG0_ID—B37

USB_OTG0_D+D—1J19
USB_OTG0_D—1J20
USB_OTG0_DP—1J19
USB_OTG0_DM—1J20
USB_AVDD1_0V9
USB_AVDD1_1V8
USB_AVDD1_3V3

SATA_INTERFACE
SATA1_TX+D—W37
SATA1_TX—W38
SATA1_RX+D—V38
SATA1_RX—V37
SATA1_RX—D—V37

PCIE20_TXp/SATA2_TXp
PCIE20_TXn/SATA2_TXn
PCIE20_RXp/SATA2_RXp
PCIE20_RXn/SATA2_RXn

PCIE20_REFCLKp
PCIE20_REFCLKn
MULTI_PHY_AVDD_0V9
MULTI_PHY_AVDD_1V8

USB_HOST2_DP
USB_HOST2_DM
USB_HOST3_DP
USB_HOST3_DM
USB_AVDD2_0V9
USB_AVDD2_1V8
USB_AVDD2_3V3

GPIO4_A7_d
GPIO4_B0_d
GPIO4_B1_d
GPIO4_B2_d
GPIO4_B3_d
GPIO4_B4_d
GPIO4_B5_d
GPIO4_B6_d
GPIO4_B7_d

GPIO4_C0_d
GPIO4_C1_d

SDMMC1_D0
SDMMC1_D1
SDMMC1_D2
SDMMC1_D3
SDMMC1_CMD
SDMMC1_CLK
SDMMC1_PWRn
SDMMC1_DET

UART1_RX_M0
UART1_TX_M0
UART1_RTSn_M0
UART1_CTSn_M0

I2S2_SCLK_RX_M0
I2S2_LRCK_RX_M0
I2S2_MCLK_M0
I2S2_SCLK_TX_M0
I2S2_LRCK_TX_M0
I2S2_S0D_M0
I2S2_S0L_M0

CLK32K_OUT1_WIFI

SPI3_CLK_M1
SPI3_MOSI_M1
I2S3_LRCK_M1
SPI3_MISO_M1
SPI3_CS0_M1
VCCI04

HDIMTX_SCL
HDIMTX_SDA
HDIMTX_CEC_M

SARADC_VIN0
SARADC_VIN1
SARADC_VIN2
SARADC_VIN3

SARADC_AVDD_1V
OTP_VCC18

MIPLCSL_RX_D0p
MIPLCSL_RX_D0n
MIPLCSL_RX_D1p
MIPLCSL_RX_D1n
MIPLCSL_RX_D2p
MIPLCSL_RX_D2n
MIPLCSL_RX_D3p
MIPLCSL_RX_D3n

MIPLCSL_RX_CLK0p
MIPLCSL_RX_CLK0n
MIPLCSL_RX_CLK1p
MIPLCSL_RX_CLK1n

MIPLCSL_RX_AVDD_0V9
MIPLCSL_RX_AVDD_1V8

GPIO3_C6_d
GPIO3_C7_d
GPIO3_D0_d
GPIO3_D1_d
GPIO3_D2_d
GPIO3_D3_d
GPIO3_D4_d
GPIO3_D5_d

GPIO3_D6_d
GPIO3_D7_d
GPIO4_A0_d
GPIO4_A1_d
GPIO4_A2_d
GPIO4_A3_d
GPIO4_A4_d
GPIO4_A5_d
GPIO4_A6_d

VCCI06_1
VCCI06_2

QSDMMC1_D0
QSDMMC1_D1
QSDMMC1_D2
QSDMMC1_D3
QSDMMC1_CMD
QSDMMC1_CLK
QWIFI_REG_ONn_H_GPIO2_B1
QWIFI_WAKE_HOSTn_H_GPIO2_B2

QUART1_RX_M0
QUART1_TX_M0
QUART1_RTSn_M0
QUART1_CTSn_M0

QBT_REG_ONn_H_GPIO2_B7
QBT_WAKE_HOSTn_H_GPIO2_C0
QHOST_WAKE_BTn_H_GPIO2_C1

QI2S2_SCLK_TX_M0
QI2S2_LRCK_TX_M0
QI2S2_S0D_M0
QI2S2_S0L_M0

CLK32K_OUT1_WIFI

QHDIMTX_SCL
QHDIMTX_SDA
QHDIMTX_CEC_M

SARADC_VIN0
SARADC_VIN1
SARADC_VIN2
SARADC_VIN3

SARADC_AVDD_1V
OTP_VCC18

MIPLCSL_RX_D0p
MIPLCSL_RX_D0n
MIPLCSL_RX_D1p
MIPLCSL_RX_D1n
MIPLCSL_RX_D2p
MIPLCSL_RX_D2n
MIPLCSL_RX_D3p
MIPLCSL_RX_D3n

MIPLCSL_RX_CLK0p
MIPLCSL_RX_CLK0n
MIPLCSL_RX_CLK1p
MIPLCSL_RX_CLK1n

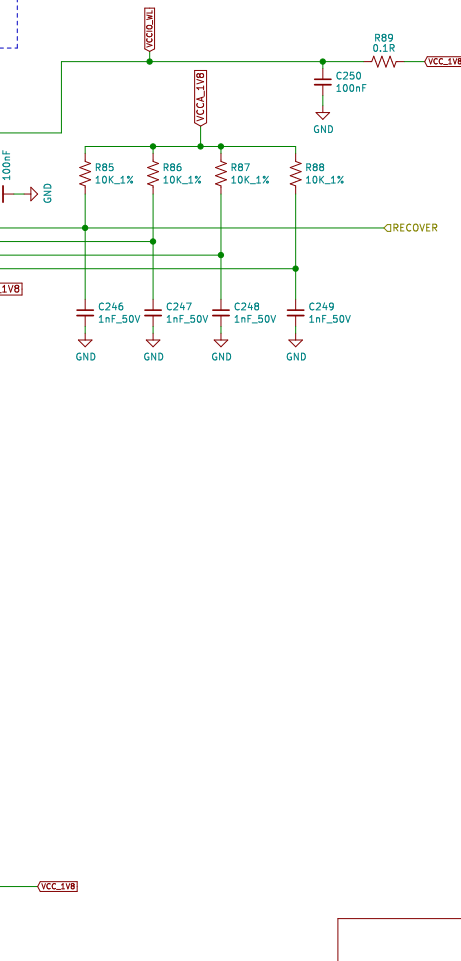
MIPLCSL_RX_AVDD_0V9
MIPLCSL_RX_AVDD_1V8

GPIO3_C6_d
GPIO3_C7_d
GPIO3_D0_d
GPIO3_D1_d
GPIO3_D2_d
GPIO3_D3_d
GPIO3_D4_d
GPIO3_D5_d

GPIO3_D6_d
GPIO3_D7_d
GPIO4_A0_d
GPIO4_A1_d
GPIO4_A2_d
GPIO4_A3_d
GPIO4_A4_d
GPIO4_A5_d
GPIO4_A6_d

VCCI06_1
VCCI06_2

RK3566 TO WIFI/BT MODULE



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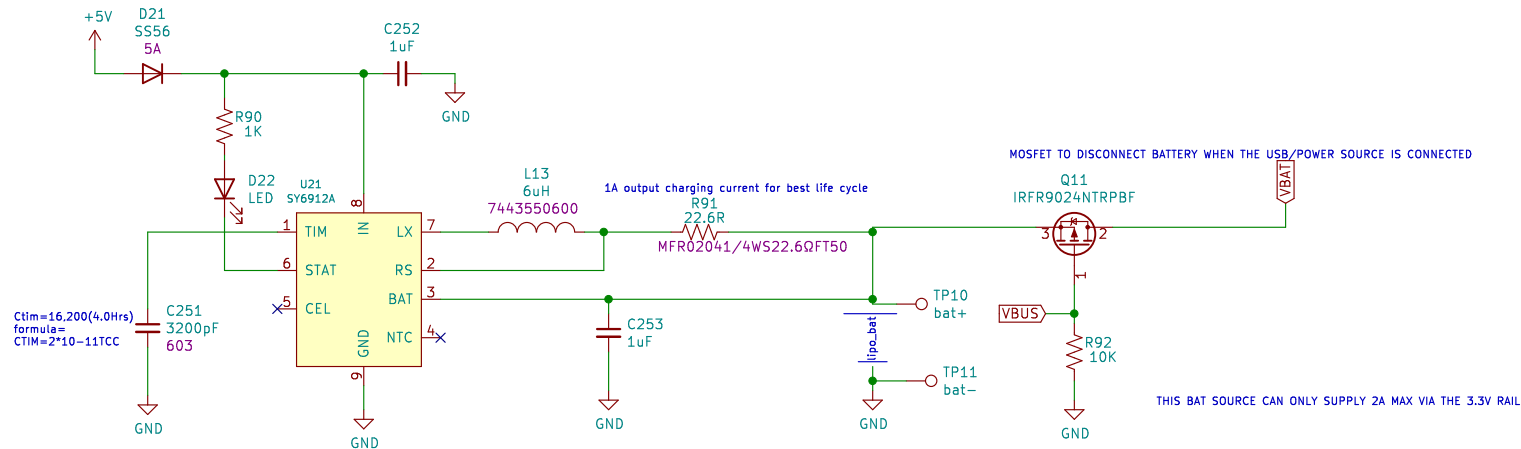
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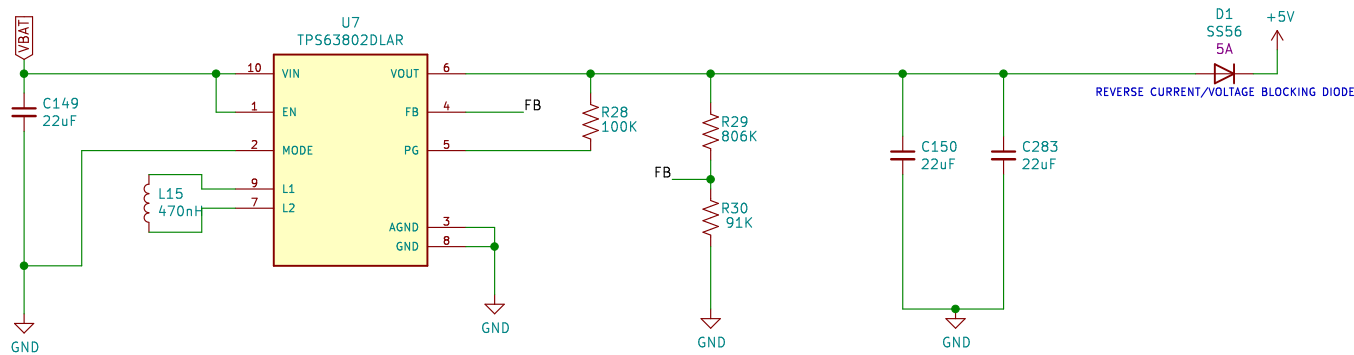
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BAT CHARGER SECTION



V_BAT TO 5V(2A) BOOSTS IC



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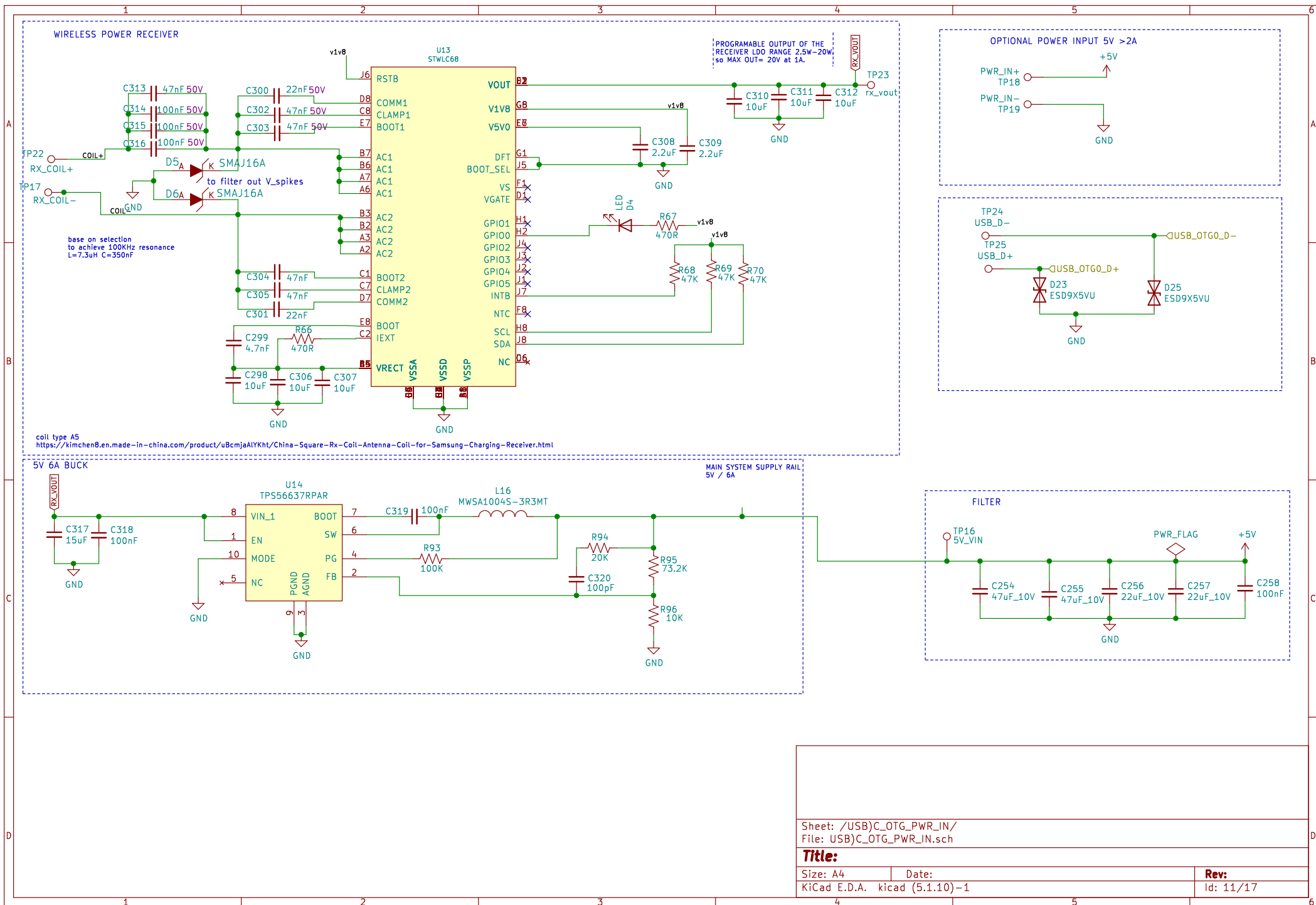
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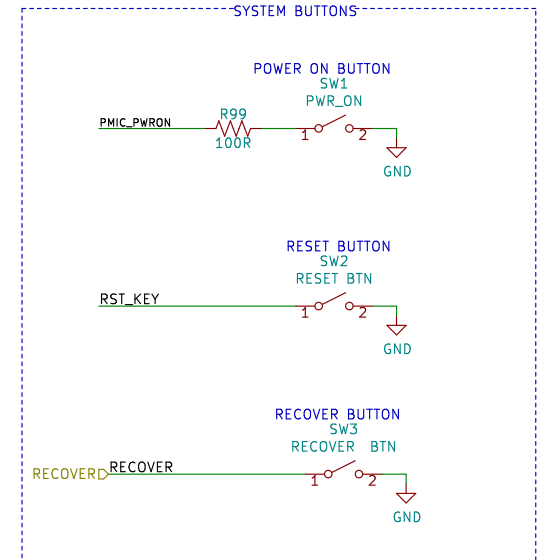
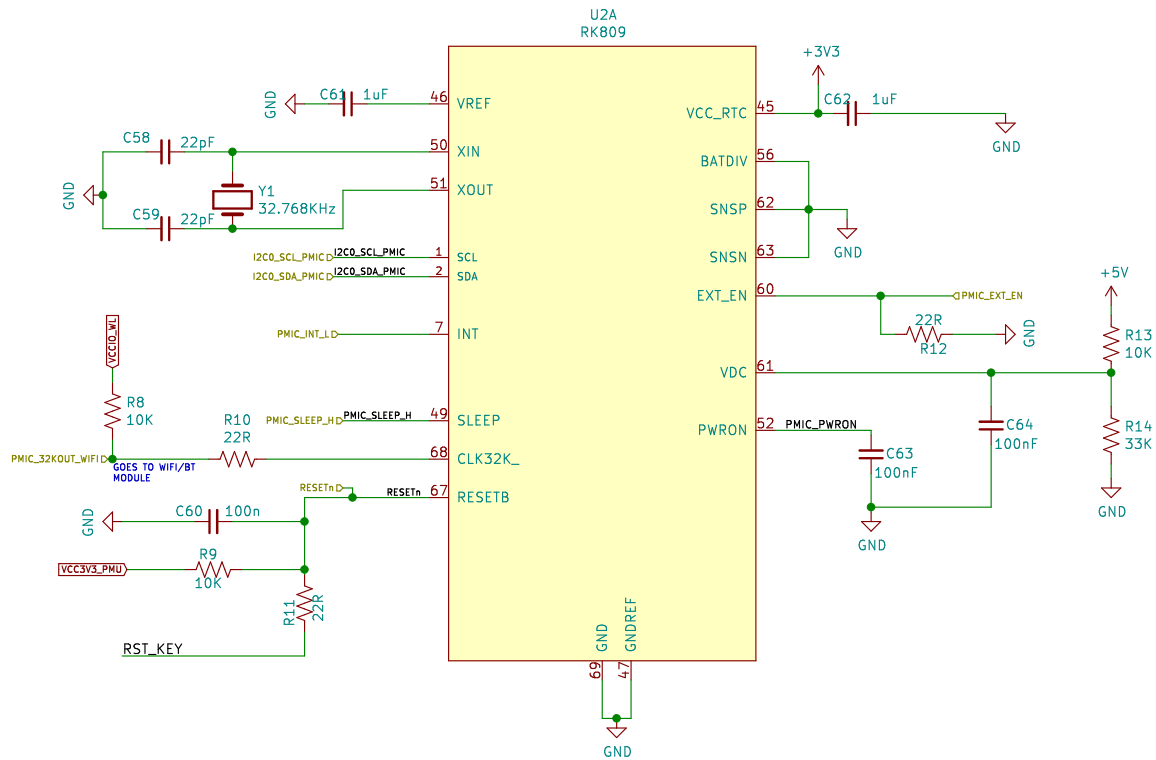
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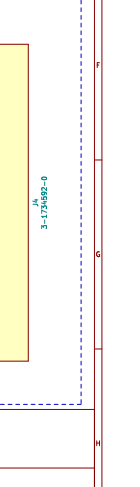
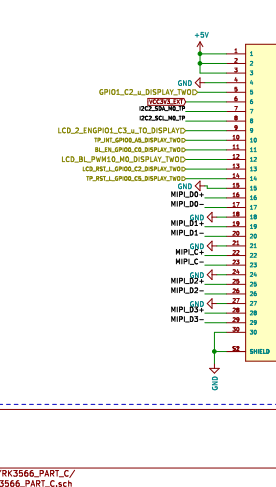
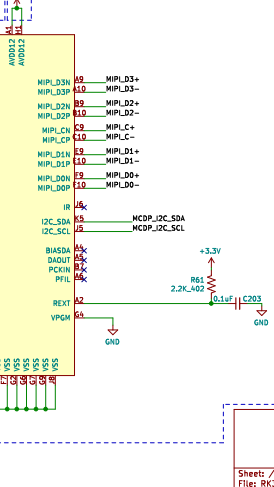
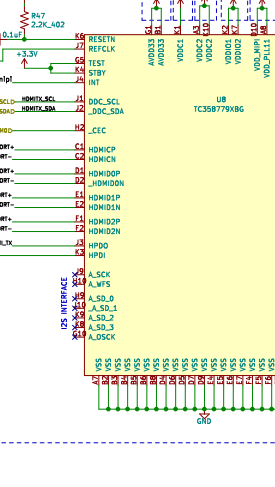
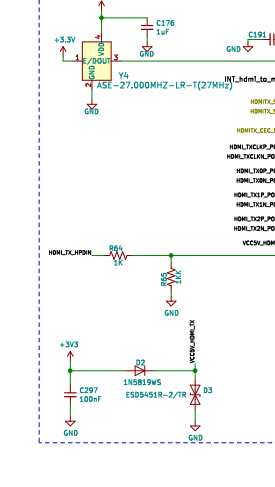
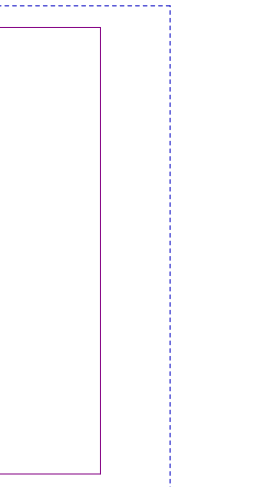
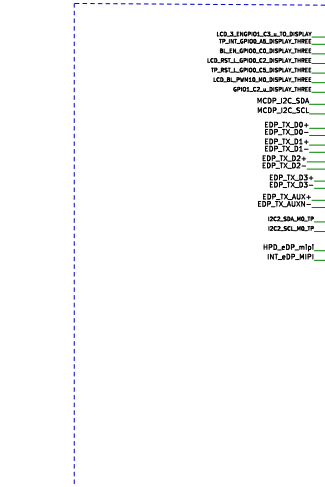
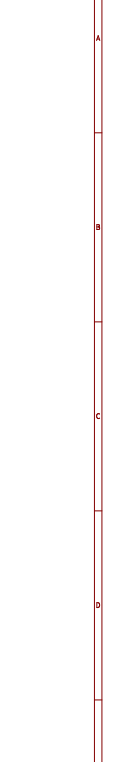
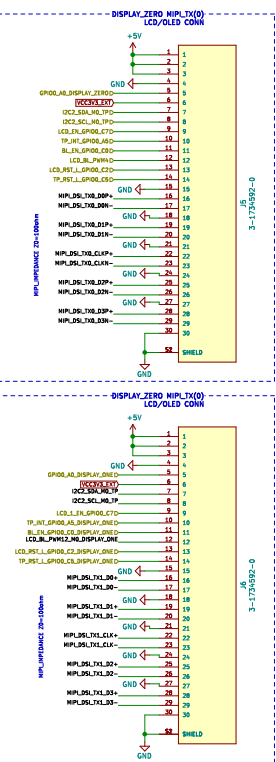
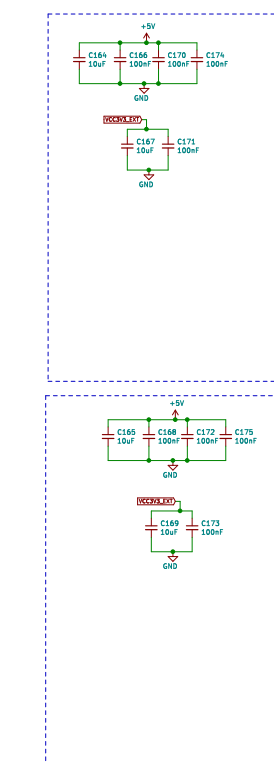
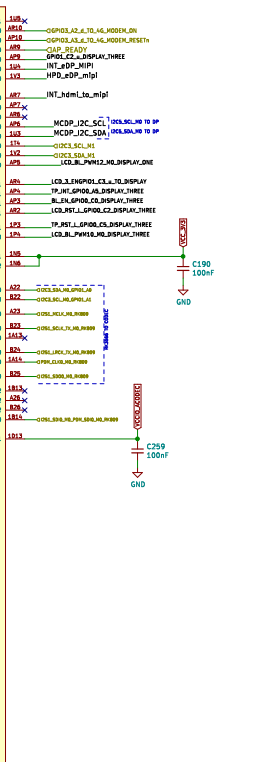
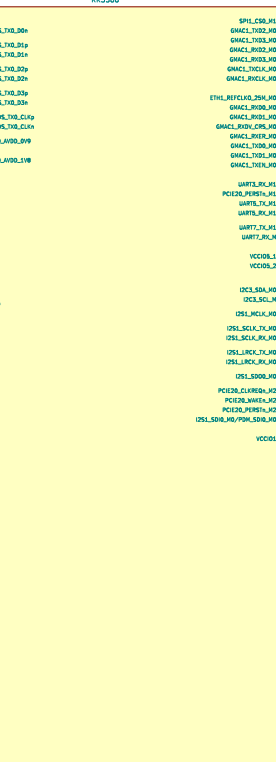
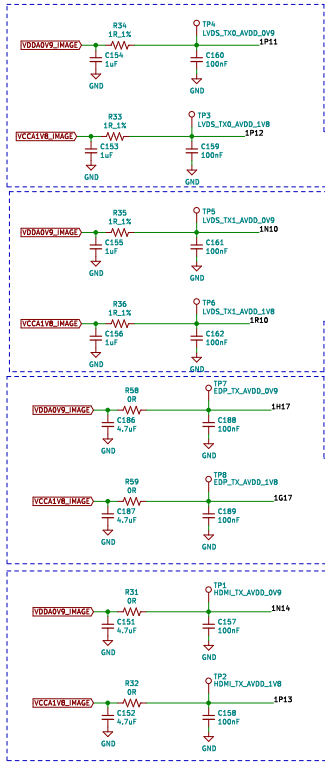
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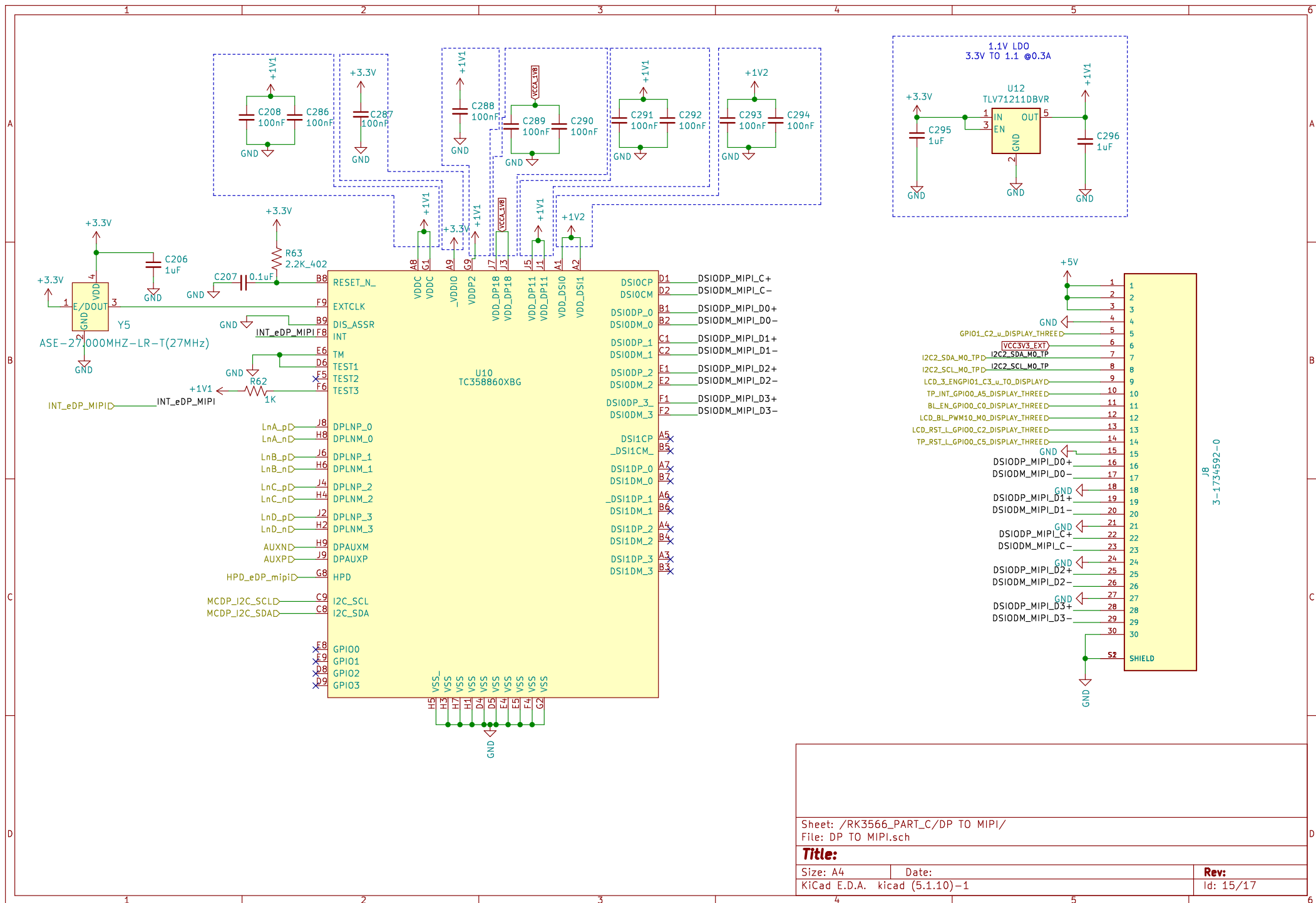
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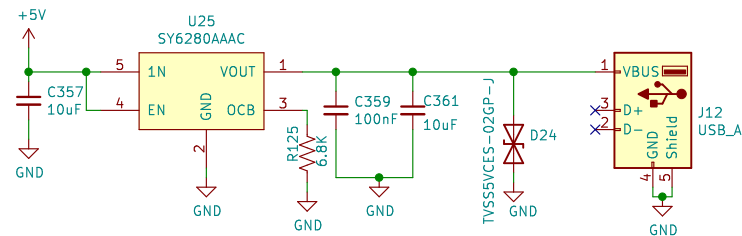
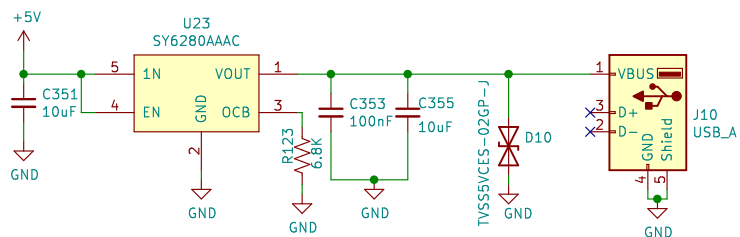
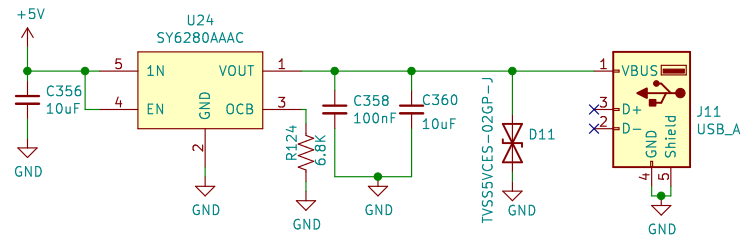
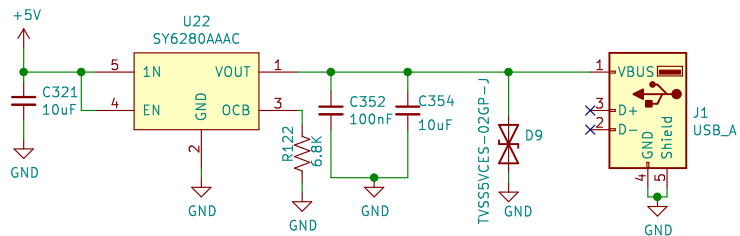
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