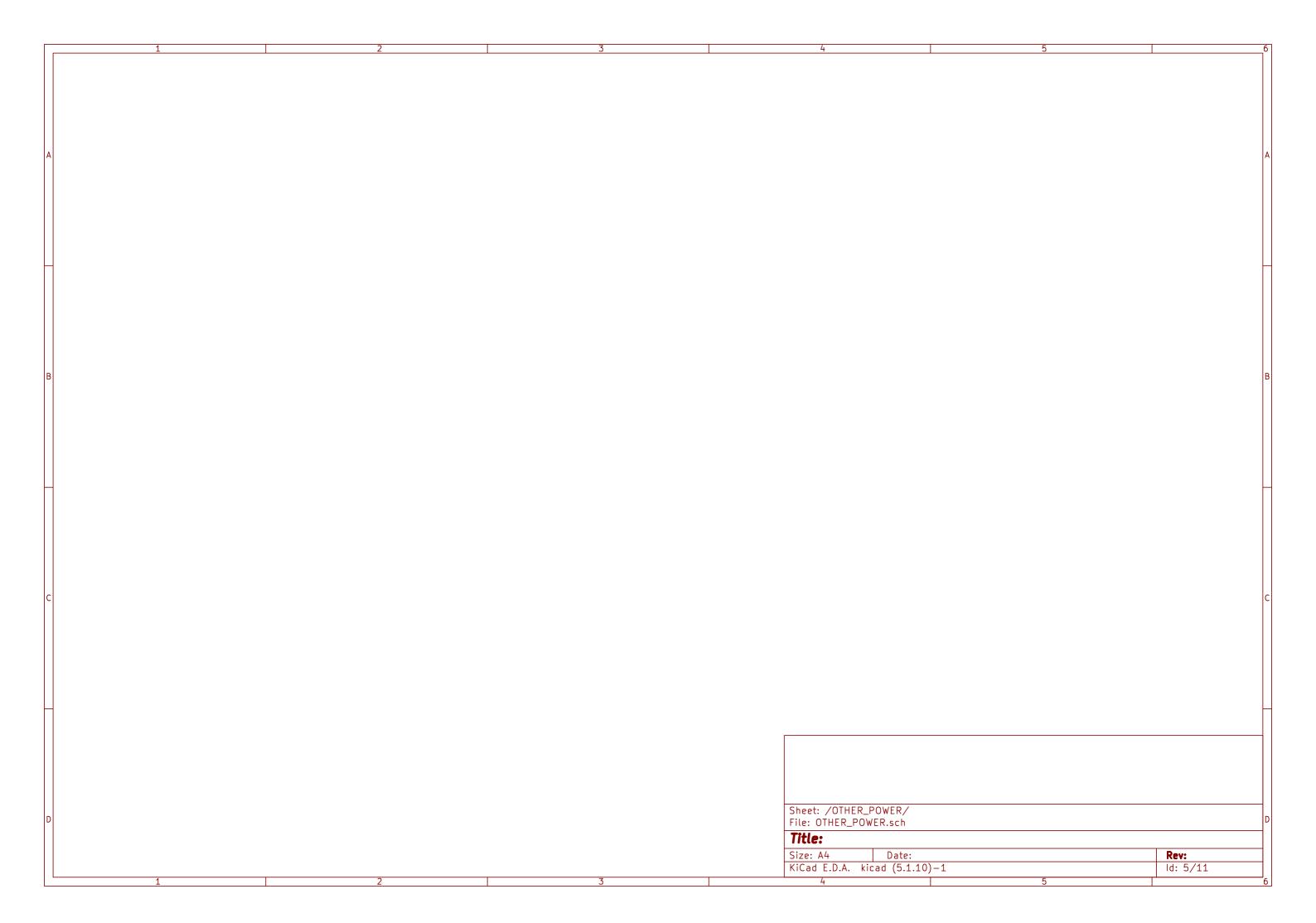
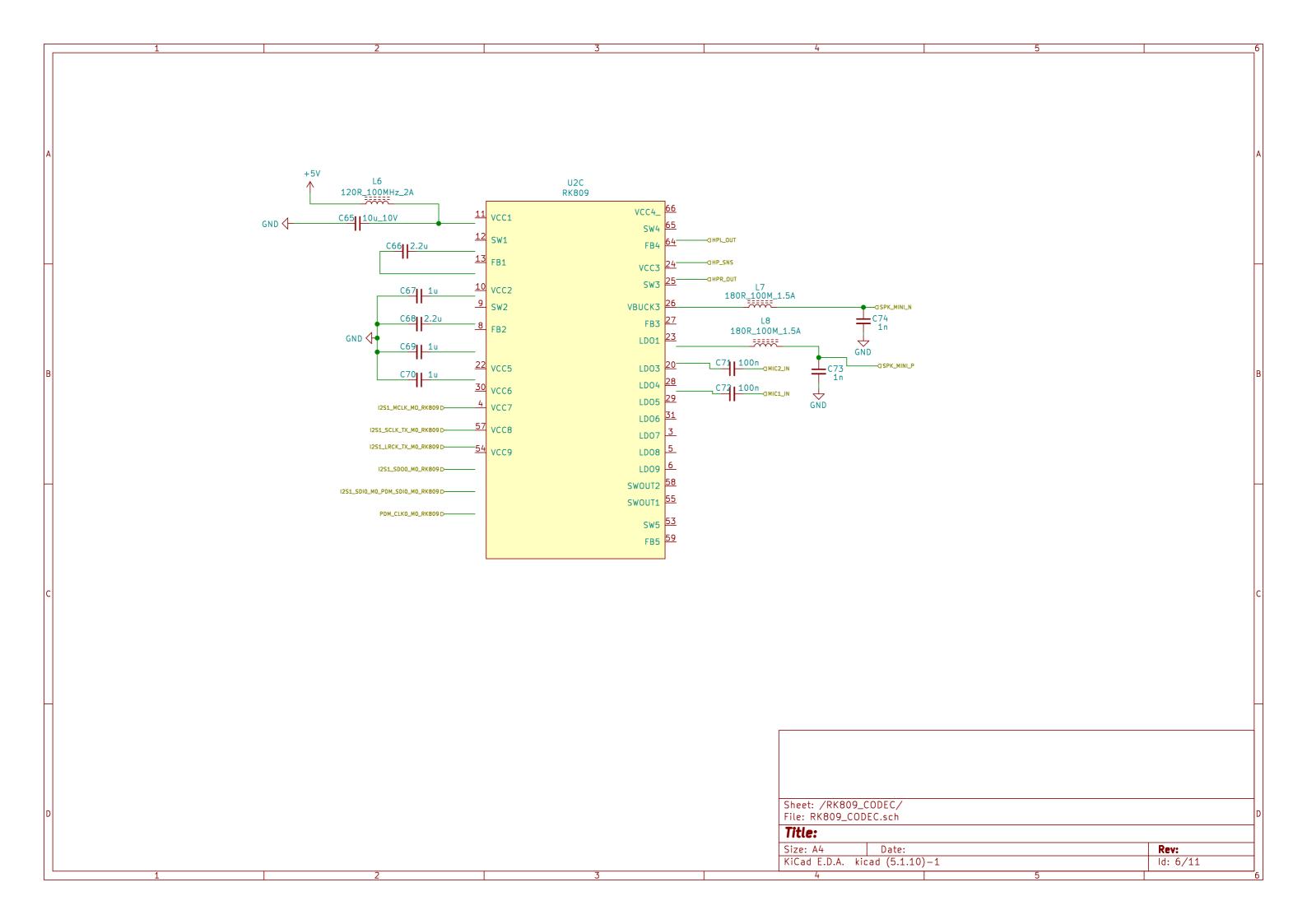
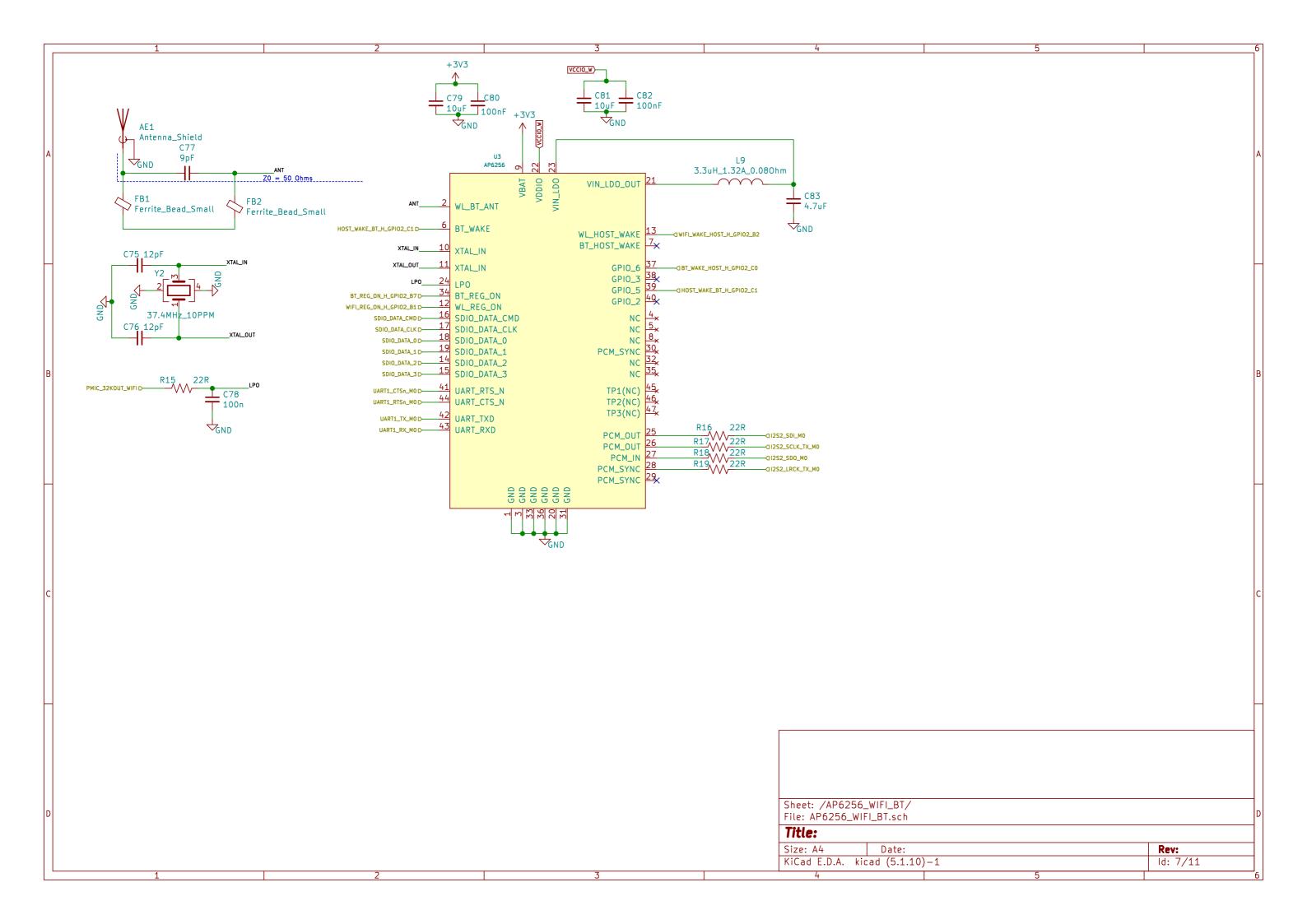
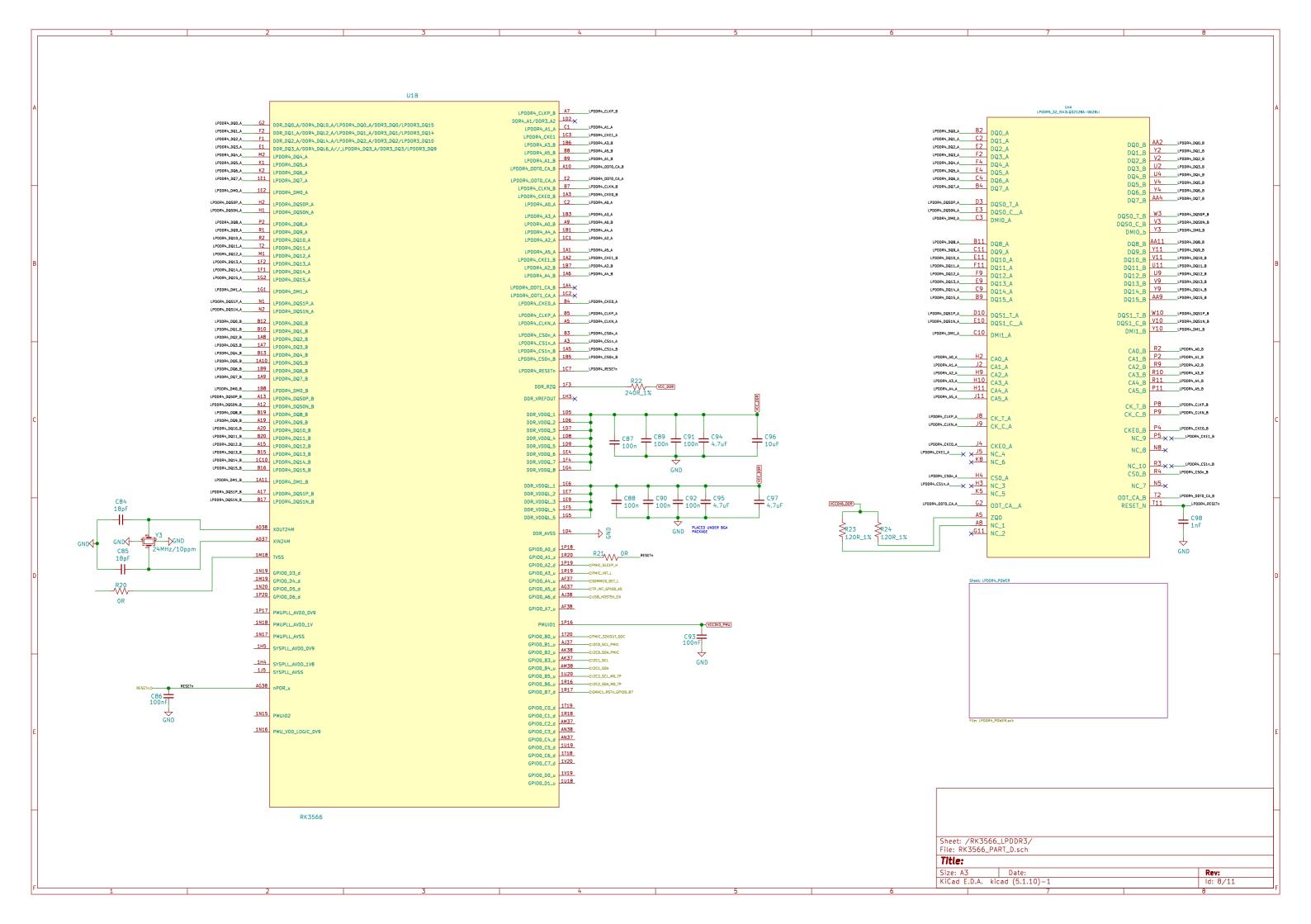


U1C RK3566 A32 EMMC_DO B27 EMMC_D1 eMMC D1 FLASH D1D B32 EMMC_D2 eMMC_D2_FLASH_D2 D-B29 EMMC_D3
B33 EMMC_D4
EMMC_D5 SDMMC1_D3 1A20 SDMMC1_D3 eMMC_D3_FLASH_D3 D eMMC_D4_FLASH_D4 D SDMMC1_CMD B35 ____ GSDMMC1_CMD eMMC_D5_FLASH_D5.D SDMMC1_CLK B37 ____GSDMMC1_CLK B30 EMMC_D6 eMMC_D6_FLASH_D6 D SDMMC1_PWREN SDMMC1_DET B34 __QWIFI_REG_ON_H_GPI02_B1 __QWIFI_WAKE_HOST_H_GPI02_B2 A33 EMMC_D7 eMMC_D7_FLASH_D7 D A27 EMMC_CMD EMMC_CLKOUT eMMC_CMD_FLASH_WRn D 1A16 EMMC_DATA_STROBE 1B16 EMMC_RSTn 1A15 FSPI_CLK 1A17 FSPI_D0 1A18 FSPI_D1
1B17 FSPI_CSOn 1C15 FSPI_D3 1C13 VCCI02 1E20 SDMMCO_DO 1F19 SDMMCO_D1 CLK32K_OUT1_WIFI 1D20 SDMMCO_D2 1F18 SDMMCO_D3 SPI3_CLK_M1 1U7 SPI3_MOSI_M1 AP12 12S3_LRCK_M1 1V6 SPI3_MISO_M1 1U6 1E19 SDMMCO_CMD G38 SDMMCO_CLK VCCIO3 R37
USB_OTGO_DP
USB_OTGO_DM SPI3_CS0_M1 1T6 HDMITX_SCL AR12 T38 USB_OTGO_VBUSDET USB_OTGO_ID HDMITX_SDA HDMITX_CEC_M AP11 1V5 1J19 USB_HOST1_DP VCCIO7 1N8 1J20 USB_HOST1_DM 1J17 USB_AVDD1_0V9 SARADC_VIN0
SARADC_VIN1
SARADC_VIN2
1017
1818
1419 1H16 USB_AVDD1_1V8 1H18 USB_AVDD1_3V3 SARADC_VIN3 1A19 W37 USB3_HOST1_SSTXp/SATA1_TXp SATA1_TX-D W38 USB3_HOST1_SSTXn/SATA1_TXn SARADC_AVDD_1V 1D16 SATA1_RX+D____V38 USB3_HOST1_SSRXp/SATA1_RXp OTP_VCC18 1D15 SATA1_RX-D V37 USB3_HOST1_SSRXn/SATA1_RXn MIPI_CSI_RX_D0p AP18
MIPI_CSI_RX_D0n AR18 AA37 PCIE20_TXp/SATA2_TXp AA38 PCIE20_TXn/SATA2_TXn AB37
PCIE20_RXp/SATA2_RXp
PCIE20_RXn/SATA2_RXn MIPI_CSI_RX_D1p MIPI_CSI_RX_D1n 1K19 PCIE20_REFCLKP
1K20 PCIE20_REFCLKn MIPI_CSI_RX_D2p AP15 MIPI_CSI_RX_D2n AR15 1K16 MULTI_PHY_AVDD_0V9 MIPI_CSI_RX_D3p AR14 1K17 MULTI_PHY_AVDD_1V8 MIPI_CSI_RX_D3n AP14 MIPI_CSI_RX_CLK0p 1V9 MIPI_CSI_RX_CLKOn 1U9 V2 USB_HOST2_DP V1 USB_HOST2_DM MIPI_CSI_RX_CLK1p 1V8 Y2 USB_HOST3_DP USB_HOST3_DM MIPI_CSI_RX_CLK1n 1U8 MIPI_CSI_RX_AVDD_0V9 1N9 1J4 1J3 USB_AVDD2_1V8 USB_AVDD2_1V8 USB_AVDD2_3V3 MIPI_CSI_RX_AVDD_1V8 1P9 GPI03_C6_d 1T2 GPI03_C6_d 112 GPI03_C7_d 1U1 GPI03_D0_d AP1 GPI03_D1_d AN2 GPI03_D2_d GPI03_D3_d 1R2 1N1 GPI04_A7_d 1L2 GPI04_B0_d AG2 AF2 AE2 GPI04_B2_d GPI04_B3_d GPI03_D4_d 1R2 GPI03_D5_d 1T1 AF1 GPI04_B4_d GPI04_B5_d GPI03_D6_d AL2 1L1 GPI04_B6_d GPI04_B7_d GPI03_D7_d AL1 GPI04_A0_d AK2 GPIO4_A1_d 1P2 GPI04_A1_d GPI04_A2_d GPI04_A3_d GPI04_A4_d GPI04_A5_d AJ1 AB2 GPI04_C0_d
GPI04_C1_d GPI04_A6_d AH2 VCCIO6_1 VCCIO6_2 Sheet: /RK3566_PART_B/ File: RK3566_PART_B.sch Title: Size: A3 Date: KiCad E.D.A. kicad (5.1.10)-1









LPDDR3_RAM POWER F1 VDD1_1 F12 VDD1_2 G4 VDD1_3 G9 VDD1_4 T9 VDD1_5 U1 VDD1_7 VDD1_8 VCC1V8_DDR VSS_1 A3 A10 VSS_2 C1 VSS_3 C5 VSS_6 C8 VSS_5 D4 VSS_6 D9 VSS_10 D11 VSS_11 E1 VSS_12 E8 VSS_15 G3 VSS_14 C5 VSS_15 G6 VSS_17 G5 VSS_18 G6 VSS_19 G10 VSS_20 J1 VSS_21 J1 VSS_21 J1 VSS_22 J1 VSS_23 VSS_23 VSS_23 VSS_24 VSS_25 K2 VSS_26 VSS_27 VSS_28 K11 VSS_27 VSS_28 VSS_28 VSS_29 VSS_28 VSS_49 VSS_50 VSS_40 VSS_50 ABB C103 C106 TuF 100nF VCC_DDR DECOUPLING CAPACITORS VCC_DDR) C101 22uF C104 C109 C109 VCC_DDR DECOUPLING CAPACITORS VCCOV6_DDR C132 C134 C136 C138 C140 C142 C144 C100nF T100nF T100nF T100nF C100 C102 C102 C125 _ C127 C129 A4 VDD2_1 A9 VDD2_2 F5 VDD2_3 F8 VDD2_4 H1 VDD2_5 H5 VDD2_6 VDD2_6 H8 VDD2_7 H12 VDD2_7 VCC_DDR) 3.3V TO 1.8V@2A H8 VDD2_7 VDD2_8 H12 VDD2_9 K10 VDD2_11 K10 VDD2_11 N1 VDD2_12 N1 VDD2_13 VDD2_15 N12 VDD2_15 N12 VDD2_16 R1 VDD2_16 R8 VDD2_19 R12 VDD2_18 R8 VDD2_19 R12 VDD2_20 UDS VDD2_21 UB AB4 VDD2_23 AB9 VDD2_24 3.3V TO 0.6V@2A L11 2.2uH WPN4020H2R2MT +3V3 TLV62569DDC L10 2.2uH U5 TLV62569DDC WPN4020H2R2MT C146 C148 ₹R26 200K ± C107 +4.7uF GND GND R25 20K GND ₹R27 100K GND GND + C105 GND GND GND DNU_7 DNU_8 AA12 AA12 AB12 AB12 DNU_10 DNU_11 AB12 DNU_12 ×A1 ×A2 ×A11 DNU_1 DNU_2 ×A11 DNU_3 ×A12 DNU_4 ×B1 DNU_5 DNU_6 Sheet: /RK3566_LPDDR3/LPDDR4_POWER/ File: LPDDR4_POWER.sch Title: Size: A3 Date: KiCad E.D.A. kicad (5.1.10)-1

U1D RK3566 SPI1_CSO_M1
GMAC1_TXD2_M0
GMAC1_TXD3_M0
GMAC1_RXD2_M0
GMAC1_RXD3_M0
GMAC1_TXCLK_M0
GMAC1_TXCLK_M0
GMAC1_RXCLK_M0
1V3 AR30 MIPI_DSI_TX0_D0p MIPI_DSI_TX0_D0n/LVDS_TX0_D0n AR29 MIPI_DSI_TX0_D1p/LVDS_TX0_D1p
AP29 MIPI_DSI_TX0_D1n/LVDS_TX0_D1n AP27
MIPI_DSI_TX0_D2p/LVDS_TX0_D2p
MIPI_DSI_TX0_D2n/LVDS_TX0_D2n AR26 MIPL_DSI_TX0_D3p/LVDS_TX0_D3p
MIPL_DSI_TX0_D3n/LVDS_TX0_D3n ETH1_REFCLKO_25M_M0
GMAC1_RXD0_M0
GMAC1_RXD1_M0
GMAC1_RXDV_CRS_M0

4R7
AP7
AP7
AP8
AP8
AP8
AP8
AP8 1V16 MIPLDSI_TX0_CLKp/LVDS_TX0_CLKp
1V15 MIPLDSI_TX0_CLKn/LVDS_TX0_CLKn GMAC1_RXER_M0 1U3
GMAC1_TXDD_M0 1T4
GMAC1_TXD1_M0 1V2
GMAC1_TXEN_M0 AP5 1P11 MIPI_DSI_TX0/LVDS_TX0_AVDD_0V9 1P12 MIPI_DSI_TX0/LVDS_TX0_AVDD_1V8 AP24 MIPI_DSI_TX1_D0p
AR24 MIPI_DSI_TX1_D0n AR23 MIPI_DSI_TX1_D1p
AP23 MIPI_DSI_TX1_D1n AP21 MIPI_DSI_TX1_D2p MIPI_DSI_TX1_D2n AR20 MIPI_DSI_TX1_D3p
AP20 MIPI_DSI_TX1_D3n VCCI05_1 VCCI05_2 1N6 1V11 MIPI_DSI_TX1_CLKp
1U11 MIPI_DSI_TX1_CLKn 1N10 MIPI_DSI_TX1_AVDD_0V9
1R10 IPI_DSI_TX1_AVDD_1V8 J37 EDP_TX_D0p EDP_TX_D0n K38 EDP_TX_D1p
EDP_TX_D1n M37 EDP_TX_D2p EDP_TX_D2n PCIEZO_CLKREQn_M2
PCIEZO_WAKEn_M2
PCIEZO_WAKEn_M2
PCIEZO_PERSTn_M2
IZS1_SDIO_MO/PDM_SDIO_MO
1814 — GIZS1_SDIO_MO_PDM_SDIO_MO_RK809 N37
EDP_TX_D3p
EDP_TX_D3n 1H19 EDP_TX_AUXp 1H20 EDP_TX_AUXn VCCI01 1013 1H17 EDP_TX_AVDD_0V9 EDP_TX_AVDD_1V8 AP36 HDMI_TX_D2p HDMI_TX_D2n AR35 HDMI_TX_D1p AP35 HDMI_TX_D1n AP33 HDMI_TX_DOP HDMI_TX_DOn AR32 HDMI_TX_CLKp HDMI_TX_CLKn 1V17 HDMI_TX_HPDIN 1U16 HDMI_TX_REXT 1N14 1N13 HDMI_TX_AVDD_0V9_1 HDMI_TX_AVDD_0V9_2 1P13 HDMI_TX_AVDD_1V8 Sheet: /RK3566_PART_C/ File: RK3566_PART_C.sch Title: Size: A3 Date: KiCad E.D.A. kicad (5.1.10)-1

