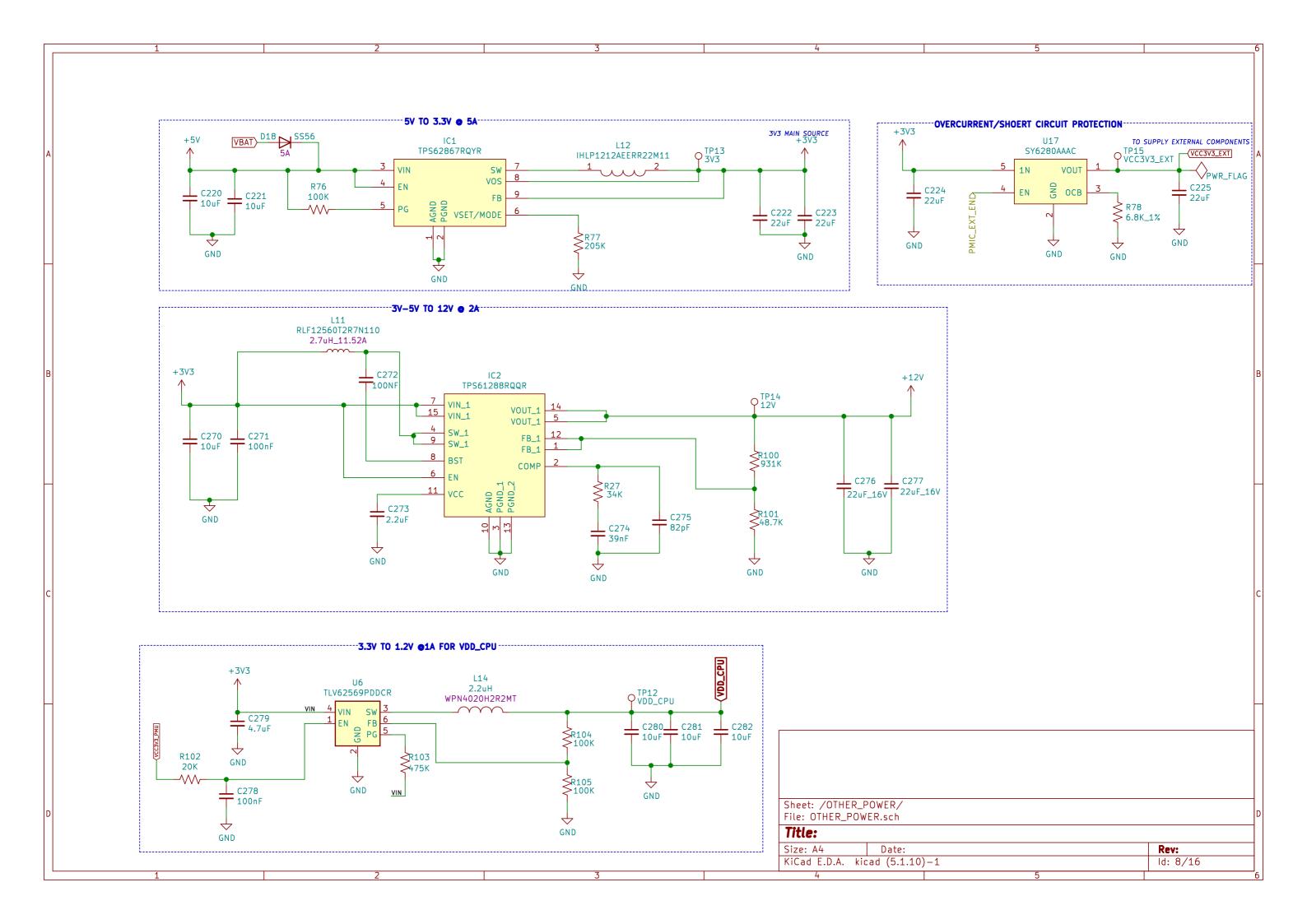
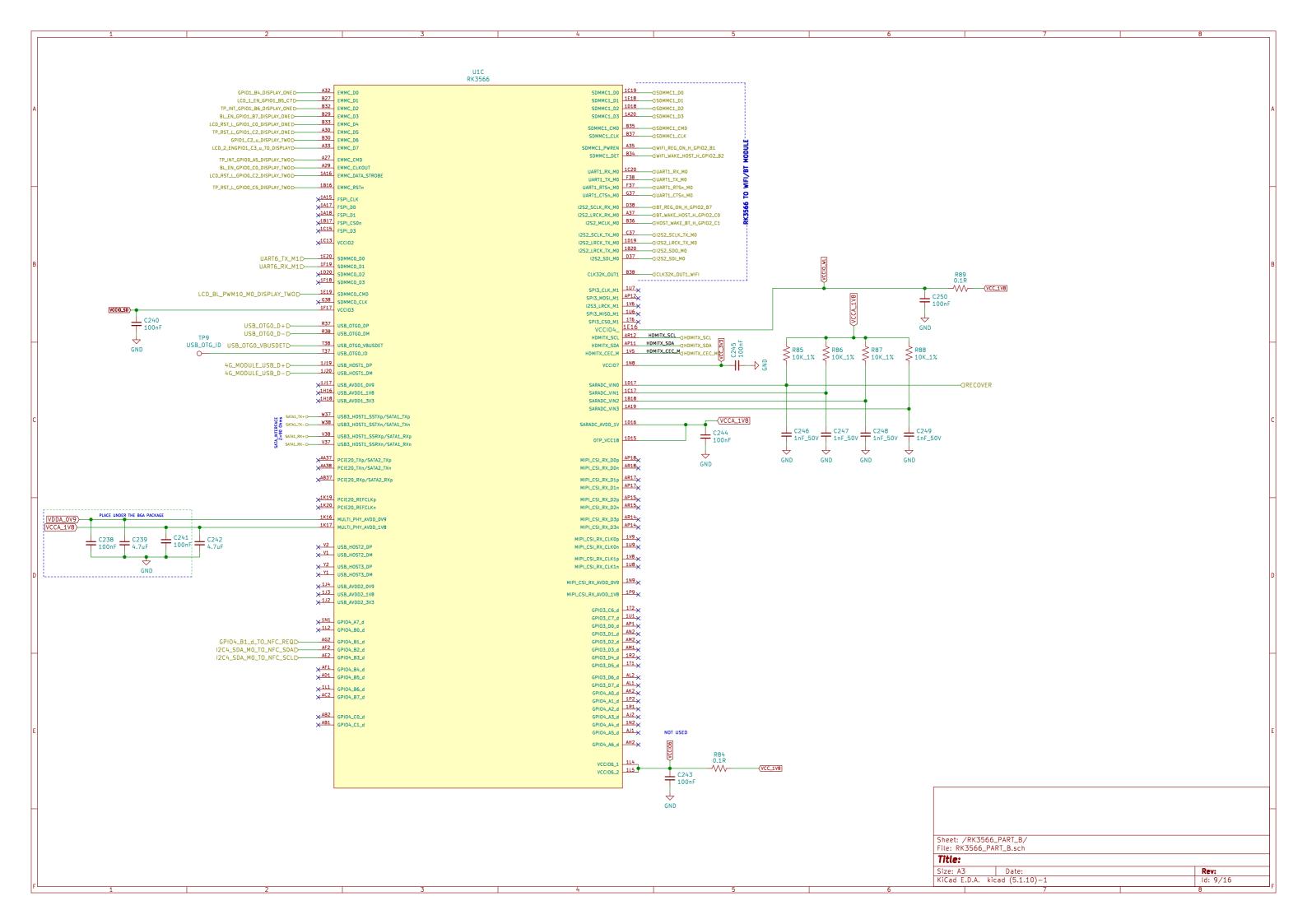
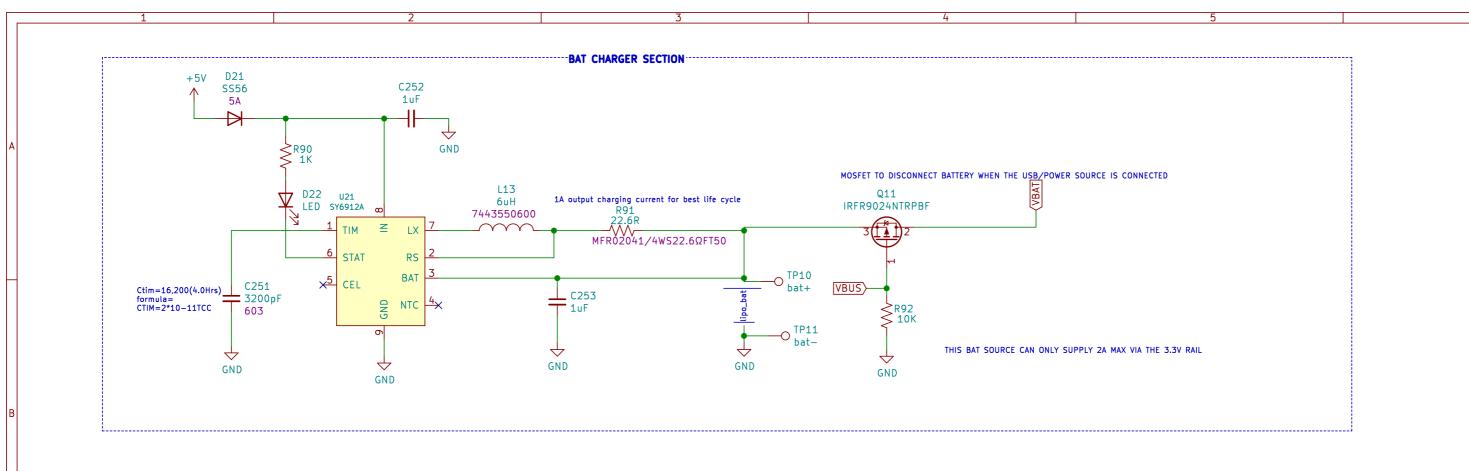
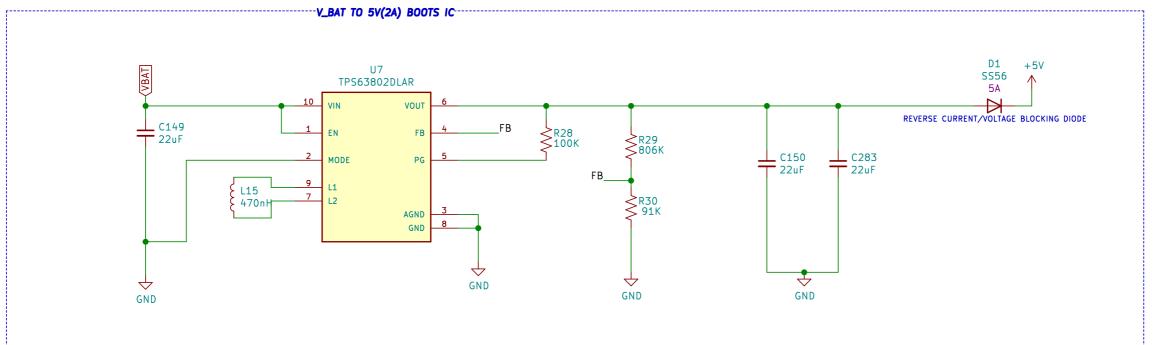


## LPDDR3\_RAM POWER F1 VDD1\_1 F12 VDD1\_2 G4 VDD1\_3 G9 VDD1\_4 T9 VDD1\_5 U1 VDD1\_7 VDD1\_8 VCC1V8\_DDR VSS\_1 A3 VSS\_2 A10 VSS\_3 C1 VSS\_3 C5 VSS\_6 C8 VSS\_6 C12 VSS\_7 D2 VSS\_8 D9 VSS\_10 E1 VSS\_11 E1 VSS\_12 E5 VSS\_13 E12 VSS\_14 G3 VSS\_16 G3 VSS\_16 G3 VSS\_16 G3 VSS\_16 G3 VSS\_17 G5 VSS\_18 G10 VSS\_19 G10 VSS\_19 G10 VSS\_19 G10 VSS\_10 G12 VSS\_20 J11 VSS\_22 J3 VSS\_23 J112 VSS\_26 K2 VSS\_27 K2 VSS\_28 K11 VSS\_26 K4 VSS\_27 K9 VSS\_28 VSS\_26 VSS\_26 VSS\_29 VSS\_26 V1 VSS\_49 VSS\_50 VSS\_49 VSS\_50 AB3 VSS\_56 AB3 VSS\_56 AB3 VSS\_56 AB8 VSS\_57 AB8 VCC\_DDR DECOUPLING CAPACITORS C101 22uF C104 C109 C109 D8 VDDQ\_7 D12 VDDQ\_8 F3 VDDQ\_9 F10 VDDQ\_10 U3 VDDQ\_11 U10 VDDQ\_12 W1 VDDQ\_13 W5 VDDQ\_14 W8 VDDQ\_15 VDQ\_15 AA3 VDDQ\_15 AA5 VDDQ\_18 AA6 VDDQ\_19 AA10 VDDQ\_20 VCC\_DDR DECOUPLING CAPACITORS VCCOV6\_DDR C138 C140 C142 100nF 100nF 100nF C144 C147 C103 C100 C102 C102 A4 VDD2\_1 A9 VDD2\_2 F5 VDD2\_3 F8 VDD2\_3 H1 VDD2\_5 H5 VDD2\_6 VDD2\_6 H8 VDD2\_7 VCC\_DDR) VCC1V8\_DDR H8 VDD2\_7 VDD2\_8 H12 VDD2\_9 K10 VDD2\_11 K10 VDD2\_11 N1 VDD2\_12 N1 VDD2\_13 VDD2\_15 N12 VDD2\_15 N12 VDD2\_16 R1 VDD2\_16 R8 VDD2\_19 R12 VDD2\_18 R8 VDD2\_19 R12 VDD2\_20 UDS VDD2\_21 UB AB4 VDD2\_23 AB9 VDD2\_24 C106 C131 C260 C261 C262 C263 C264 C265 T00nF T00nF T00nF T00nF C266 C267 C131 3.3V TO 0.6V@2A GND +3V3 L10 2.2uH WPN4020H2R2MT U5 TLV62569PDDCR \_\_\_\_\_\_ C107 4.7uF C122 C124 22uF 22uF VCCA1V8\_PMU GND R25 20K DNU\_7 DNU\_8 AA12 AA12 AB12 AB12 DNU\_10 DNU\_11 AB12 DNU\_12 ×A1 ×A2 ×A11 DNU\_1 DNU\_2 ×A11 DNU\_3 ×A12 DNU\_4 ×B1 DNU\_5 DNU\_6 C105 100nF GND GND Sheet: /RK3566\_LPDDR3/LPDDR4\_POWER/ File: LPDDR4\_POWER.sch Title: Size: A3 Date: KiCad E.D.A. kicad (5.1.10)-1









Title:	
Sheet: /BATTERY_CHARGER/ File: BATTERY_CHARGER.sch	

Size: A4	Date:	Rev:	
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