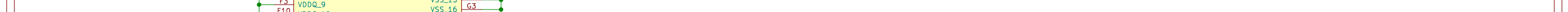
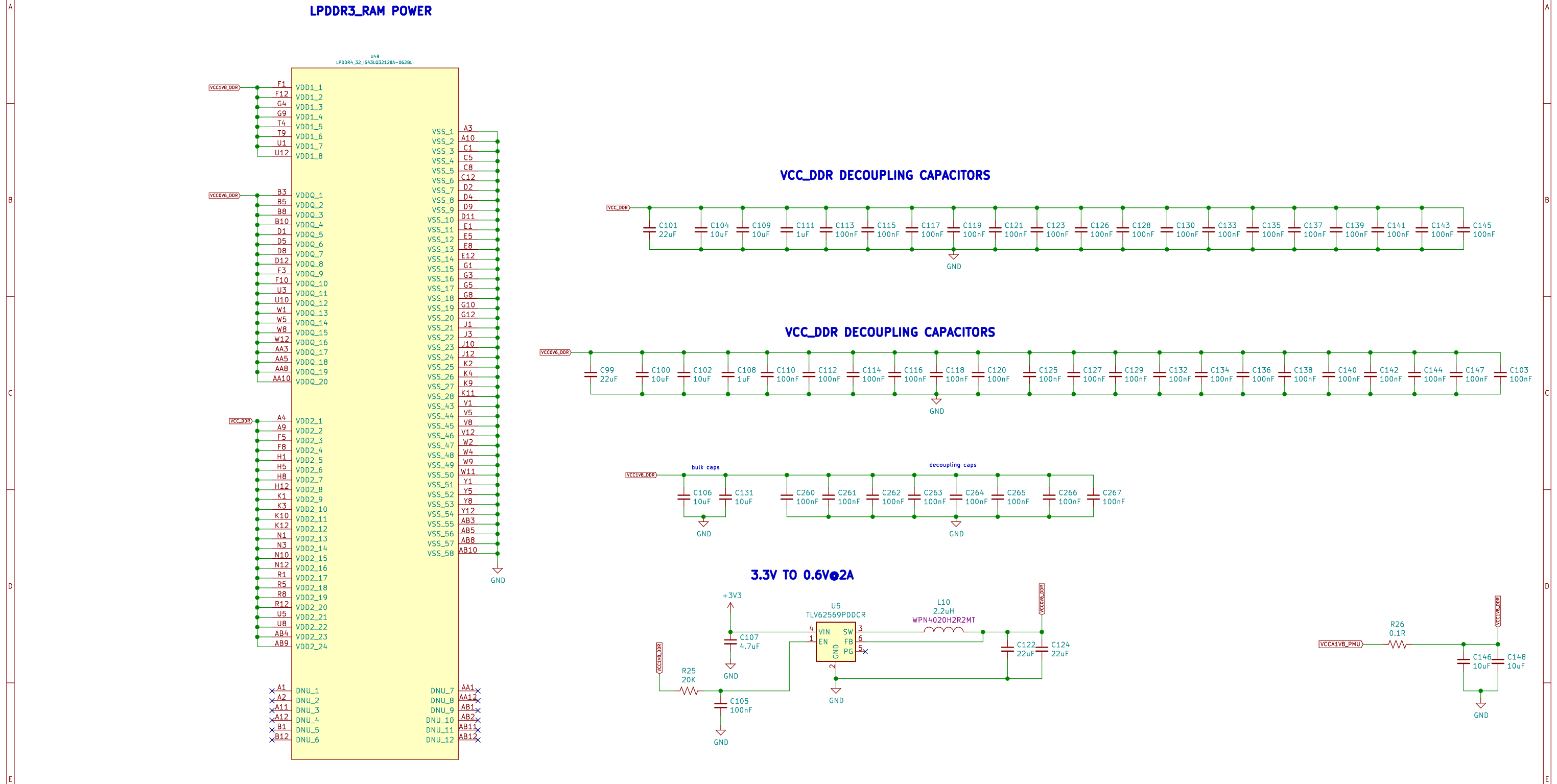
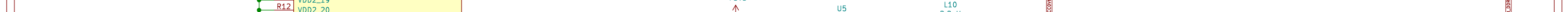
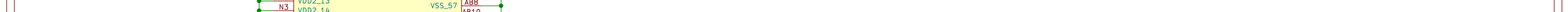


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Timing diagram showing the relationship between VDDQ6, VSS5, and J12 signals. The diagram includes a legend for VDDQ6_DDR (green), VSS5_23 (blue), and J12 (red). The signals are plotted against time, with VDDQ6_DDR and VSS5_23 showing a transition from high to low, and J12 showing a transition from low to high. The diagram is labeled with AA3, VDDQ_17, VSS_23, J12, and VCCQV6_DDR.



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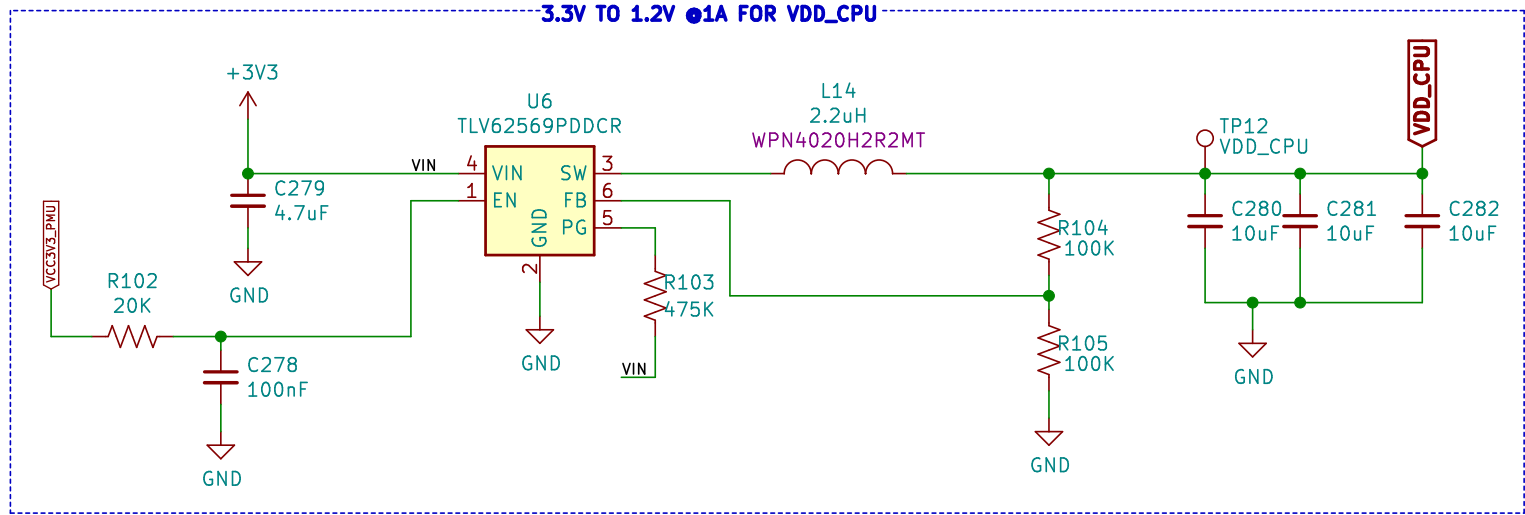
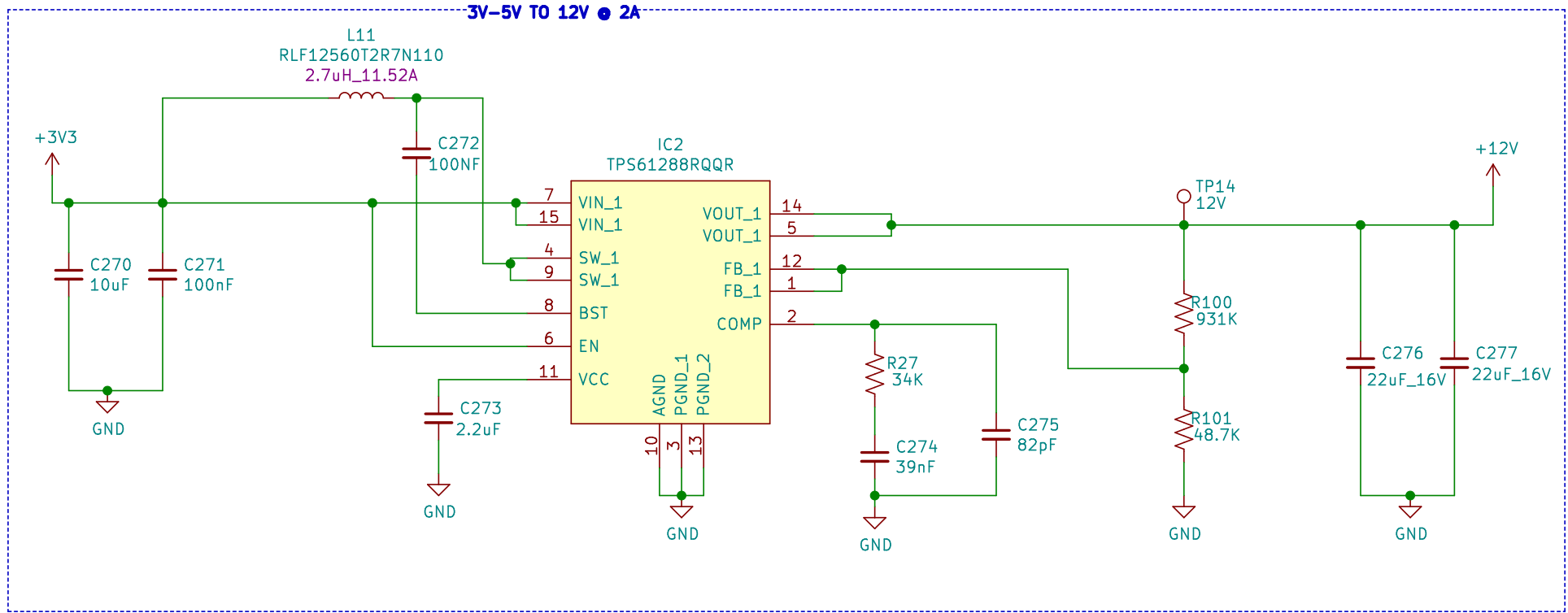
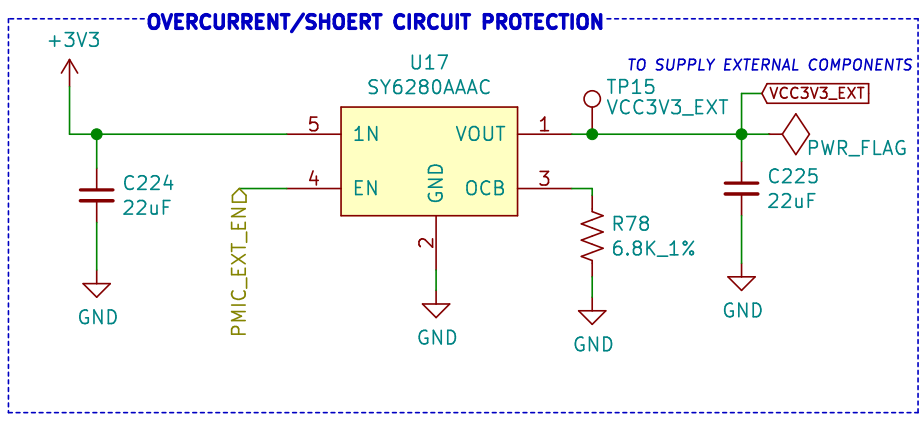
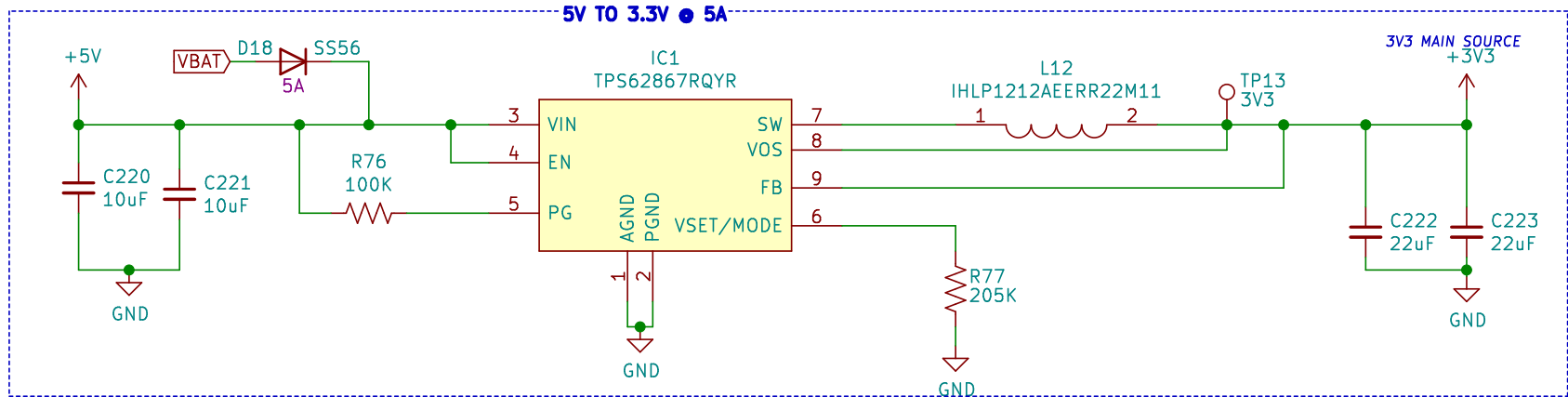
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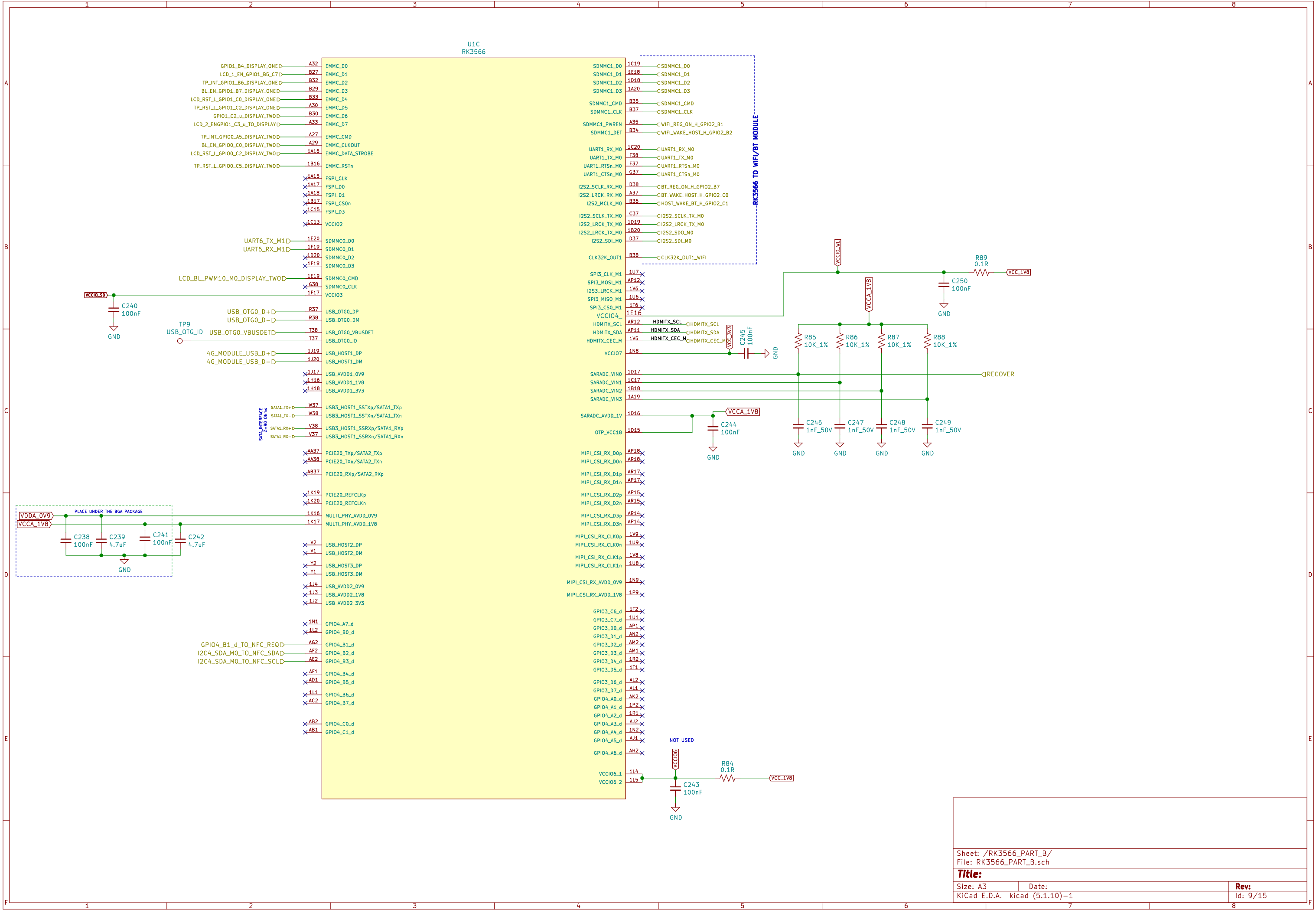
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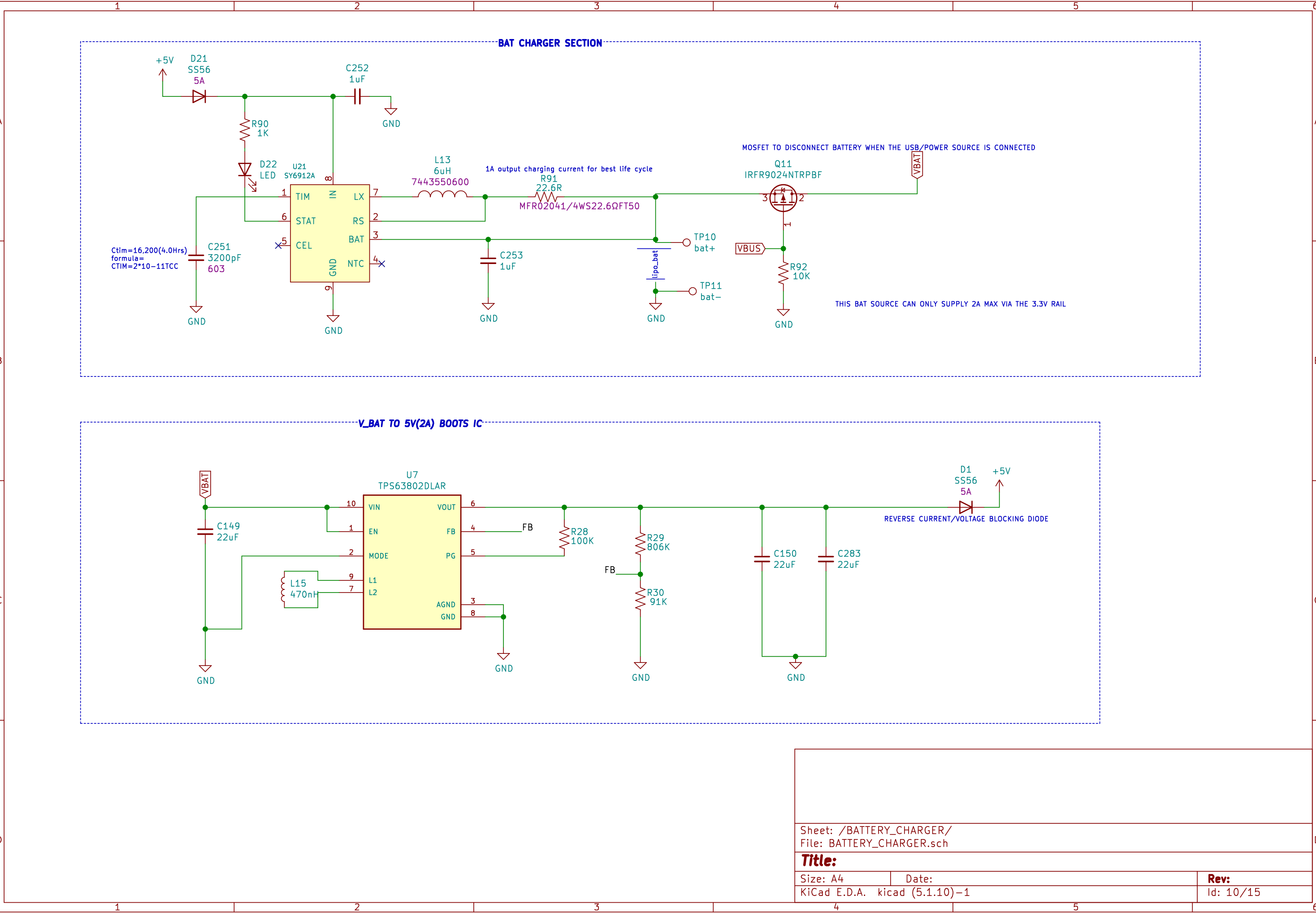
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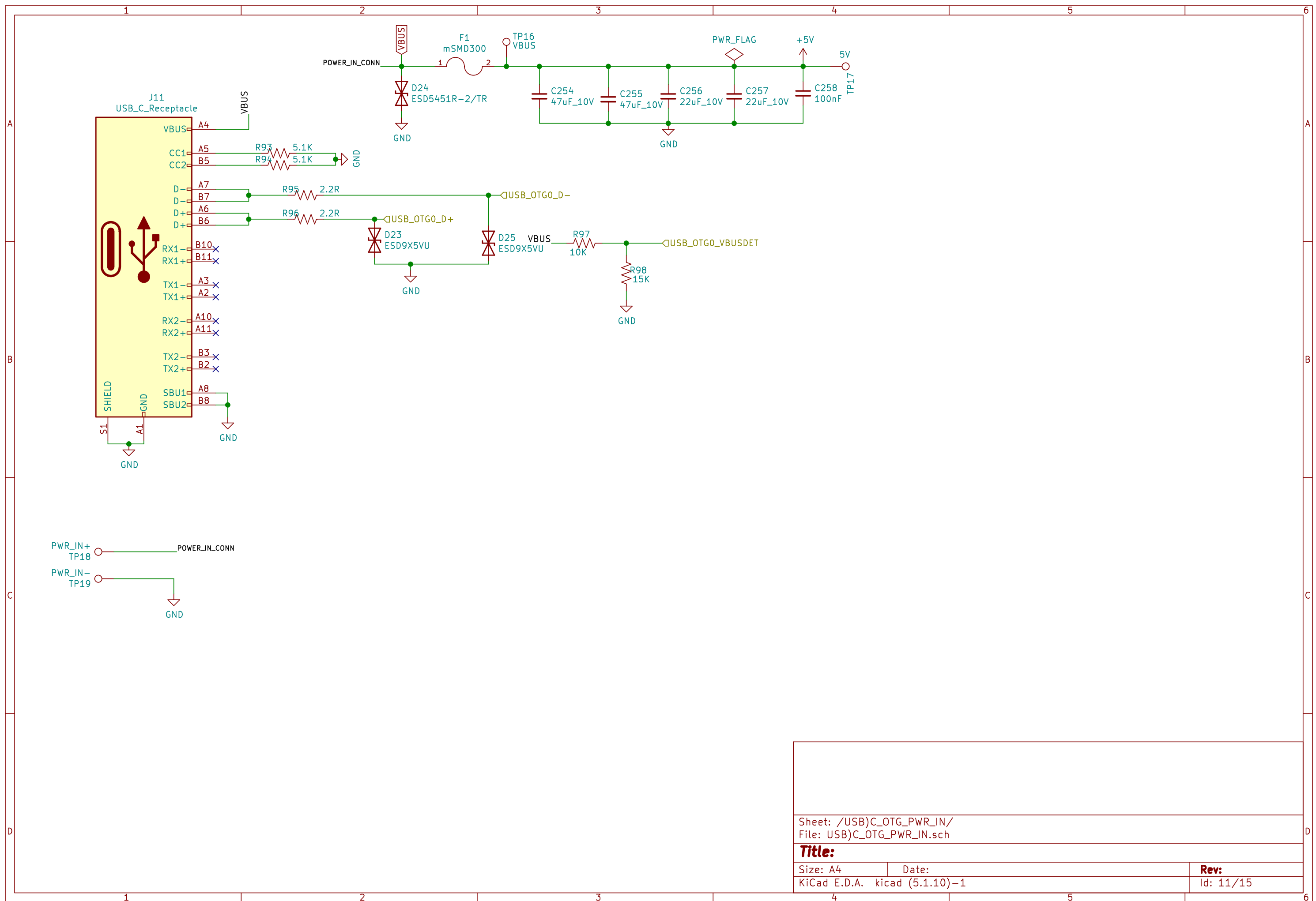
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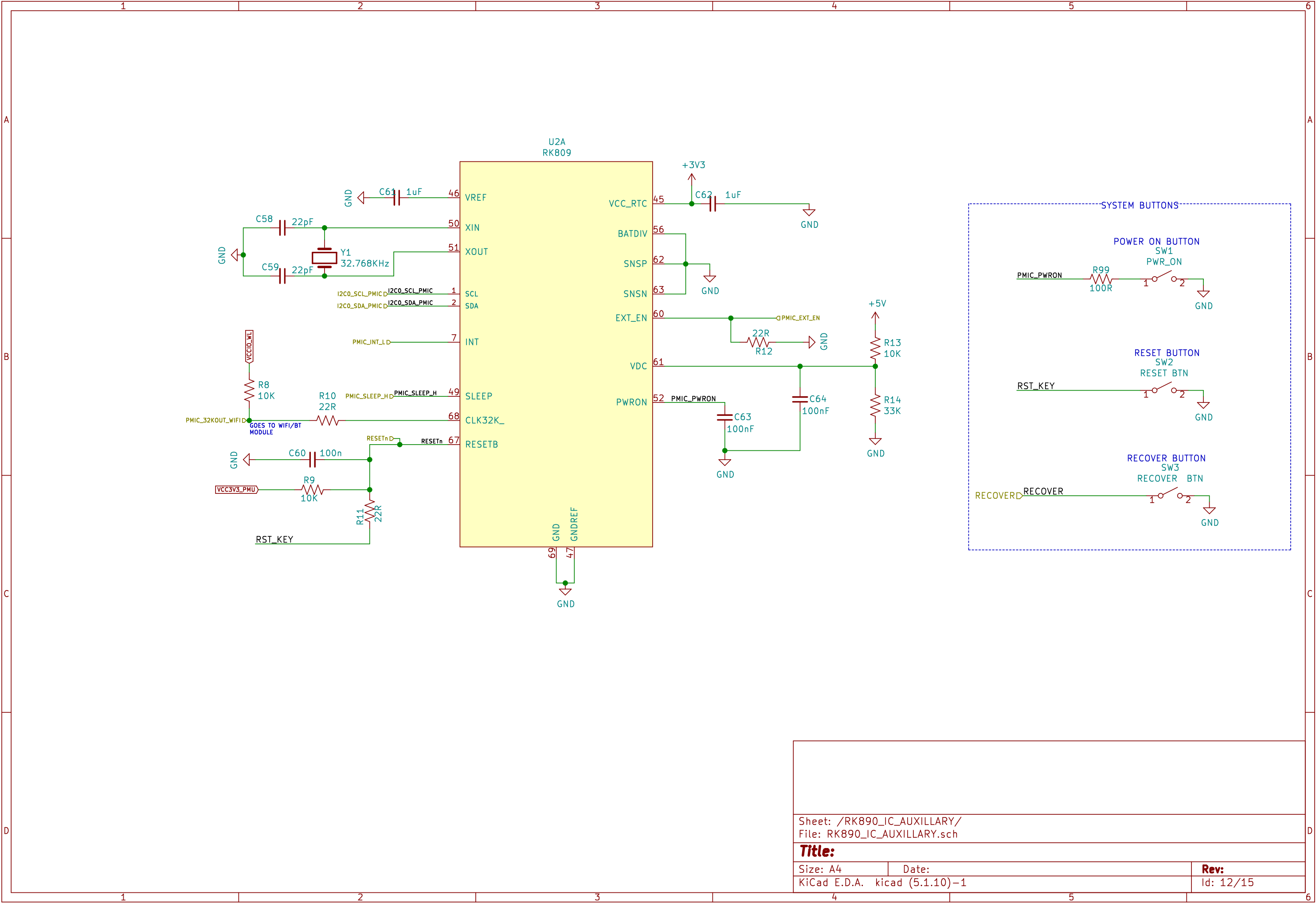
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NOTE: SCREEN USED AS RECOMMENDED IS A 4x AMOLED DISPLAY WITH 30 PIN MIPI FPC CONNECTION.
LINK FOR VIEW AND BUY: <https://www.aliibaba.com/product-detail/5-5-Inch-HD-720-1080-6241691540.html>

