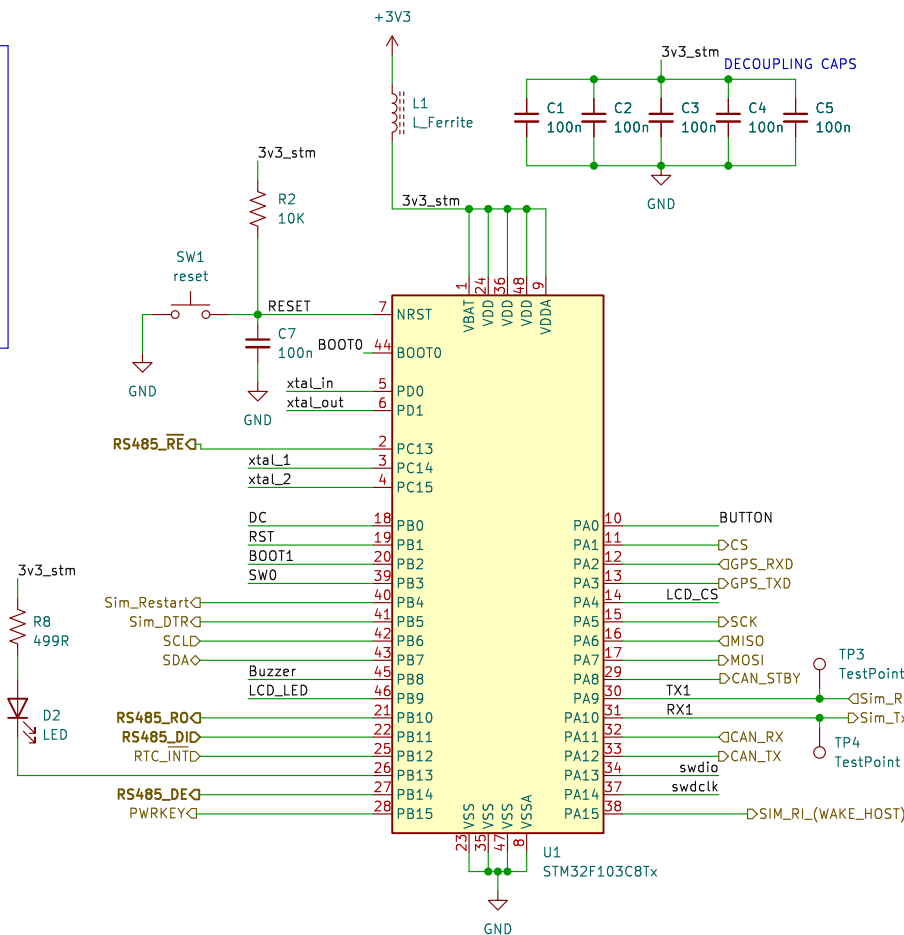
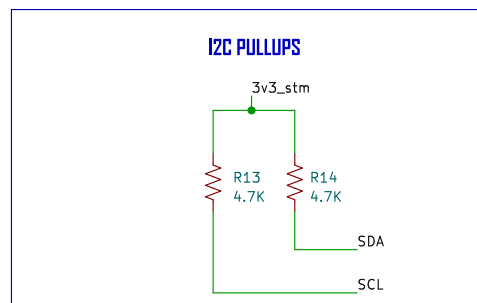
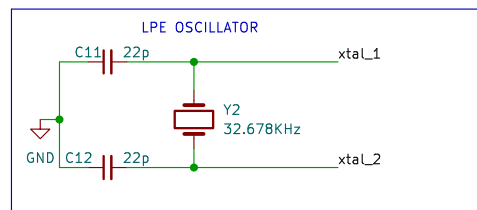
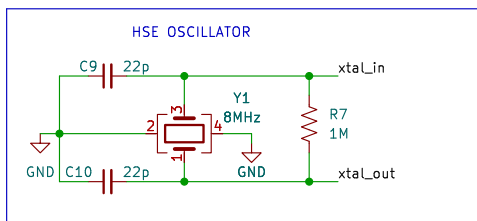
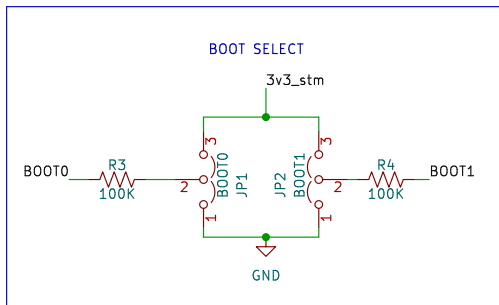
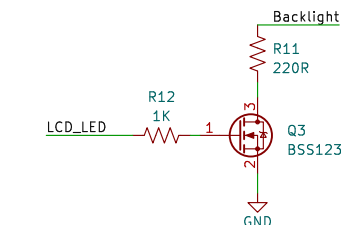
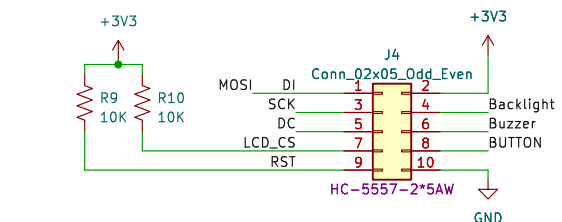
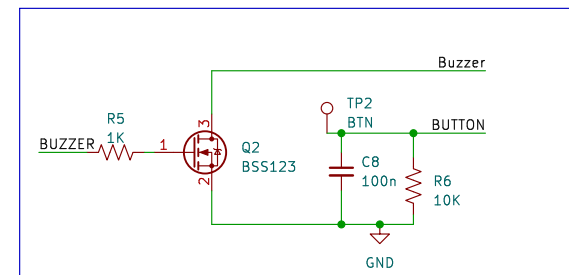
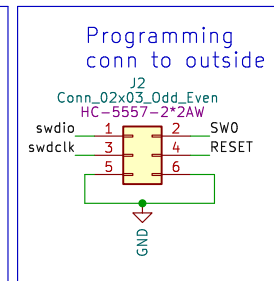
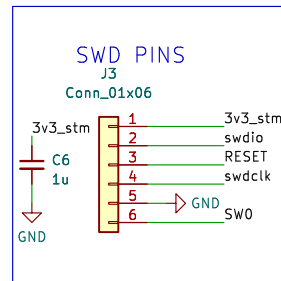


BIM BOARD	
Designed By: Robert Mutura	
Reviewed by: Timothy Kyalo	
GEVITON	
Sheet: /	
File: BIM_PCB.kicad_sch	
Title: BIM BOARD	
Size: A4	Date: 2023-12-06
KiCad E.D.A. kicad 7.0.2	Rev: REV 2.0.0
	Id: 1/14

# STM32F103C8T6 MCU



- H1 MountingHole
- H2 MountingHole
- H3 MountingHole
- H4 MountingHole



BIM BOARD  
Designed By: Robert Mutura  
Reviewed by: Timothy Kyalo

## GEVITON

Sheet: /STM32FC8T6\_MCU/  
File: STMFC8T6\_MCU.kicad\_sch

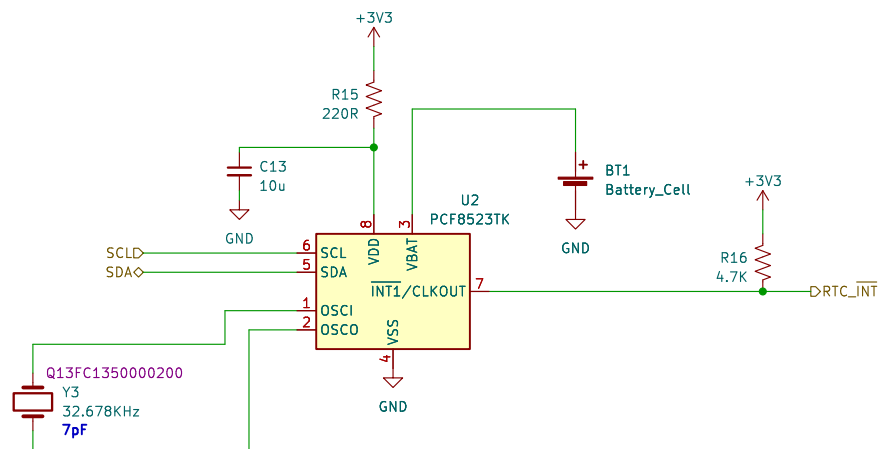
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Size: A4 Date: 2023-12-06  
KiCad E.D.A. kicad 7.0.2

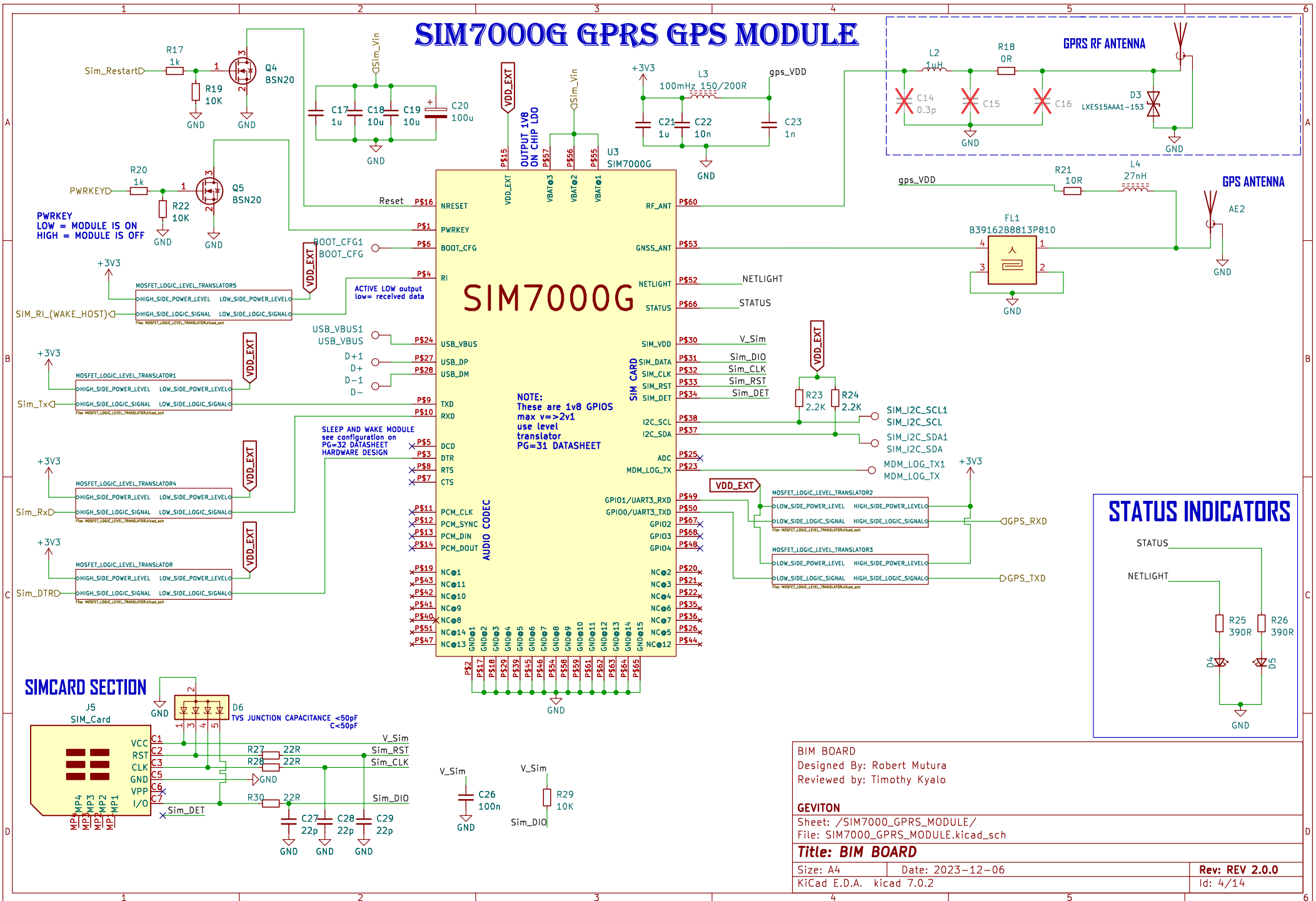
Rev: REV 2.0.0  
Id: 2/14

# PCF8523

## RTC IC



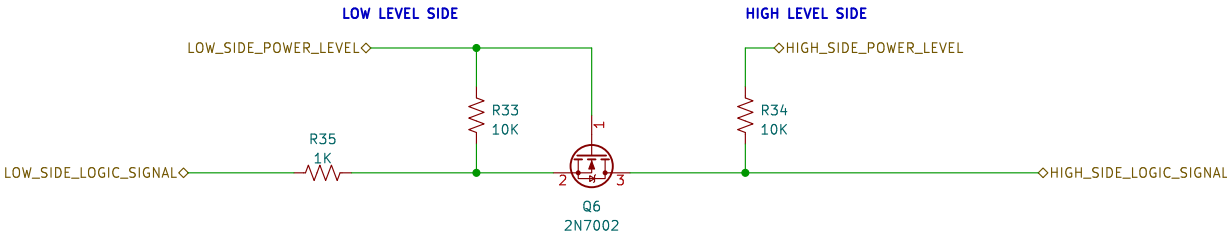
BIM BOARD		
Designed By: Robert Mutura		
Reviewed by: Timothy Kyalo		
GEVITON		
Sheet: /RTC_MODULE_IC_PCF8523/		
File: RTC_MODULE_IC_PCF8523.kicad_sch		
Title: BIM BOARD		
Size: A4	Date: 2023-12-06	Rev: REV 2.0.0
KiCad E.D.A. kicad 7.0.2		Id: 3/14



Rev: REV 2.0.0  
Id: 4/14

# BI-DIRECTIONAL LEVEL TRANSLATOR

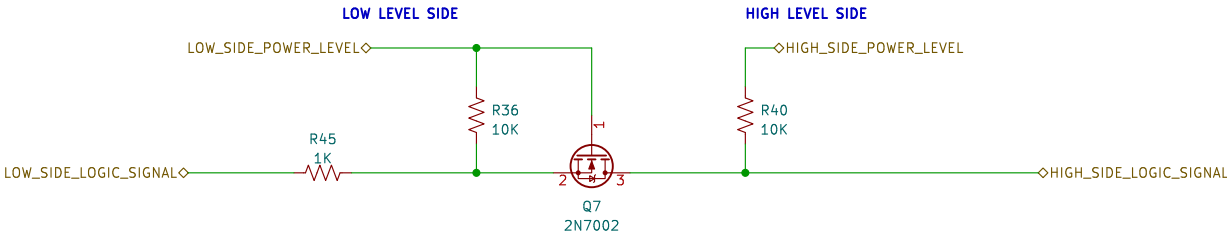
## MOSFET - IMPLEMENTATION



Sheet: /SIM7000_GPRS_MODULE/MOSFET_LOGIC_LEVEL_TRANSLATOR2/ File: MOSFET_LOGIC_LEVEL_TRANSLATOR.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 7.0.2		Id: 5/14

# BI-DIRECTIONAL LEVEL TRANSLATOR

## MOSFET - IMPLEMENTATION



Sheet: /SIM7000\_GPRS\_MODULE/MOSFET\_LOGIC\_LEVEL\_TRANSLATOR3/  
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**Title:**

Size: A4

Date:

KiCad E.D.A. kicad 7.0.2

Rev:

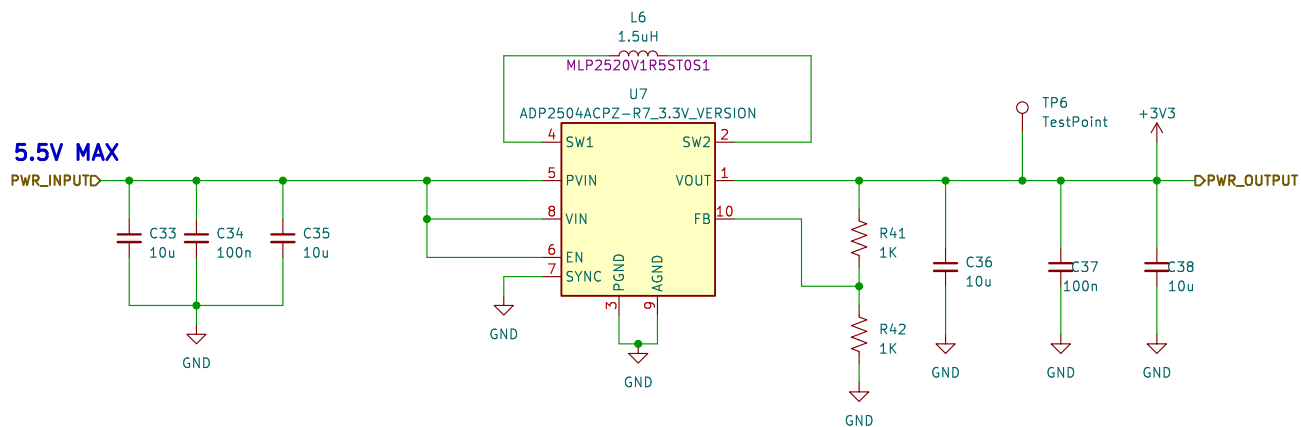
Id: 6/14



BIM BOARD	
Designed By: Robert Mutura	
Reviewed by: Timothy Kyalo	
GEVITON	
Sheet: /RS485_MAX485/	
File: RS485_MAX485.kicad_sch	
Title: <b>BIM BOARD</b>	
Size: A4	Rev: <b>REV 2.0.0</b>
Date: 2023-12-06	
KiCad E.D.A. kicad 7.0.2	Id: 7/14

# ADP2504ACPZ-R7

## BUCK BOOST CONVERTER 1A



BIM BOARD  
Designed By: Robert Mutura  
Reviewed by: Timothy Kyalo

### GEVITON

Sheet: /BUCK\_AD2504ACPZ-R7/  
File: BUCK\_REGULATOR\_AD2504ACPZ-R7.kicad\_sch

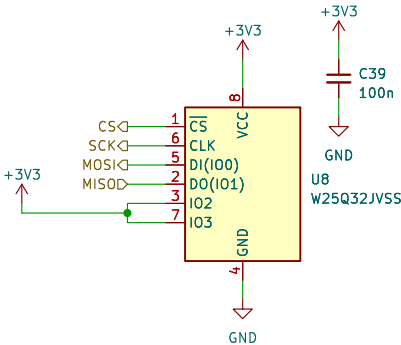
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Size: A4  
Date: 2023-12-06  
KiCad E.D.A. kicad 7.0.2

Rev: REV 2.0.0  
Id: 8/14

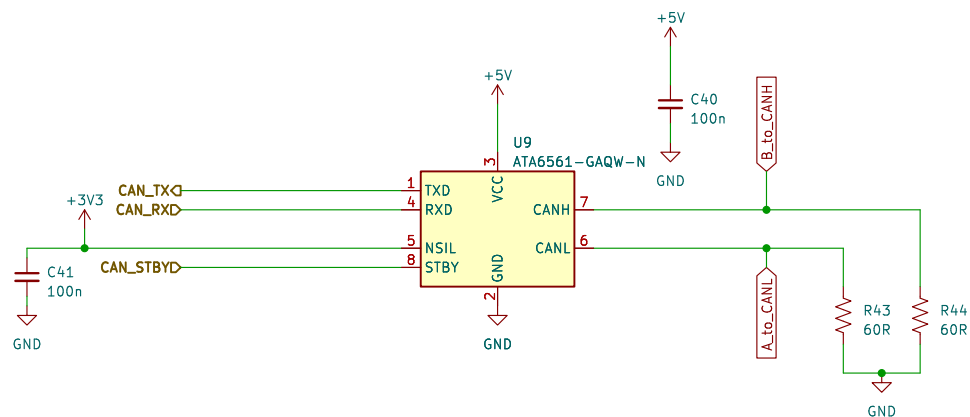


# FLASH MEMEORY



BIM BOARD		
Designed By: Robert Mutura		
Reviewed by: Timothy Kyalo		
GEVITON		
Sheet: /FLASH_MEMORY/		
File: FLASH_MEMORY.kicad_sch		
Title: BIM BOARD		
Size: A4	Date: 2023-12-06	Rev: REV 2.0.0
KiCad E.D.A. kicad 7.0.2		Id: 9/14

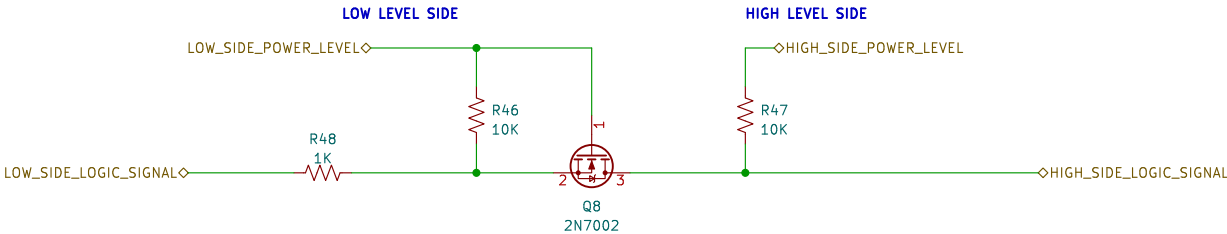
CAN TRANSCEIVER  
ATA6561



BIM BOARD		
Designed By: Robert Mutura		
Reviewed by: Timothy Kyalo		
GEVITON		
Sheet: /CAN_TRANSCEIVER_ATA6561/		
File: CAN_TRANSCEIVER_ATA6561.kicad_sch		
Title: BIM BOARD		
Size: A4	Date: 2023-12-06	Rev: REV 2.0.0
KiCad E.D.A. kicad 7.0.2	Id: 10/14	

# BI-DIRECTIONAL LEVEL TRANSLATOR

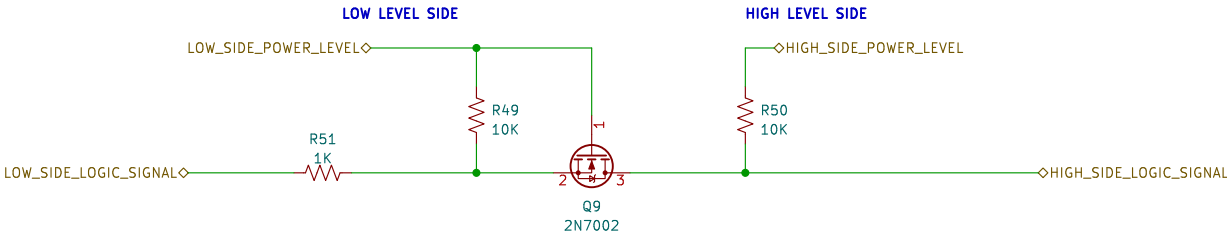
## MOSFET - IMPLEMENTATION



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<b>Title:</b>		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 7.0.2		Id: 11/14

# BI-DIRECTIONAL LEVEL TRANSLATOR

## MOSFET - IMPLEMENTATION



Sheet: /SIM7000\_GPRS\_MODULE/MOSFET\_LOGIC\_LEVEL\_TRANSLATOR5/  
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**Title:**

Size: A4

Date:

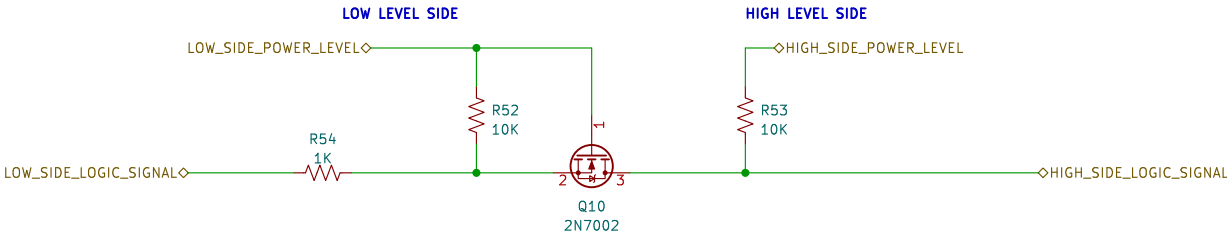
KiCad E.D.A. kicad 7.0.2

**Rev:**

Id: 12/14

# BI-DIRECTIONAL LEVEL TRANSLATOR

## MOSFET - IMPLEMENTATION



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**Title:**

Size: A4

Date:

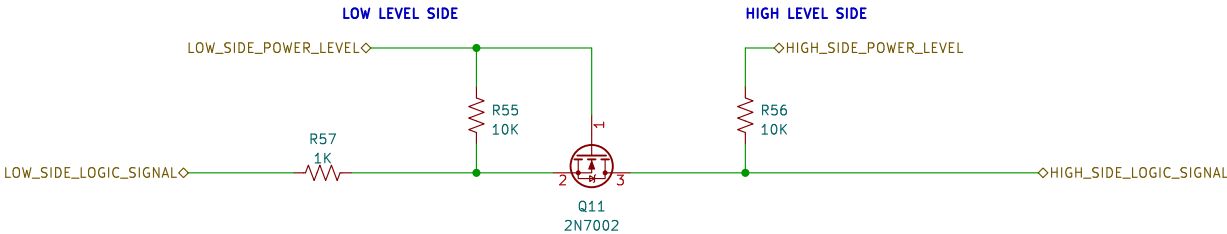
KiCad E.D.A. kicad 7.0.2

**Rev:**

Id: 13/14

# BI-DIRECTIONAL LEVEL TRANSLATOR

## MOSFET - IMPLEMENTATION



Sheet: /SIM7000\_GPRS\_MODULE/MOSFET\_LOGIC\_LEVEL\_TRANSLATOR4/  
File: MOSFET\_LOGIC\_LEVEL\_TRANSLATOR.kicad\_sch

**Title:**

Size: A4

Date:

KiCad E.D.A. kicad 7.0.2

Rev:

Id: 14/14