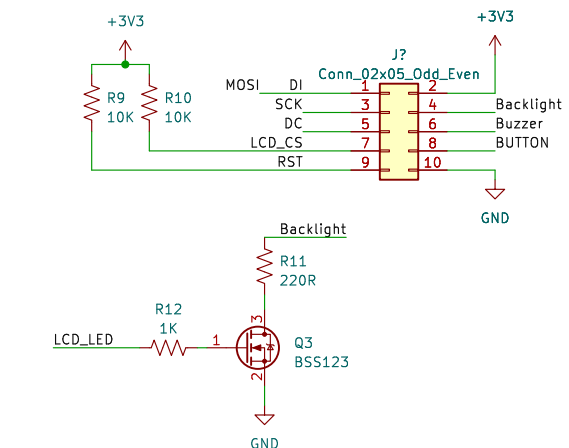
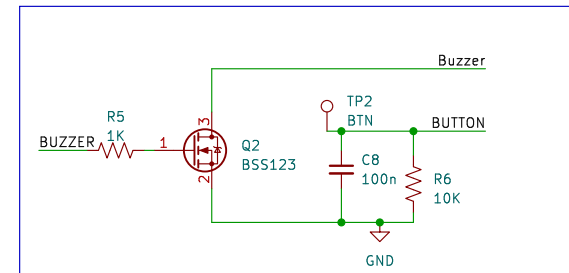
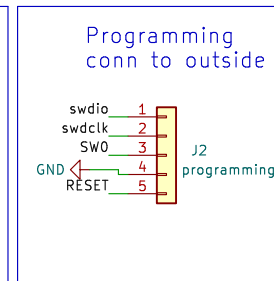
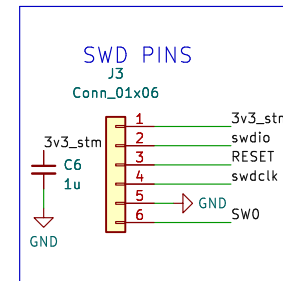
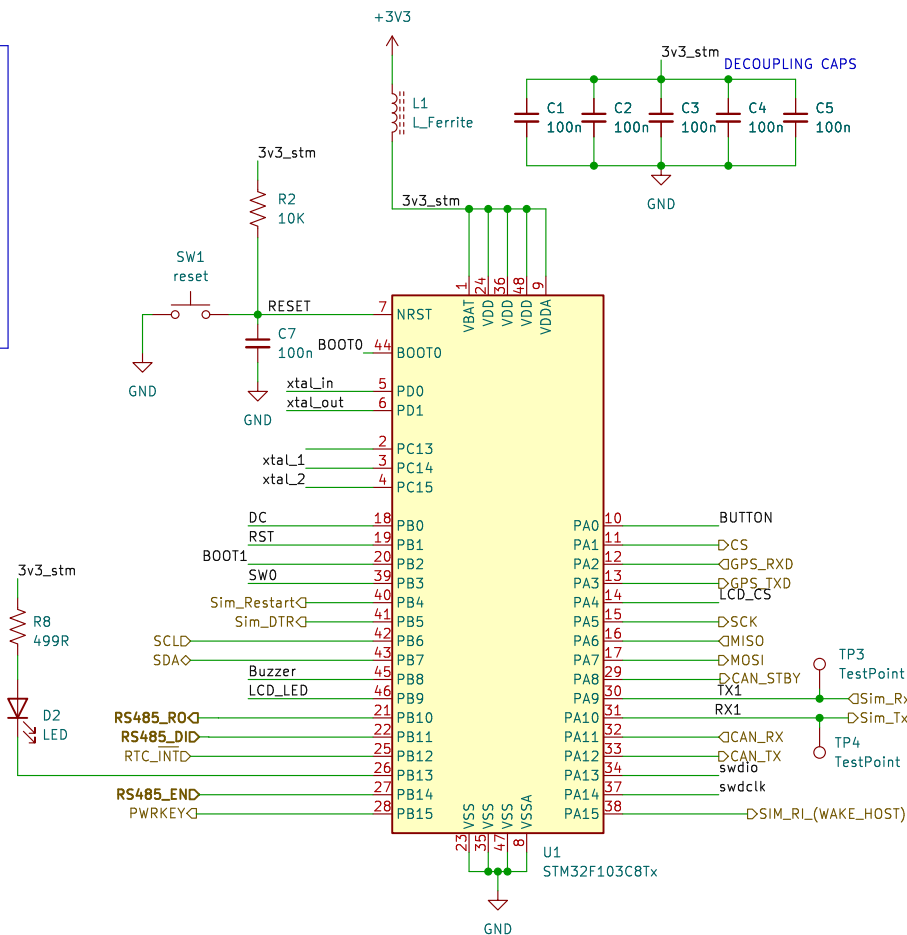
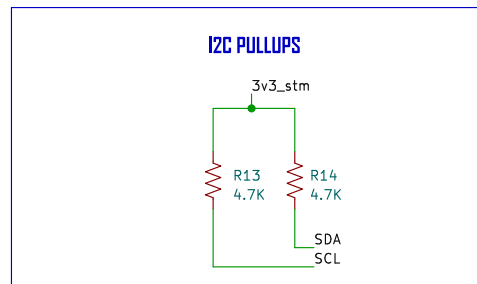
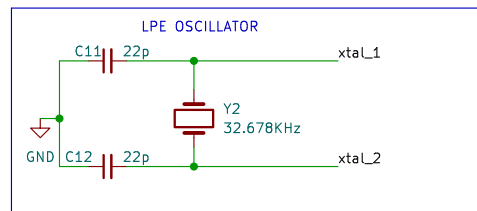
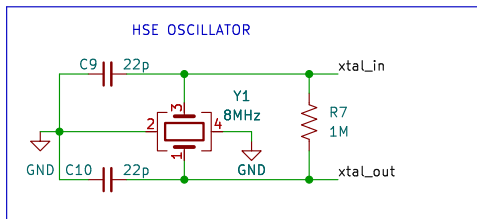
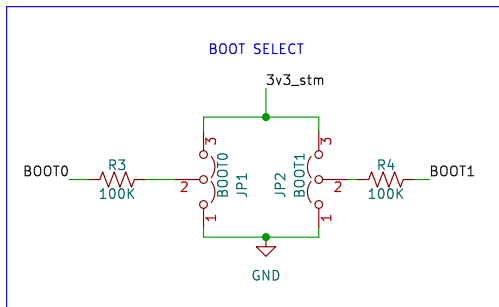


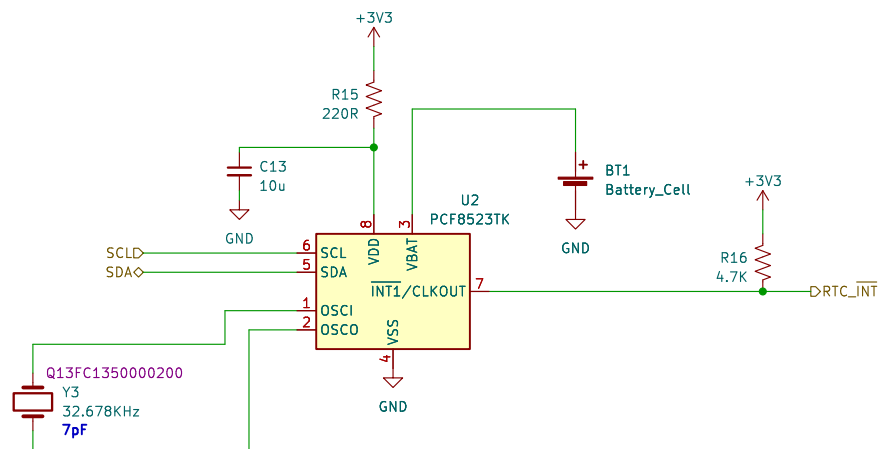
STM32F103C8T6 MCU



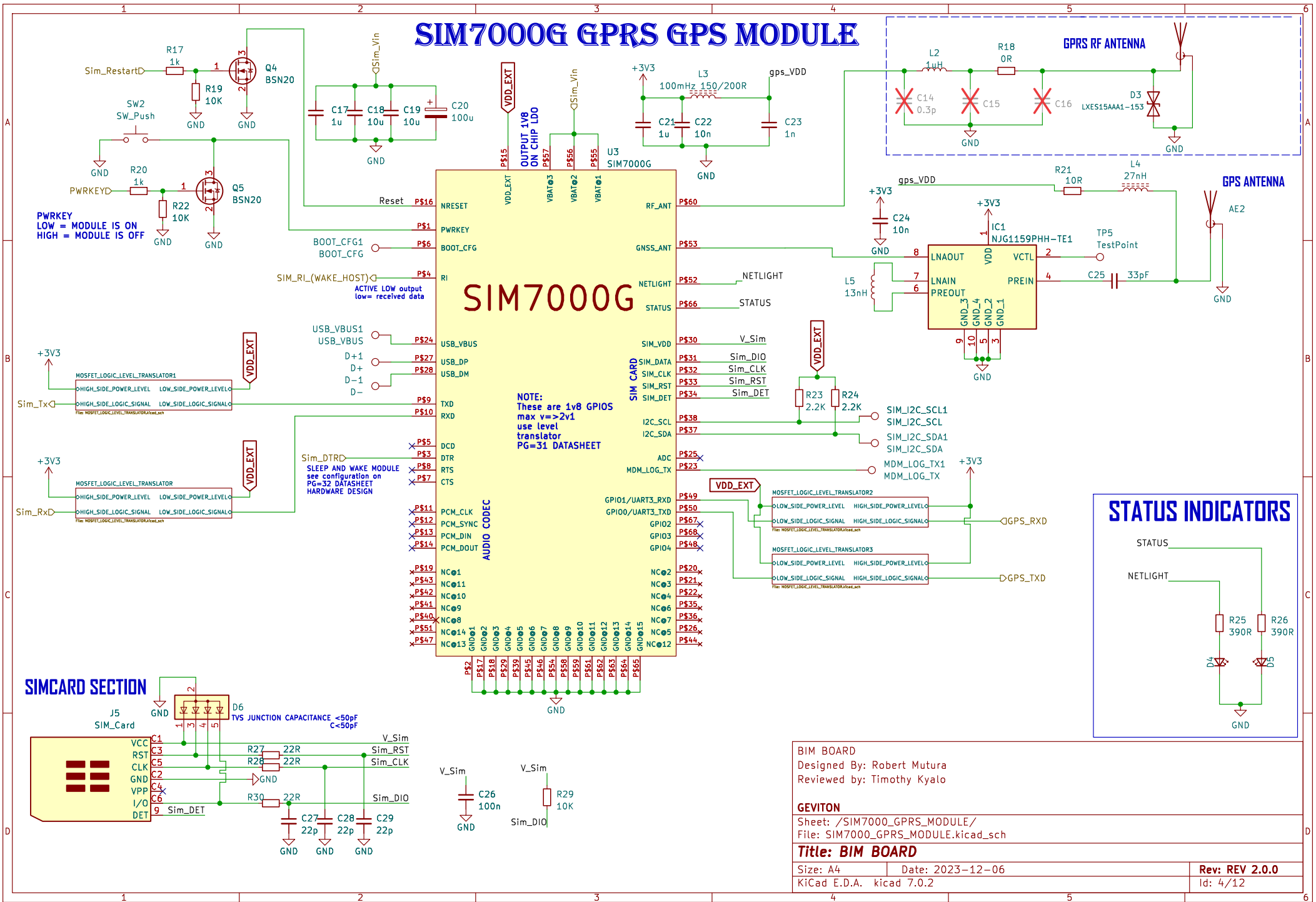
BIM BOARD	
Designed By: Robert Mutura	
Reviewed by: Timothy Kyalo	
GEVITON	
Sheet: /STM32FC8T6_MCU/	
File: STMFC8T6_MCU.kicad_sch	
Title: BIM BOARD	
Size: A4	Date: 2023-12-06
KiCad E.D.A. kicad 7.0.2	Rev: REV 2.0.0
	Id: 2/12

PCF8523

RTC IC



BIM BOARD		
Designed By: Robert Mutura		
Reviewed by: Timothy Kyalo		
GEVITON		
Sheet: /RTC_MODULE_IC_PCF8523/		
File: RTC_MODULE_IC_PCF8523.kicad_sch		
Title: BIM BOARD		
Size: A4	Date: 2023-12-06	Rev: REV 2.0.0
KiCad E.D.A. kicad 7.0.2		Id: 3/12



Rev: REV 2.0.0
Id: 4/12

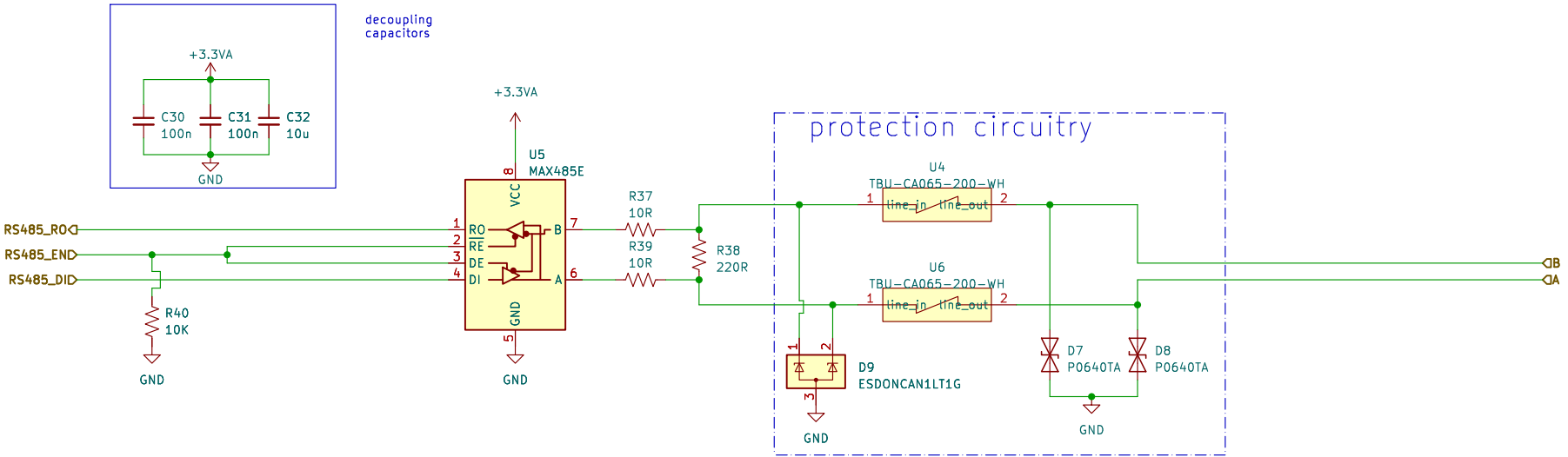
1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

Sheet: /SIM7000_GPRS_MODULE/MOSFET_LOGIC_LEVEL_TRANSLATOR2/ File: MOSFET_LOGIC_LEVEL_TRANSLATOR.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 7.0.2	Id: 5/12	

1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

Sheet: /SIM7000_GPRS_MODULE/MOSFET_LOGIC_LEVEL_TRANSLATOR3/ File: MOSFET_LOGIC_LEVEL_TRANSLATOR.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 7.0.2		Id: 6/12

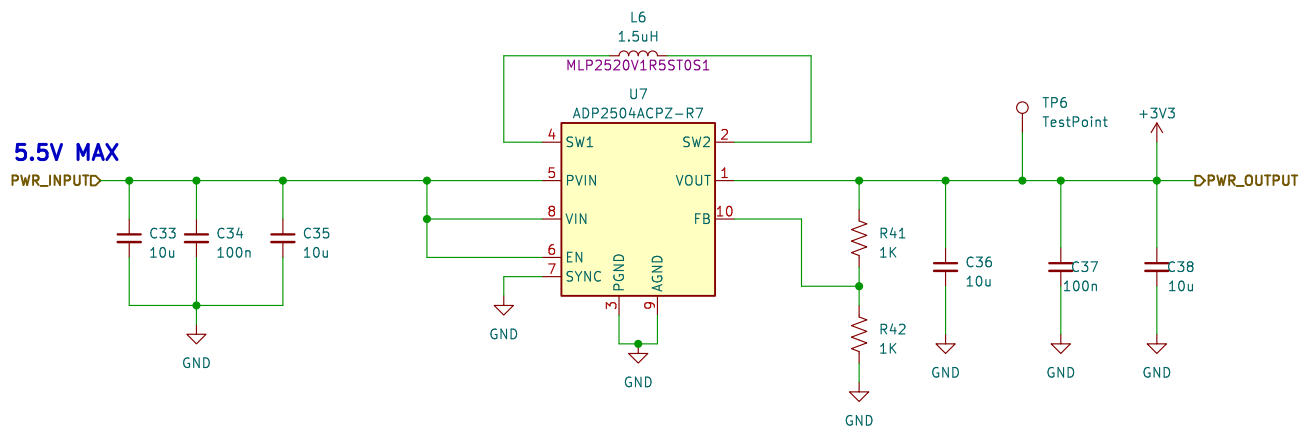
RS485 MAX485E



BIM BOARD		
Designed By: Robert Mutura		
Reviewed by: Timothy Kyalo		
GEVITON		
Sheet: /RS485_MAX485/		
File: RS485_MAX485.kicad_sch		
Title: BIM BOARD		
Size: A4	Date: 2023-12-06	Rev: REV 2.0.0
KiCad E.D.A. kicad 7.0.2		Id: 7/12

ADP2504ACPZ-R7

BUCK BOOST CONVERTER 1A



BIM BOARD
Designed By: Robert Mutura
Reviewed by: Timothy Kyalo

GEVITON

Sheet: /BUCK_AD2504ACPZ-R7/
File: BUCK_REGULATOR_AD2504ACPZ-R7.kicad_sch

Title: BIM BOARD

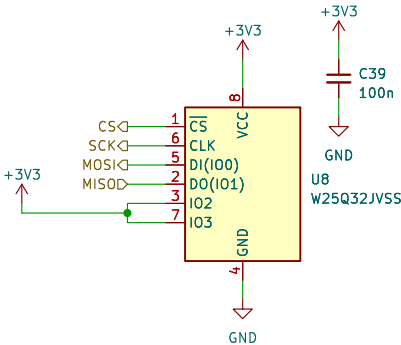
Size: A4
KiCad E.D.A. kicad 7.0.2

Date: 2023-12-06

Rev: REV 2.0.0

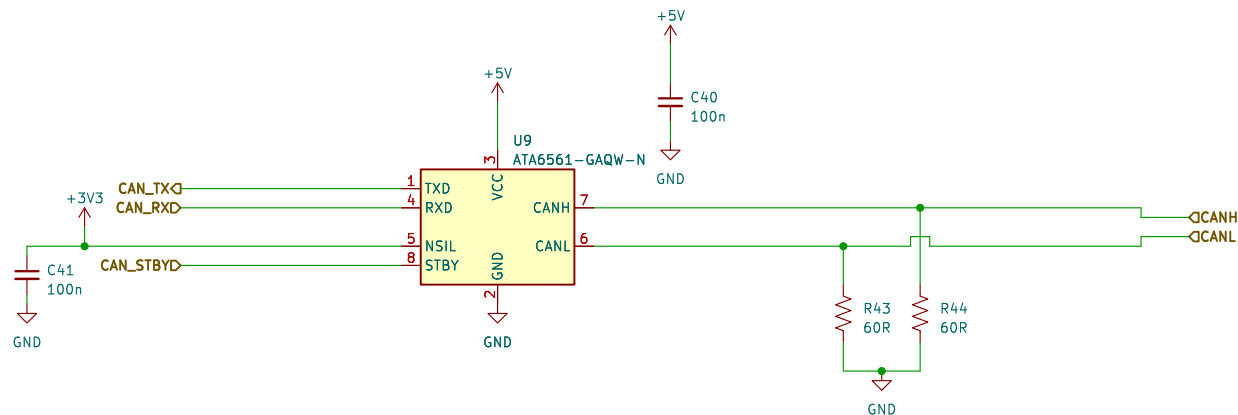
Id: 8/12

FLASH MEMEORY



BIM BOARD		
Designed By: Robert Mutura		
Reviewed by: Timothy Kyalo		
GEVITON		
Sheet: /FLASH_MEMORY/		
File: FLASH_MEMORY.kicad_sch		
Title: BIM BOARD		
Size: A4	Date: 2023-12-06	Rev: REV 2.0.0
KiCad E.D.A. kicad 7.0.2		Id: 9/12

CAN TRANSCEIVER ATA6561



BIM BOARD		
Designed By: Robert Mutura		
Reviewed by: Timothy Kyalo		
GEVITON		
Sheet: /CAN_TRANSCEIVER_ATA6561/		
File: CAN_TRANSCEIVER_ATA6561.kicad_sch		
Title: BIM BOARD		
Size: A4	Date: 2023-12-06	Rev: REV 2.0.0
KiCad E.D.A. kicad 7.0.2	Id: 10/12	

1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

Sheet: /SIM7000_GPRS_MODULE/MOSFET_LOGIC_LEVEL_TRANSLATOR1/ File: MOSFET_LOGIC_LEVEL_TRANSLATOR.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 7.0.2	Id: 11/12	

	1	2	3	4	5	6
A						
B						
C						
D						
	1	2	3	4	5	6

Sheet: /SIM7000_GPRS_MODULE/MOSFET_LOGIC_LEVEL_TRANSLATOR/ File: MOSFET_LOGIC_LEVEL_TRANSLATOR.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 7.0.2		Id: 12/12