

BI-DIRECTIONAL LEVEL TRANSLATOR **MOSFET - IMPLEMENTATION** LOW LEVEL SIDE HIGH LEVEL SIDE LOW_SIDE_POWER_LEVEL >--→ HIGH_SIDE_POWER_LEVEL ₹ R17 ₹ R18 R19 → HIGH_SIDE_LOGIC_SIGNAL 2N7002 BIM BOARD Designed By: Robert Mutura Reviewed by: Timothy Kyalo Sheet: /A9G_GSM_GPRS_MODULE/MOSFET_LOGIC_LEVEL_TRANSLATOR6/ File: MOSFET_LOGIC_LEVEL_TRANSLATOR.kicad_sch

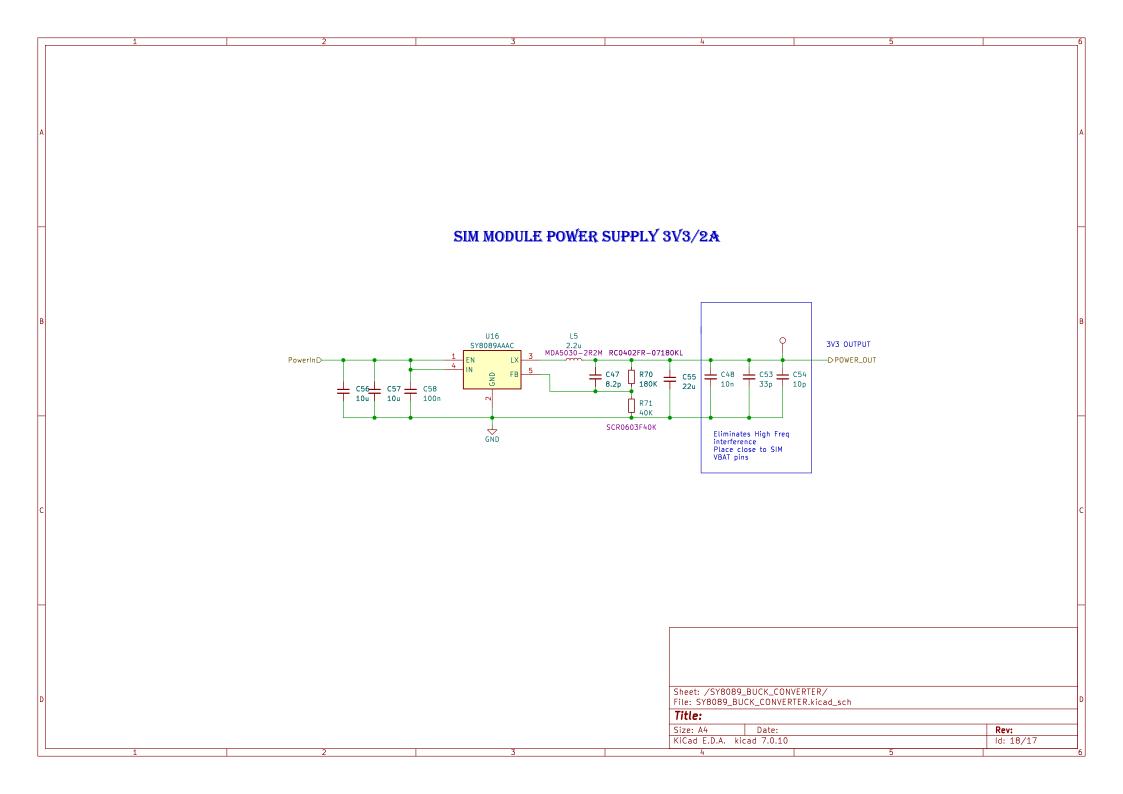
Title: BIM BOARD

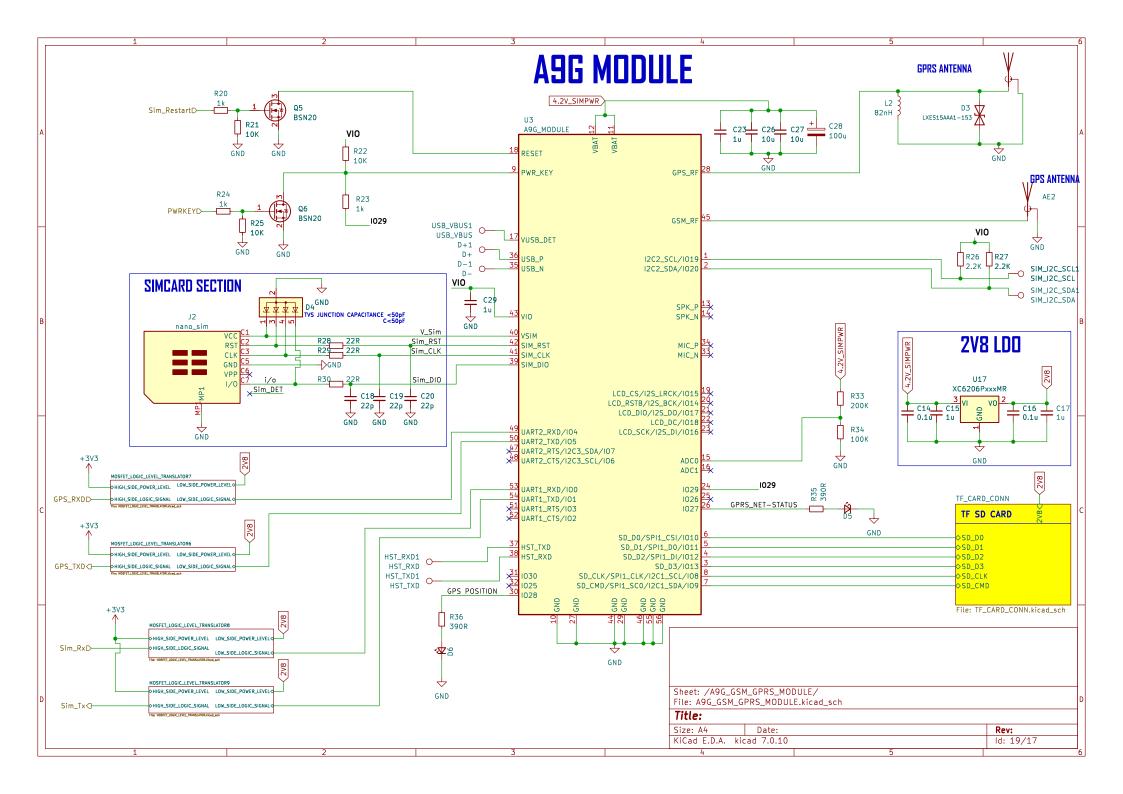
KiCad E.D.A. kicad 7.0.10

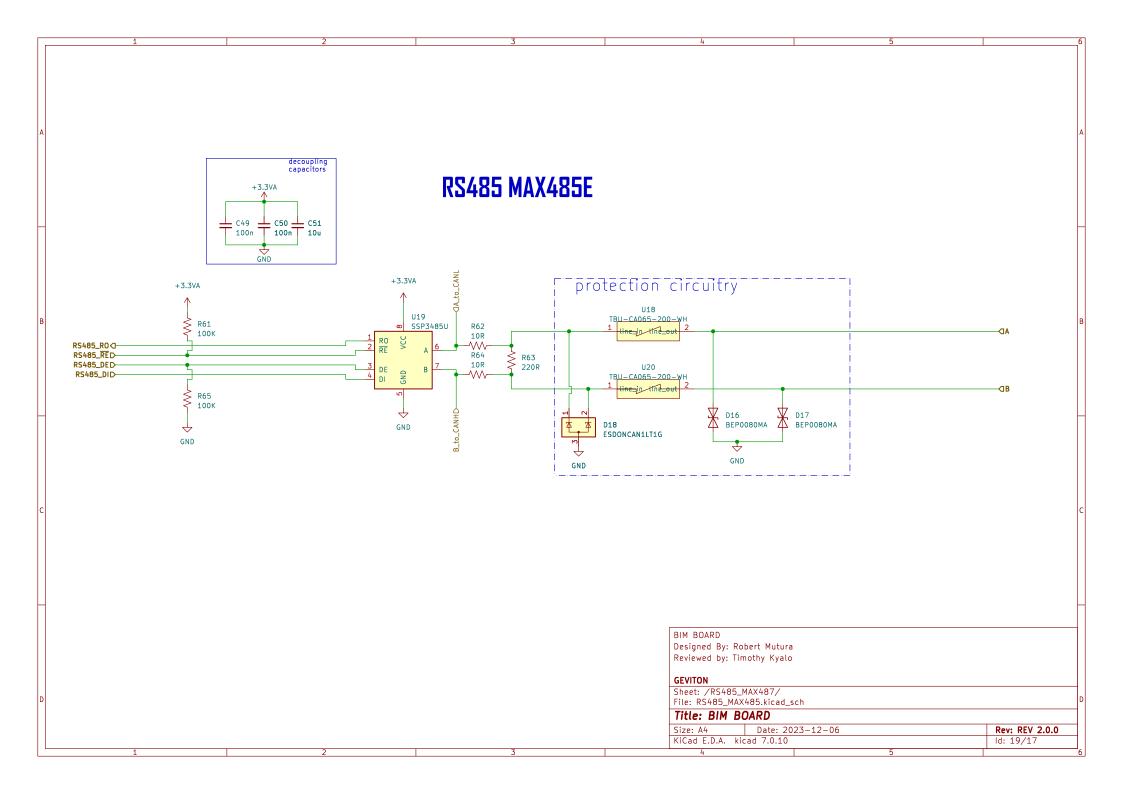
Size: A4 Date: 2023-12-06

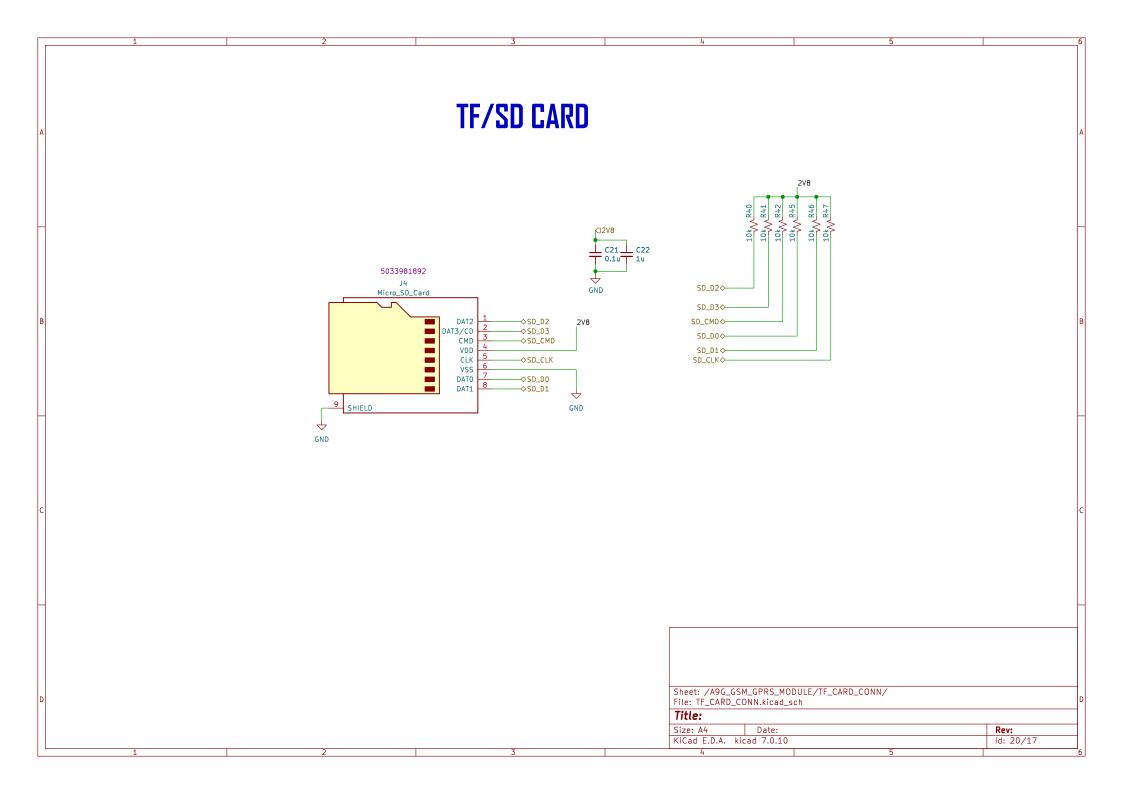
Rev: REV 2.0.0

ld: 17/17









BI-DIRECTIONAL LEVEL TRANSLATOR **MOSFET - IMPLEMENTATION** LOW LEVEL SIDE HIGH LEVEL SIDE LOW_SIDE_POWER_LEVEL >--→ HIGH_SIDE_POWER_LEVEL ₹ R48 ₹ R49 R50 → HIGH_SIDE_LOGIC_SIGNAL 2N7002 BIM BOARD Designed By: Robert Mutura Reviewed by: Timothy Kyalo Sheet: /A9G_GSM_GPRS_MODULE/MOSFET_LOGIC_LEVEL_TRANSLATOR7/ File: MOSFET_LOGIC_LEVEL_TRANSLATOR.kicad_sch Title: BIM BOARD Size: A4 Date: 2023-12-06 Rev: REV 2.0.0 KiCad E.D.A. kicad 7.0.10 ld: 21/17

BI-DIRECTIONAL LEVEL TRANSLATOR **MOSFET - IMPLEMENTATION** LOW LEVEL SIDE HIGH LEVEL SIDE → HIGH_SIDE_POWER_LEVEL LOW_SIDE_POWER_LEVEL >--₹ R51 10K ₹ R52 R53 → HIGH_SIDE_LOGIC_SIGNAL 2N7002 BIM BOARD Designed By: Robert Mutura Reviewed by: Timothy Kyalo Sheet: /A9G_GSM_GPRS_MODULE/MOSFET_LOGIC_LEVEL_TRANSLATOR8/ File: MOSFET_LOGIC_LEVEL_TRANSLATOR.kicad_sch Title: BIM BOARD Size: A4 Date: 2023-12-06 Rev: REV 2.0.0 KiCad E.D.A. kicad 7.0.10 ld: 22/17

BI-DIRECTIONAL LEVEL TRANSLATOR **MOSFET - IMPLEMENTATION** LOW LEVEL SIDE HIGH LEVEL SIDE LOW_SIDE_POWER_LEVEL >--→ HIGH_SIDE_POWER_LEVEL ₹ R54 ₹ R55 R56 → HIGH_SIDE_LOGIC_SIGNAL 2N7002 BIM BOARD Designed By: Robert Mutura Reviewed by: Timothy Kyalo Sheet: /A9G_GSM_GPRS_MODULE/MOSFET_LOGIC_LEVEL_TRANSLATOR9/ File: MOSFET_LOGIC_LEVEL_TRANSLATOR.kicad_sch Title: BIM BOARD Size: A4 Date: 2023-12-06 Rev: REV 2.0.0 KiCad E.D.A. kicad 7.0.10 ld: 23/17