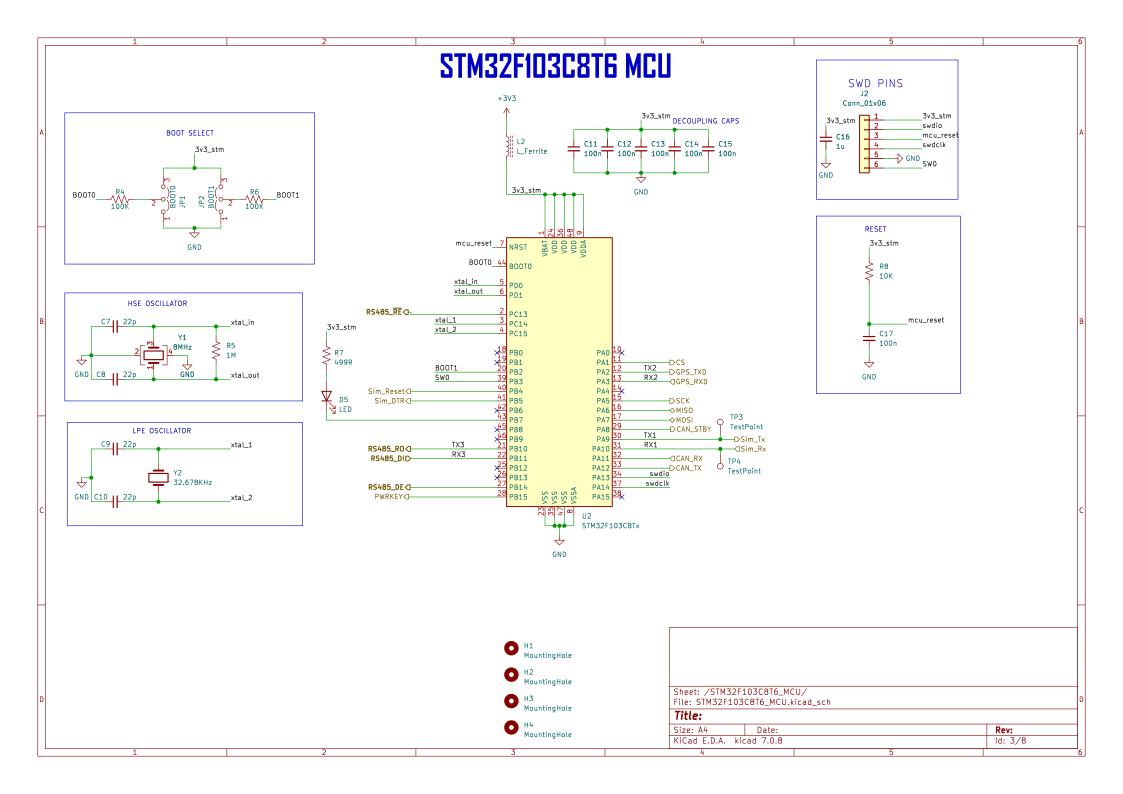
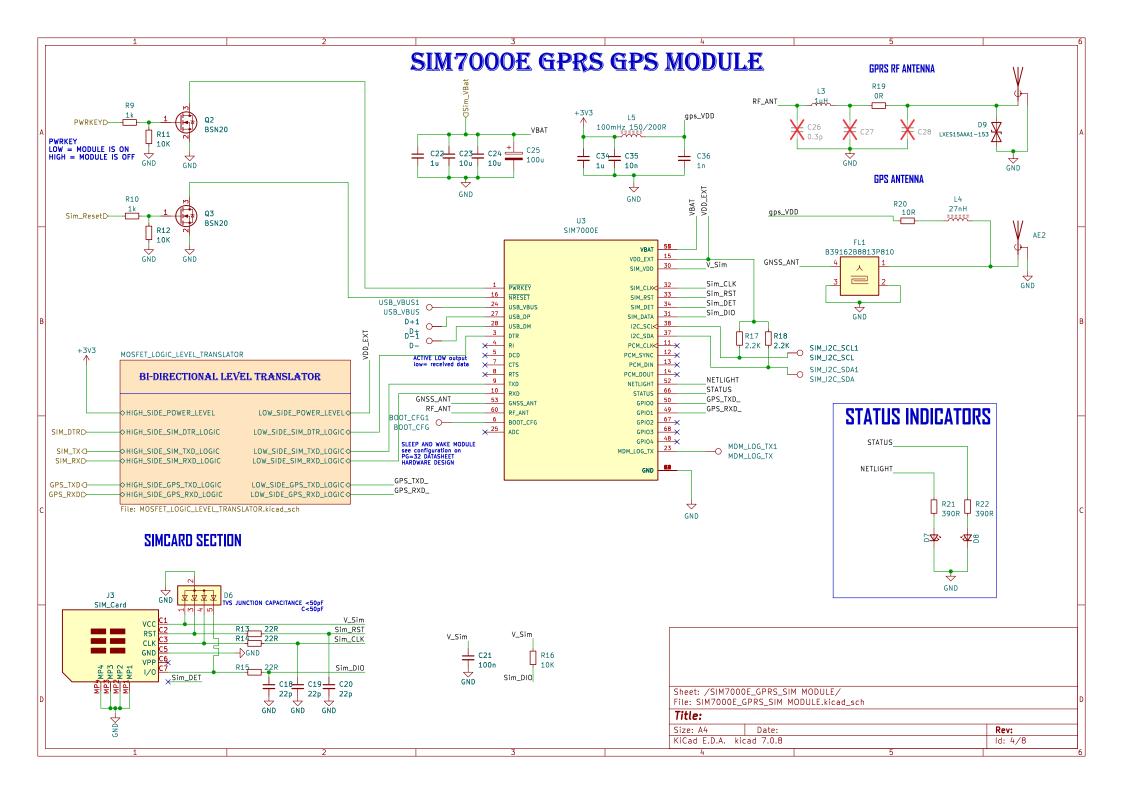
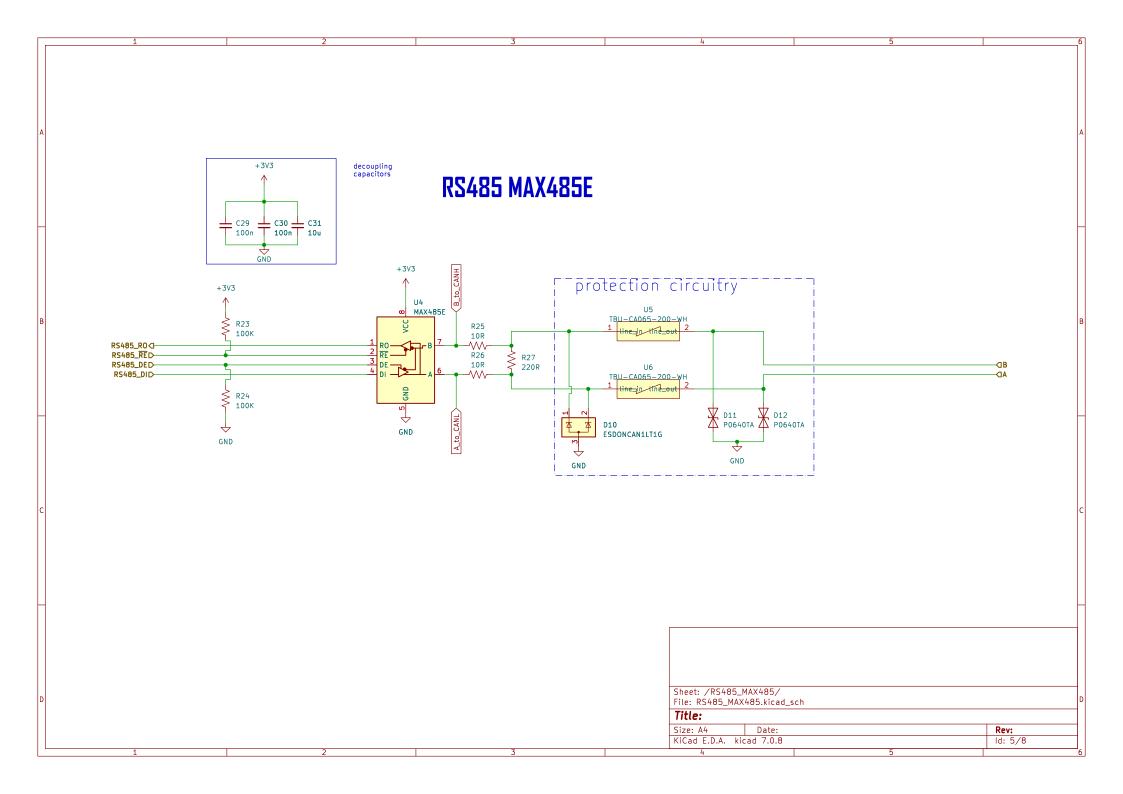


1A BUCK-BOOST CONVERTER L1 1.5uH MLP2520V1R5ST0S1 U1 ADP2<u>504ACPZ</u>-R7_3.3V_VERSION O TP2 TestPoint +3/3 4 SW1 PWR_INPUTD-PVIN VOUT FB C1 C2 100n + C3 6 7 SYNC 9 QN 9 QN 9 P 1K C4 C5 100n C6 10u GND GND GND GND GND GND Sheet: /BUCK_BOOST_Regulator_ADP2504ACPZ-R7/ File: BUCK_BOOST_Regulator_ADP2504ACPZ-R7.kicad_sch Title: Size: A4 Date: KiCad E.D.A. kicad 7.0.8 ld: 2/8







BI-DIRECTIONAL LEVEL TRANSLATOR **MOSFET - IMPLEMENTATION** LOW_SIDE_POWER_LEVEL >-→ HIGH_SIDE_POWER_LEVEL LOW_SIDE_POWER_LEVEL >-→HIGH_SIDE_POWER_LEVEL ₹ R34 ₹ R38 R30 ₹ R42 ₹ R44 LOW_SIDE_SIM_DTR_LOGIC →HIGH_SIDE_SIM_DTR_LOGIC R40 Q6 LOW_SIDE_GPS_RXD_LOGIC & →HIGH_SIDE_GPS_RXD_LOGIC 2N7002 Q8 2N7002 → HIGH_SIDE_POWER_LEVEL LOW_SIDE_POWER_LEVEL >--₹ R35 ₹ R39 LOW_SIDE_POWER_LEVEL >--→ HIGH_SIDE_POWER_LEVEL R31 LOW_SIDE_SIM_TXD_LOGIC → HIGH_SIDE_SIM_TXD_LOGIC ₹ R43 ₹ R45 **Q**7 R41 2N7002 LOW_SIDE_GPS_TXD_LOGIC <> →HIGH_SIDE_GPS_TXD_LOGIC Q9 2N7002 LOW_SIDE_POWER_LEVEL >--→HIGH_SIDE_POWER_LEVEL ₹ R33 ₹ R37 10K 10K R29 LOW_SIDE_SIM_RXD_LOGIC <>-→ HIGH_SIDE_SIM_RXD_LOGIC Q5 2N7002 Sheet: /SIM7000E_GPRS_SIM MODULE/MOSFET_LOGIC_LEVEL_TRANSLATOR/ File: MOSFET LOGIC LEVEL TRANSLATOR.kicad sch

Title: Size: A4

Date:

KiCad E.D.A. kicad 7.0.8

Rev:

ld: 6/8

