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# **Gates memory**

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#### **D-Latch**

Inputs: N Data Clk
Outputs: N Out

**Description:** The D-Latch will keep updating as long as its Clk input is positive. It outputs whatever value is currently stored.

### **Latch (Edge triggered)**

Inputs: N Data Clk
Outputs: N Out

The Latch is Edge-Triggered, meaning that it only stores the value when the Clk input changes from 0 to positive. In order to change

**Description:** the data stored, the Clk input must go back to zero or negative and then back to positive. The Latch will output whatever value is currently stored.

## **RAM**

Inputs: Nata AddrRead AddrWrite Clk Reset

Outputs: N Out

**Description:** These memory chips works like a row of data, where you specify the address you want to write to (and the address you want to read

from).

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Remember: The row starts with zero: 0, 1, 2, 3, 4 and so on. This concept is very similar to arrays in programming.

### **RS-Latch**

N S R **Inputs:** N Out **Outputs:** 

**Description:** SR is short for "Set-Reset". The chip keeps the first value input to S. When R is positive, the value of S is reset.

## **SR-Latch**

N S R **Inputs:** N Out **Outputs:** 

Same as RS-Latch, The difference is that each input has to 0 in order for the other to work.

### **Toggle (Edge triggered)**

**Inputs:** N Clk OnValue OffValue

N Out **Outputs:** 

This chip toggles between two values when its Clk input is positive. Its OffValue and OnValue inputs specify these two values. Like the Description:

Latch, the Toggle chip is also Edge-Triggered. When created, the Toggle chip begins Off.

### **Toggle While(Edge triggered)**

N Clk OnValue OffValue **Inputs:** 

N Out **Outputs:** 

This chip toggles between two values when its Clk input is positive. Its OffValue and OnValue inputs specify these two values. Like the **Description:** 

Latch, the Toggle chip is also Edge-Triggered. When created, the Toggle chip begins Off. As addition While has to be 1 in order to

toggle to the OnValue.

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## **Up/Down Counter**

N Clk Increment Decrement Reset **Inputs:** 

N Out **Outputs:** 

When Clk is triggered, outputs 1 when Increment is 1, outputs 0 when Decrement is 1. Doesn't do anything when Increment and Description:

Decrement are equal.

## Write Only Memory(4 store)

N Data AddrWrite Clk **Inputs:** 

N Out **Outputs:** 

**Description:** Use this gate to store secret data which you don't want anyone to read, including yourself.

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