

+ MOSFET & Driver

+ Bootstrap Capacitor

$$\frac{R_{u1}}{R_{u1} + R_{u2}}$$

$$\text{MOSFET Total Gate Charge: } Q_G = 44,5 \text{ nC (typ)} \quad \left[\begin{array}{l} V_{DS} = 40V \\ V_{GS} = 10V \\ I_D = 10A \end{array} \right]_{(max)} \\ = 67 \text{ nC}$$

$$Q_G = 33,5 \text{ nC (typ)} \quad \left[\begin{array}{l} V_{DS} = 40V \\ V_{GS} = 7,5V \\ I_D = 10A \end{array} \right]_{(max)}$$

$$C(F) = Q(C) \div U(V) \\ = 47 \text{ nC} \div 12V \\ = 3,9166 \text{ nF}$$

$$C_{boot} = C \times 10 \quad (\text{rule of thumb}) \\ = 39,166 \text{ nF} \\ \approx 39 \text{ nF} \quad (\text{next best value in E12 table}) \\ \approx 47 \text{ nF} \quad (\text{one size larger for when MOSFET is const. turned on in Buck-Boost mode})$$

+ Gate Resistor

$$\text{IR2184 Output Rating:} \\ I_{O1} = 1,4 A \quad (typ) \\ = 1,9 A \quad (max) \\ I_{O2} = 1,8 A \quad (typ) \\ = 2,3 A \quad (max)$$

$$R = 5,6 \Omega$$

$$I = 12V / 5,6 \Omega \\ = 2,143 A \Rightarrow \text{in spec enough for me } \underline{\underline{1}} \underline{\underline{1}} \underline{\underline{1}}$$

+ LM5161

+ Switching Frequency

$$\text{Given: } V_{in} = ca. 5V - 100V \\ F_{sw} = ca. 200kHz - 1MHz \text{ is desirable} \\ R_{on} = 100k\Omega - 300k\Omega \text{ as per datasheet examples} \\ V_{out} = 4V$$

$$F_{sw} = \frac{V_{out}}{1008 \times 10^{-10} \times R_{on}} \quad [R_{on} = 100 k\Omega] \\ = 396,825 \text{ kHz}$$

+ Feedback

$$V_{out} = \frac{V_{ref} \times (R_{FB2} + R_{FB1})}{R_{FB1}} \quad \left[\begin{array}{l} V_{out} = 4V \\ V_{ref} = 2V \end{array} \right]$$

$$\frac{R_{FB2}}{R_{FB1}} = \frac{V_{out}}{V_{ref}} - 1 \\ = \frac{4}{2} \Rightarrow R_{FB1} = R_{FB2} \quad (\text{just a random standard value I chose}) \\ = 5k\Omega$$

Switching Frequency

$$F_{sw,max} (@V_{in,min}) = \frac{V_{out}}{V_{in,min} \times T_{off,min}(ns)} \quad \left[\begin{array}{l} T_{off,min} = 170-200ns \\ V_{in,min} = 5V \\ V_{out} = 4V \end{array} \right] \\ = \frac{4V}{5V \times 200ns} = 4 \text{ MHz} ??? \text{ WTF}$$

Datasheet might have wrong formula

$$F_{sw,max} = 1 \div (\text{Period}) \\ = 1 \div \left(\left(1 - \left(1 - \frac{V_{out}}{V_{in,min}} \right) \right) \times T_{off,min} \right) \quad \left. \vphantom{F_{sw,max}} \right\} \text{my own formula} \\ = 1 \text{ MHz}$$

$$F_{sw} = \frac{V_{out}}{1008 \times 10^{-10} \times R_{on}} \quad [R_{on} = 100 k\Omega] \\ = 396,825 \text{ kHz}$$

Inductor Selection

$$L_{min} = \frac{V_o \times (V_{in,max} - V_o)}{V_{in,max} \times F_{sw} \times I_{o,max} \times 0,4} \quad \left[\begin{array}{l} V_o = 4V \\ V_{in,max} = 100V \\ I_{o,max} = 1A \end{array} \right] \\ = 2,41920 \mu H$$

! Allow for inductance tolerances & !
• inductor saturation: choose a bit higher value •

$$I_{L,peak} = I_{o,max} + \frac{\Delta I_{L,max}}{2} \\ = 1,2222 A < 1,6 A \quad \checkmark$$

MOSFETs can only do 1,6A

$$\Delta I_L = \frac{(V_{in,max} - V_{out}) \times D}{F_s \times L} \quad \left[\begin{array}{l} D = \text{Duty cycle} \\ F_s = \text{Switching Frequency} \\ L = \text{inductance} \end{array} \right] \\ = 444,6448 \text{ mA} \quad \Delta I_{L,max} = \text{peak to peak inductor current ripple at } V_{in,max} \text{ and } V_{in,min}$$

! formula pulled from another TI datasheet

$$D = \frac{V_{out}}{V_{in,max} \times \eta} \quad \eta = \text{efficiency} \approx 90\% \\ = 4,49\%$$

Output Capacitor

$$C_{out} = \frac{\Delta I_{L,max}}{8 \times F_{sw} \times \Delta V_{o,ripple}} \\ = 34,0299 \text{ mF} \quad \text{WTF ???} \\ = 14,0001 \mu F \quad [V_{ripple} = 10mV]$$

!!! use X7R with high enough voltage rating

$$\Delta V_{o,ripple} = \left(I_{L,peak} \times R_L \times T_s \right) / 2 \quad \left[\begin{array}{l} I_L = \text{inductor current} \\ R_L = \text{inductor resistance} = 10m\Omega \text{ (max)} \\ T_s = \text{Period} = 1/F_{sw} \end{array} \right] \\ = 155,5402 \text{ mV} \quad \text{? AF ???}$$

! formula from Gemini AI

Series Ripple Resistor

Only necessary when FPM pin is pulled high and I'm not gonna do that cuz I'm lazy B)

VCC & Bootstrap Capacitor

$$C_{BST} = 10 \text{ nF} \\ C_{VCC} = 1 \mu F$$

!!! It's recommended to add a resistor (>3Ω) to C_{BST} to protect the VCC-BST diode during full load transient operation!!!

Input Capacitor

$$C_{in} = \frac{I_{o,max} \times D \times (1-D)}{\Delta V_{in,ripple} \times F_{sw}} = 2,1 \mu F \quad \left[\begin{array}{l} D = 0,5 \\ I_{o,max} = 300mA \end{array} \right]$$

⇒ just use 2x 2,2μF caps bro

!!! divide C_{in} into 2 caps & add a third 0,1μF to filter out any noise as close to PGND & VIN as possible !!!

Soft Start Capacitor

SS won't be needed
a minimum of C_{SS}=1nF is required to stabilize the trans-conductance error amplifier

EN/UVLO Resistor

$$V_{in(HYS)} = I_{UVLO(HYS)} \times R_{UV2} \\ R_{UV2} = V_{in(HYS)} \div I_{UVLO(HYS)} \\ = 0,5V \div 20\mu A \text{ (typ)} \\ = 25 k\Omega$$

$$V_{in,UVLO(trig)} = V_{UVLO(TN)} \times \left(1 + \frac{R_{UV2}}{R_{UV1}} \right) \\ = 5,0205 V$$

V_{in(HYS)} = 0,5V
V_{in,UVLO(trig)} = rising threshold ≈ 5V

V_{UVLO(rtn)} = UVLO Threshold ≈ 1,24V

simple voltage divider formula:

$$V_{out} = \frac{R_{u1}}{R_{u1} + R_{u2}} \times V_{in}$$

$$\frac{V_{out}}{V_{in}} = \frac{R_{u1}}{R_{u1} + R_{u2}} \\ \frac{1,24V}{5V} = \frac{x}{x + 25k\Omega} \\ \Rightarrow x = R_{UV1} = 8244,681 \Omega \approx 8,2 k\Omega$$

Example:

