

## MOSFET & Driver

### Bootstrap Capacitor

$$\text{MOSFET Total Gate Charge: } Q_G = \frac{44.5 \text{ nC}}{67 \text{ nC}} \quad (t_{\text{typ}}) \quad (\text{max}) \quad \begin{bmatrix} V_{DS} = 40V \\ V_{GS} = 10V \\ I_D = 10A \end{bmatrix}$$

$$Q_G = \frac{33.5 \text{ nC}}{51 \text{ nC}} \quad (t_{\text{typ}}) \quad (\text{max}) \quad \begin{bmatrix} V_{DS} = 40V \\ V_{GS} = 7.5V \\ I_D = 10A \end{bmatrix}$$

$$C(F) = Q(C) \div U(V) \\ = 47 \text{ nC} \div 12V \\ = 3.9166 \text{ nF}$$

$$C_{\text{Boot}} = C \times 10 \quad (\text{rule of thumb}) \\ = 39.166 \text{ nF} \\ \approx 39 \text{ nF} \quad (\text{next best value in E12 table}) \\ \approx 47 \text{ nF} \quad (\text{one size larger for when MOSFET is const. turned on in Buck-Boost mode})$$

### Gate Resistor

IR2184 Output Rating:

$$\begin{aligned} I_{O+} &= 1.5A \quad (\text{typ}) \\ &= 1.9A \quad (\text{max}) \\ I_{O-} &= 1.8A \quad (\text{typ}) \\ &= 2.3A \quad (\text{max}) \end{aligned}$$

$$R = 5.6 \Omega$$

$$I = 12V / 5.6 \Omega \\ = 2.1439 A \Rightarrow \text{in spec enough for me} \quad \square$$

## LM5161

### Switching Frequency

$$\text{Given: } V_{in} = \text{ca. } 5V - 100V \\ F_{sw} = \text{ca. } 200\text{kHz} - 1\text{MHz} \quad \text{is desirable} \\ R_{on} = 100k\Omega - 300k\Omega \quad \text{as per datasheet examples} \\ V_{out} = 4V$$

$$F_{sw} = \frac{V_{out}}{1.008 \times 10^{-10} \times R_{on}} \quad [R_{on} = 100 k\Omega] \\ = 396,825 \text{ kHz}$$

### Feedback

$$V_{out} = \frac{V_{ref} \times (R_{FB2} + R_{FB1})}{R_{FB1}} \quad [V_{out} = 4V] \quad [V_{ref} = 2V]$$

$$\frac{R_{FB2}}{R_{FB1}} = \frac{V_{out}}{V_{ref}} - 1 \\ = \frac{1}{2} \rightarrow R_{FB1} = R_{FB2} \quad (\text{just a random standard value I chose})$$

### Switching Frequency

$$F_{sw,\text{max}} @ V_{in,\text{min}} = \frac{V_{out}}{V_{in,\text{min}} \times T_{OFF,\text{min}}} \quad \begin{bmatrix} T_{OFF,\text{min}} = 170-200\text{ns} \\ V_{in,\text{min}} = 5V \\ V_{out} = 4V \end{bmatrix} \\ = \frac{4V}{5V \times 200\text{ns}} = 4 \text{ MHz} \quad \text{ZTF}$$

Datasheet might have wrong formula

$$F_{sw,\text{max}} = 1 \div (\text{Period}) \quad \left. \begin{array}{l} = 1 \div ((1 - \frac{V_{out}}{V_{in,\text{max}}}) \times T_{OFF,\text{min}}) \\ = 1 \text{ MHz} \end{array} \right\} \text{my own formula}$$

$$F_{sw} = \frac{V_{out}}{1.008 \times 10^{-10} \times R_{on}} \quad [R_{on} = 100 k\Omega] \\ = 396,825 \text{ kHz}$$

### Inductor Selection

$$L_{min} = \frac{V_0 \times (V_{in,\text{max}} - V_0)}{V_{in,\text{max}} \times f_{sw} \times I_{o,max} \times 0.4} \quad \begin{bmatrix} V_0 = 4V \\ V_{in,\text{max}} = 100V \\ I_{o,max} = 1A \end{bmatrix} \\ = 2,4920 \mu H$$

Allow for inductance tolerances &  
inductor saturation: choose a bit higher value!

$$I_{L,\text{peak}} = I_{o,\text{max}} + \frac{\Delta I_{L,\text{max}}}{2} \\ = 1,2222 A < 1,6A \quad \checkmark$$

MOSFETs can only do 1,6A

$$\Delta I_L = \frac{(V_{in,\text{max}} - V_{out}) \times D}{f_{sw} \times L} \\ = 44,4949 \text{ mA} \quad \begin{array}{l} D = \text{Duty cycle} \\ f_{sw} = \text{switching frequency} \\ L = \text{inductance} \\ \Delta I_{L,\text{max}} = \text{peak to peak inductor current ripple at } V_{in,\text{max}} \text{ and } V_{in,\text{min}} \end{array}$$

$$D = \frac{V_{out}}{V_{in,\text{max}} \times \eta} \quad \eta = \text{efficiency} \approx 90\% \\ = 0,499\%$$

### Output Capacitor

$$C_{out} = \frac{\Delta I_{L,\text{max}} \times \Delta V_{o,\text{ripple}}}{8 \times f_{sw} \times \Delta V_{o,\text{ripple}}} \\ = 34,0233 \text{ mF} \quad \text{ZTF?} \\ = 14,0001 \text{ uF} \quad [V_{in,\text{min}} = 10mV]$$

!!! use X7R with high enough voltage rating

$$\Delta V_{o,\text{ripple}} = (I_{L,\text{peak}} \times R_L \times T_s) / 2 \quad \begin{array}{l} I_L = \text{inductance} \\ R_L = \text{inductor resistance} = 10m\Omega \text{ (max)} \\ T_s = \text{Period} = 1/f_{sw} \end{array}$$

Formula from Gemini A1

### Series Ripple Resistor

Only necessary when F PWM pin is pulled high and I'm not gonna do that cuz I'm lazy B)

### VCL & Bootstrap Capacitor

$$C_{BST} = 10 \text{ nF} \\ C_{VCL} = 1 \mu F$$

!!! It's recommended to add a resistor (>3Ω) to C<sub>BST</sub> to protect the VCL-BST diode during full load transient operation!!!

### Input Capacitor

$$C_{IN} = \frac{I_{o,max} \times (1-D)}{\Delta V_{in,\text{ripple}} \times f_{sw}} = 2,1 \mu F \quad \begin{bmatrix} D = 0,5 \\ \Delta V_{in,\text{ripple}} = 300mV \end{bmatrix}$$

Just use 2x 2.2uF caps bro

!!! divide C<sub>IN</sub> into 2 caps & add a third 0.1μF to filter out any noise as close to PGND & VIN as possible !!!

### Soft Start Capacitor

SS won't be needed a minimum of C<sub>SS</sub>=1nF is required to stabilize the transconductance error amplifier

### EN/UVLO Resistor

$$V_{IN(HYS)} = \frac{V_{UVLO(HYS)} \times R_{UV2}}{R_{UV1}} \\ R_{UV2} = \frac{V_{IN(HYS)}}{I_{UVLO(HYS)}} \div 20 \mu A \quad (\text{typ}) \\ = 25 k\Omega$$

$$V_{in,UVLO(HYS)} = \frac{V_{UVLO(HYS)} \times (1 + \frac{R_{UV2}}{R_{UV1}})}{R_{UV1}} \\ = 5,0205 V$$

$$V_{in(HYS)} = 0,5V$$

$$V_{in,UVLO(HYS)} = \text{rising threshold} \approx 5V$$

$$V_{UVLO(HYS)} = \text{UVLO threshold} = 1,24V$$

simple voltage divider formula:

$$V_{out} = \frac{R_{UV1}}{R_{UV1} + R_{UV2}} \times V_{in}$$

$$\frac{V_{out}}{V_{in}} = \frac{R_{UV1}}{R_{UV1} + R_{UV2}}$$

$$\frac{1,24V}{5} = \frac{x}{x + 25k\Omega}$$

$$\Rightarrow x = R_{UV1} = 82,44,681 \Omega$$

$$\approx 8,2 k\Omega$$

Example:

