## **Electron Wranglers Timeline**

| Date     | Objective done by                    |
|----------|--------------------------------------|
| 10/1     | All intentional research             |
| 10/22    | Simulation prototype complete        |
| 10/29    | Initial large, working prototype     |
| 10/15 or | Simulation prototype complete        |
| 10/22    |                                      |
| 11/5     | First run testing complete. Begin    |
|          | "shrinking" of circuit               |
| 11/12    | PCB layout solidified                |
| 11/19    | Complete edits to design and         |
|          | complete initial tests               |
| 12/1     | compile a report with initial test   |
|          | results, and other consideration for |
|          | Textron                              |
| 4/15     | All multiple small prototypes        |
|          | completed and sent to Textron to     |
|          | compete testing. All package         |
|          | needed will be included              |
| 4/29     | Final prototype completed and sent   |
|          | to Textron. All package needed will  |

| be included. This should be the |
|---------------------------------|
| final product.                  |