```
#include "MKL25Z4.h"
 2
    #define SO_PIN 1 // PTA1
   #define S1 PIN 12 // PTA12
#define S2 PIN 4 // PTA4
#define S3 PIN 5 // PTA5
#define OUT PIN 4 // PTD4
3
 6
    void UARTO_init(void);
8 uint8_t s;
9 uint16 t count;
10 void PORTD_IRQHandler(void);
11 void SysTick_Handler(void);
12  uint16_t colour_sample(void);
13
    void system(void);
14 void colour sample setup(void);
15 void SetFilter(uint8_t t);
16 ⊟int main (void) {
17
      UARTO init();
      uintl6 t data[4];
18
19
      uint8 t i;
20
      system();
21
      colour_sample_setup();
22 for (i=0; i<4; i++) {
23
      SetFilter(i);
24
      data[i]=colour_sample();
25
26
    }
28 - void colour sample setup(void) {
      SIM->SCGC5 |= SIM SCGC5 PORTD MASK; // sets the clock
29
      SysTick->CTRL = SysTick CTRL CLKSOURCE Msk | SysTick CTRL TICKINT Msk;//SysTick timer to use the processor clock and enable SysTick->VAL = SystemCoreClock / 10000 - 1;//
30
31
      SysTick->LOAD = 0x80000;
32
      GPIOD->PDDR |= 0x00000; //making the port D pin as input
33
       PORTD->PCR[OUT_PIN] |= 0x000A0100; //change it to the right pin(setting the interrupt for rising edges)
34
35
      NVTC SetPriority/PORTD TROm 21.
```

```
SIM->SCGC5 |= SIM_SCGC5_PORTD_MASK; // sets the clock
29
      SysTick->CTRL = SysTick_CTRL_CLKSOURCE_Msk | SysTick_CTRL_TICKINT_Msk;//SysTick timer to use the processor clock and enabled
31
      SysTick->VAL = SystemCoreClock / 10000 - 1;//
      SysTick->LOAD = 0x80000;
32
      GPIOD->PDDR |= 0x000000; //making the port D pin as input
33
34
      PORTD->PCR[OUT PIN] |= 0x000A0100; //change it to the right pin(setting the interrupt for rising edges)
35
      NVIC_SetPriority(PORTD_IRQn,2);
36 }
37
38 [uint16_t colour_sample(void){
39
      count=0;
40
      s=1;
41
      SysTick->CTRL = SysTick CTRL CLKSOURCE Msk | SysTick CTRL TICKINT Msk | SysTick CTRL ENABLE Msk; // startng the clock for
     NVIC_EnableIRQ(PORTD_IRQn);
42
43
      while(s);
44
      return count;
45
   }
46
47 - void SysTick_Handler(void) {
48
      SysTick->CTRL = SysTick CTRL CLKSOURCE Msk | SysTick CTRL TICKINT Msk;
49
50
      SysTick->VAL = SystemCoreClock / 10000 - 1;
51
      NVIC_DisableIRQ(PORTD_IRQn);
     NVIC_ClearPendingIRQ(PORTD_IRQn);
53 }
```

28 [void colour_sample_setup(void){

```
58 - void system(void) {
59
      SIM->SCGC5 |= SIM SCGC5 PORTA MASK;
60
61
        PORTA->PCR[SO PIN] &= ~PORT PCR MUX MASK;
62
        PORTA->PCR[SO PIN] |= PORT PCR MUX(1);
        PORTA->PCR[S1 PIN] &= ~PORT PCR MUX MASK;
63
64
       PORTA->PCR[S1 PIN] |= PORT PCR MUX(1);
65
       PORTA->PCR[S2 PIN] &= ~PORT PCR MUX MASK;
66
67
       PORTA->PCR[S2 PIN] |= PORT PCR MUX(1);
       PORTA->PCR[S3 PIN] &= ~PORT PCR MUX MASK;
68
       PORTA->PCR[S3 PIN] |= PORT PCR MUX(1);
69
70
71
72
       GPIOA->PDDR |= (1UL << SO PIN) | (1UL << S1 PIN);
73
       GPIOA->PDDR |= (1UL << S2 PIN) | (1UL << S3 PIN);
74
75
       GPIOA->PSOR |= (1UL << SO PIN); // SO to high
       GPIOA->PSOR |= (1UL << S1 PIN); // S1 to high
76
77
78
   1
79
80 - void SetFilter (uint8 t t) {
81 = if(t==0) {
        GPIOA->PSOR |= (1UL << S2 PIN); // S0 to high
82
83
      GPIOA->PSOR |= (1UL << S3 PIN); // S1 to high
84 - }
85 = else if(t==1){
86
       GPIOA->PCOR |= (1UL << S2 PIN); // S2 to low
      GPIOA->PCOR |= (1UL << S3 PIN); // S3 to low
87
  - 1
88
89 else if (t==2) {
       GPIOA->PSOR |= (IUL << S2 PIN); // S0 to high
90
91
      GPIOA->PCOR |= (1UL << S3 PIN); // S1 to low
92 - }
```

```
19 -
 80 - void SetFilter(uint8 t t) {
 81 | if(t==0){
         GPIOA->PSOR |= (1UL << S2 PIN); // S0 to high
 82
 83
       GPIOA->PSOR |= (IUL << S3 PIN); // S1 to high
 84 - }
 85 = else if(t==1){
         GPIOA->PCOR |= (IUL << S2 PIN); // S2 to low
 86
 87
       GPIOA->PCOR |= (1UL << S3 PIN); // S3 to low
 88 - }
 89 E else if(t==2){
 90
         GPIOA->PSOR |= (IUL << S2 PIN); // S0 to high
 91
       GPIOA->PCOR |= (1UL << S3 PIN); // S1 to low
 92 - }
93
      else if (t==3) {
94
        GPIOA->PCOR |= (1UL << S2 PIN); // S0 to low
 95
       GPIOA->PSOR |= (1UL << S3 PIN); // S1 to high
96 - }
97
    1
98 -
99 - void UARTO init(void) {
100 | SIM->SCGC4 |= 0x0400; /* enable clock for UARTO */
101 | SIM->SOPT2 |= 0x04000000;
102 | UARTO->C2 = 0;
103 UARTO->BDH = 0x00;
104 | UARTO->BDL = 0x0D;
105
    UARTO->C4 = OxOF;
106
    UARTO->C1 = 0x00;
107
    UART0->C2 = 0x08;
108
    SIM->SCGC5 |= 0x0200;
109
    PORTA \rightarrow PCR[2] = 0x0200;
110
    111
112 -
```