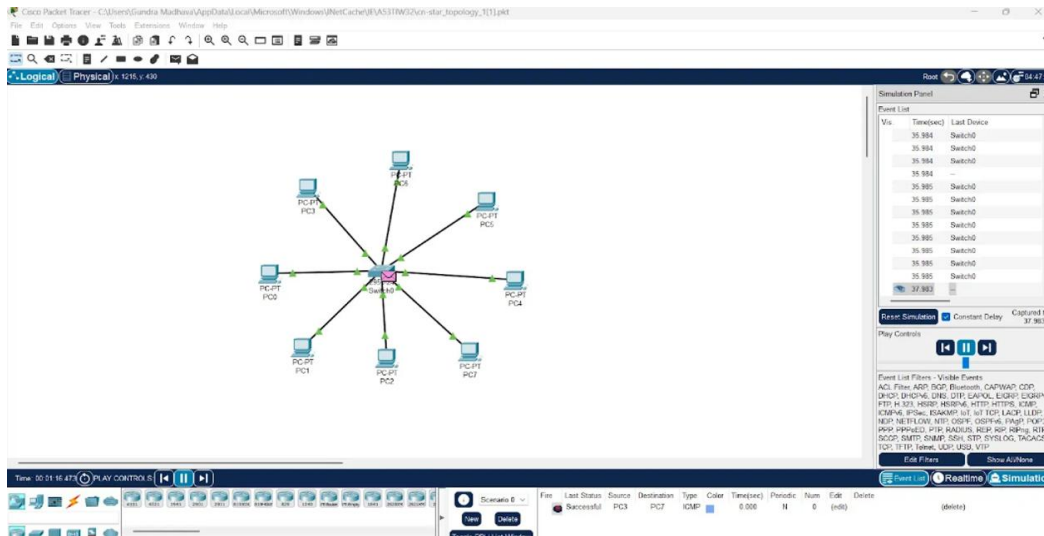
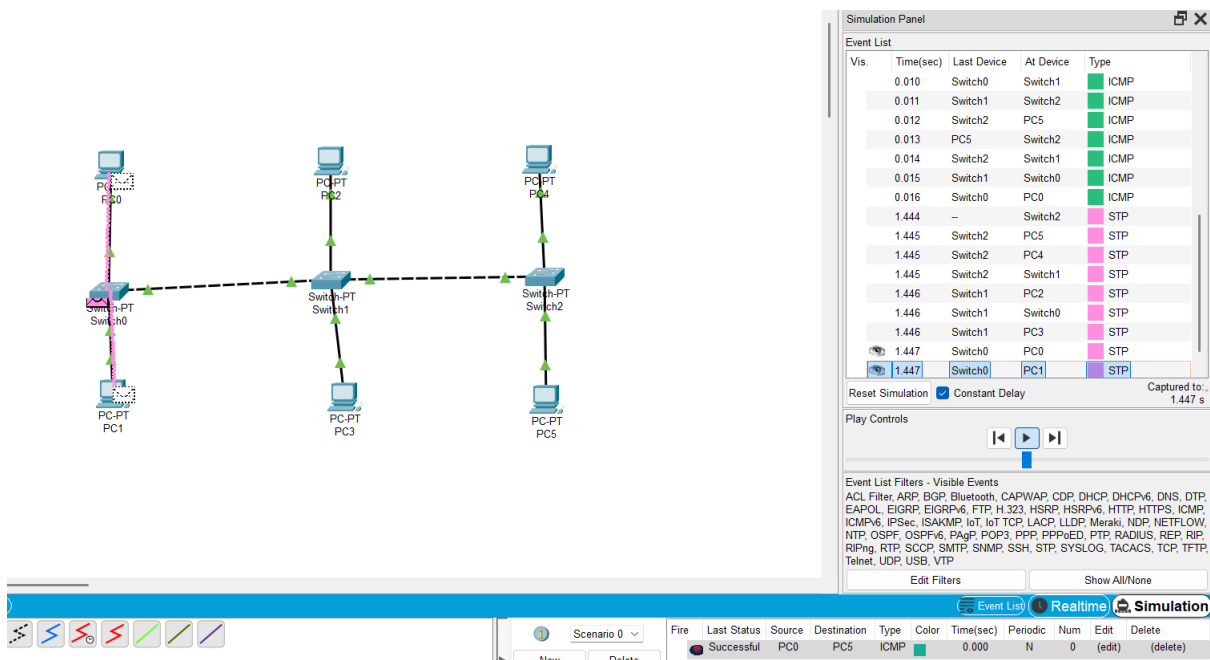


LAB EXPERIMENTS

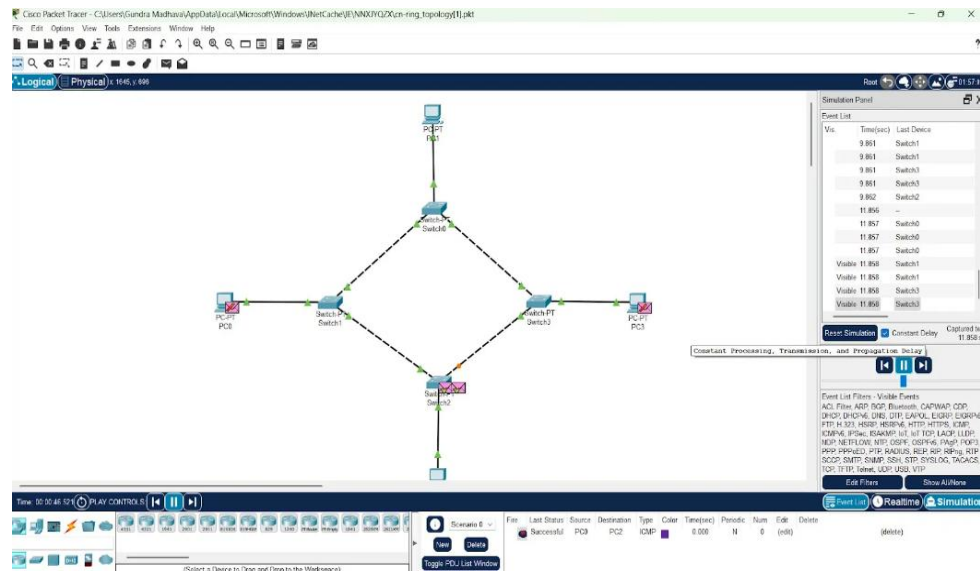
2. Star Topology



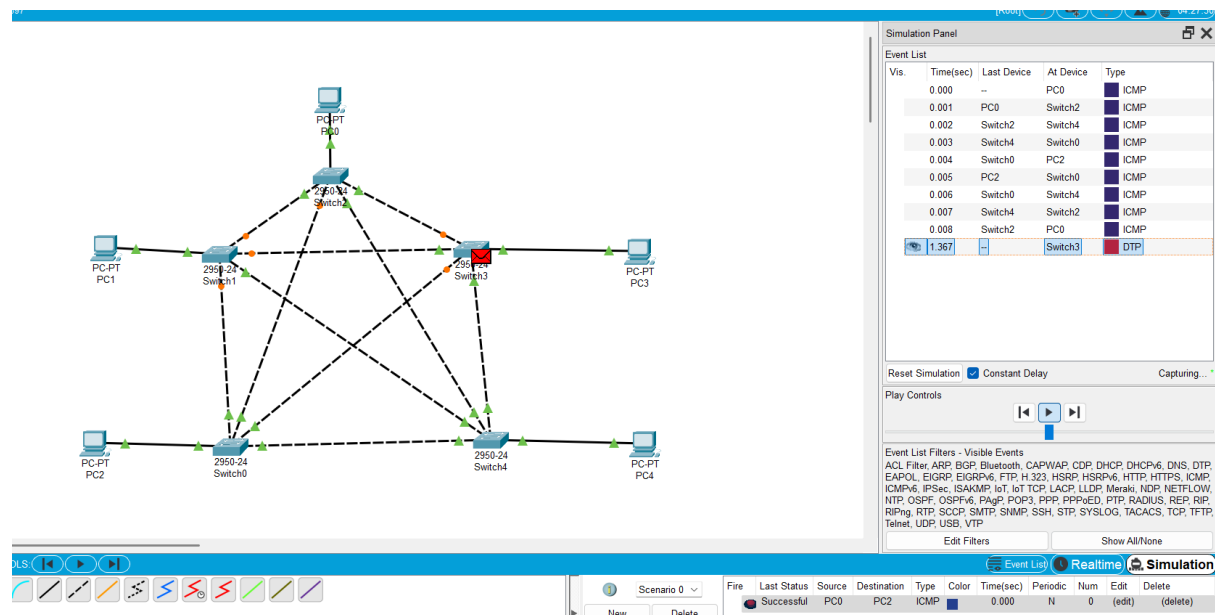
Bus Topology



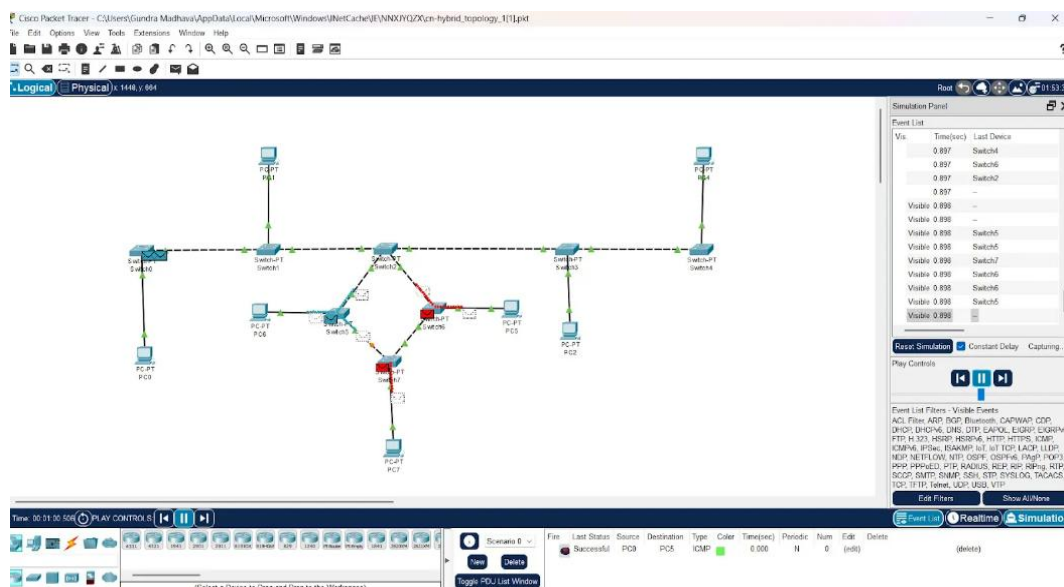
Ring Topology



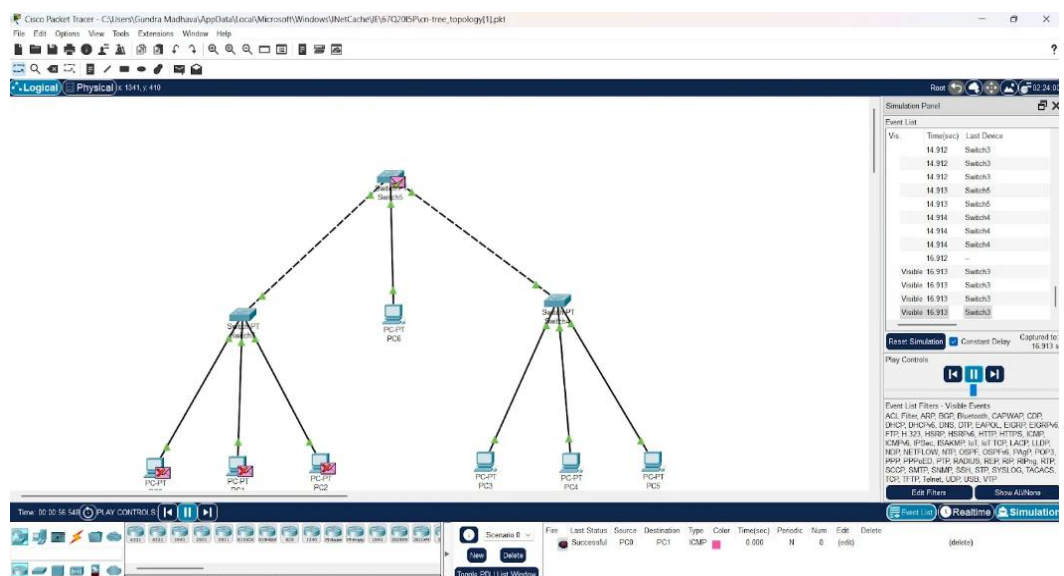
Mesh Topology



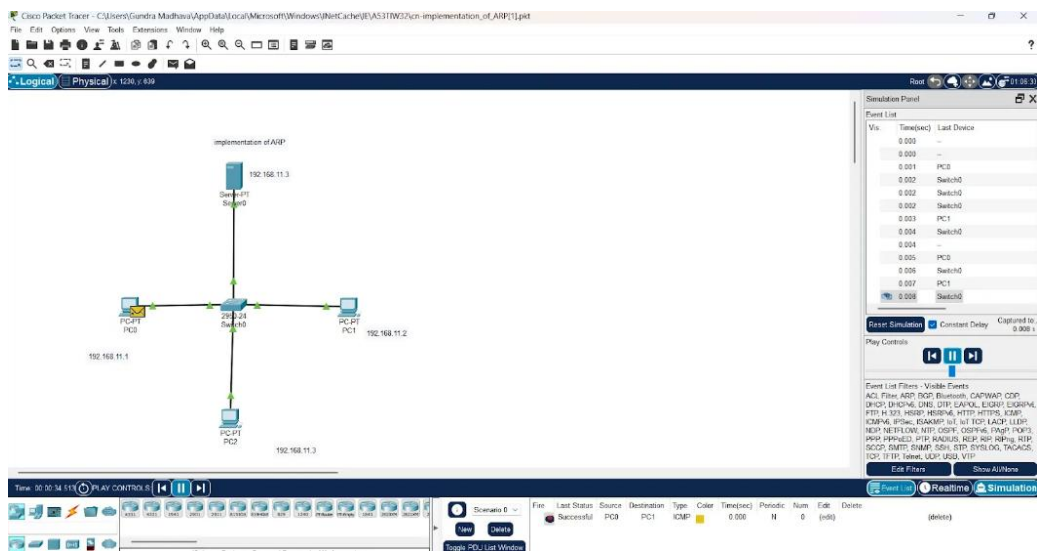
Hybrid Topology



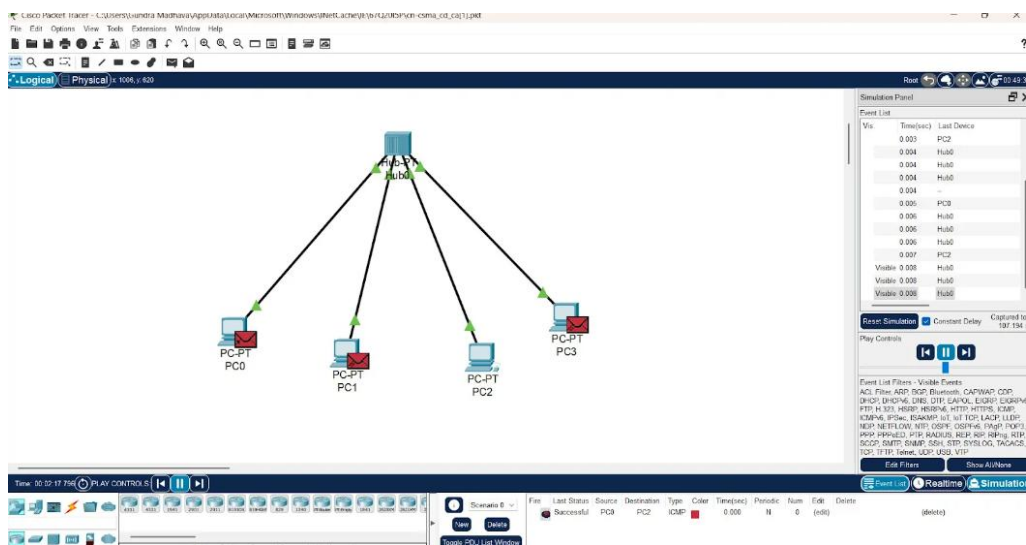
Tree Topology



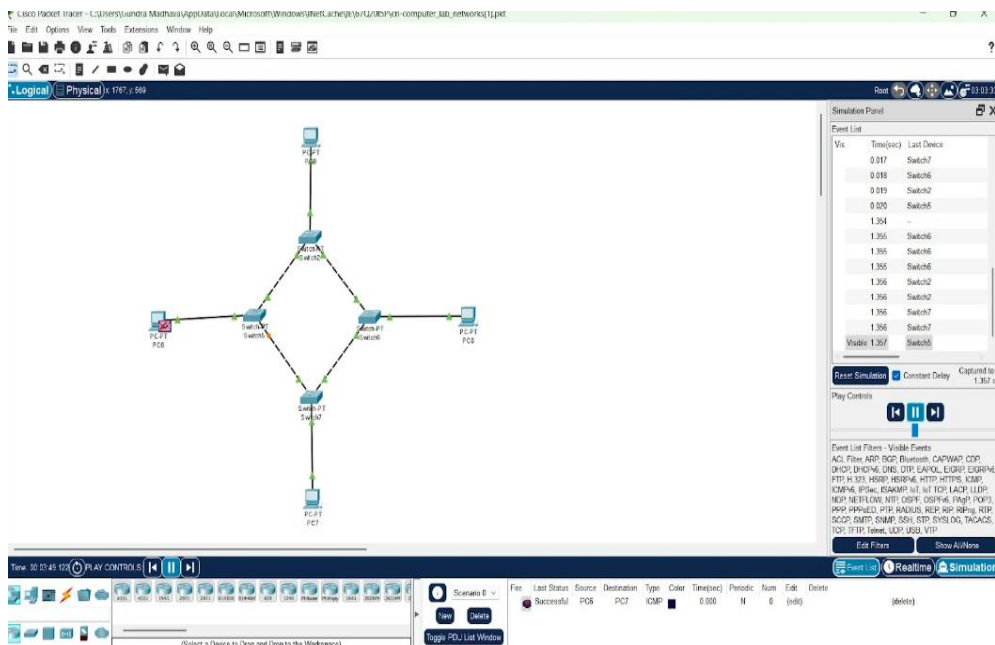
Ex.4. ARP



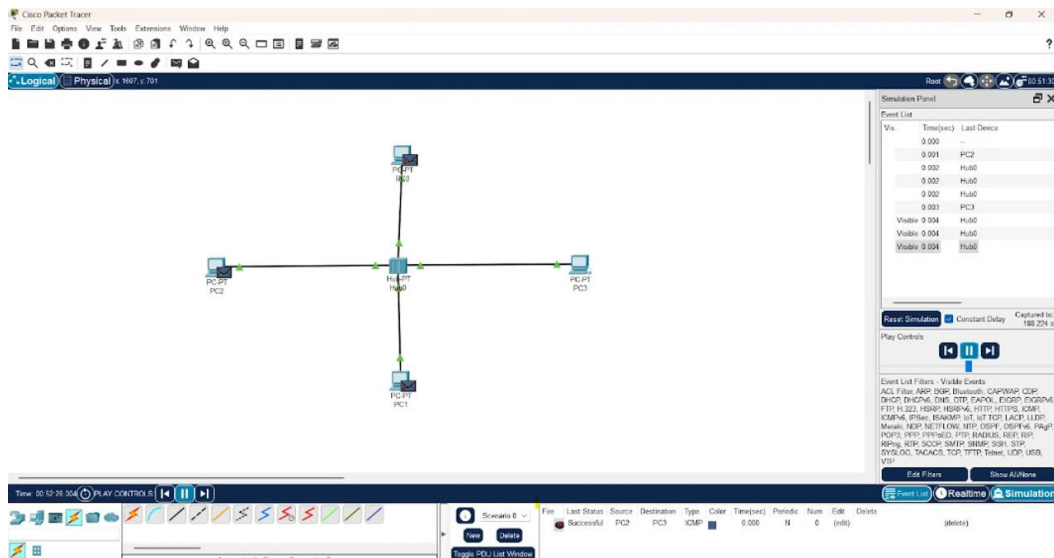
Ex. No:3 csma_cd_csma_ca



Ex. No:5 LLDP



1. Hub Processing



Switch

