NMAM INSTITUTE OF TECHNOLOGY, NITTE (An Autonomous Institution affiliated to VTU, Belagavi) II Sem B.E. (Credit System) Mid Semester Examinations - I, February 2016 15EC112 - BASIC ELECTRONICS Max. Marks: 20 Note: Answer any One full question from each Unit. Marks BT a) Show the circuits required for obtaining forward bias and reverse bias V-I Unit - I characteristics for germanium diode. Sketch characteristics, mark cut-in voltage, rd (dynamic forward resistance), reverse breakdown voltage and explain. b) Sketch one approximate characteristic and draw the corresponding electrical equivalent circuits for Silicon diode. This diode is in series with a resistance of 200 ohms. If the input DC voltage is 10 volts, what is the value of load current L3 Sketch forward V-I characteristic for PIECEWISE LINEAR approximation for a silicon diode where cut- in voltage and a dynamic resistance are considered for representing diode and explain. Show the electrical equivalent circuit for this L3 5 b) Discuss LOAD LINE analysis for diode circuit in forward bias. Diode is connected to a 10 volts d.c supply with a series resistance of 250 ohms. Show L4 to scale the load line for this resistance. With circuit diagram and relevant waveforms discuss the operation of a half wave diode rectifier. Derive expressions for output average voltage and % voltage regulation considering diode to have forward conducting resistance Rr. 6 b) Input tranformer in a 2 diode FWR has secondary rating of 15 -- 0 -- 15 volts. If load resistance is 100 ohms, determine (i) average load current, (ii) a.c. power into rectifier. Consider diodes as ideal. Sketch V-I characteristics of ZENER diode. Mark location of  $V_z$  and  $I_{zk}$ . Define b) A voltage regulator using a Zener is to produce a constant output voltage of 5 across a load resistance of 50 ohms, from an input d. c. supply of 12 volts. The Zener requires a minimum current of 10 mA to sustain reverse breakdown. Calculate value of series resistance required. Draw the circuit and show the values. In the circuit designed, if the load is disconnected what will be the power

dissipation in the Zener? oom's Taxonomy, L\* Level

for this approximation?

ation: 1 Hour