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CSPC-31: Comp Arch.

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CT-01

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### Question (1)

1) a)

we generally measure program execution time from program initiation at presentation of some input to termination at the delivery of the last output.

- worst-case execution time (WCET)
- Best case execution time (BCET)
- Average-case execution time

(b) The maximum number of pipeline stages is limited by pipeline hazards., sequencing overhead, and cost.

longer pipelines introduce more dependencies

some of the dependencies can be solved by forwarding but others require stalls which increase CPI.

Also, it is depends upon subdivision stages.

### Question (3):

a) using Amdahl's law the following equation can be used:

$$\text{Speedup}_{\text{overall}} = \frac{1}{(1 - \text{fraction}_{\text{enhanced}}) + \left(\frac{f_{\text{enhanced}}}{\text{Speedup}_{\text{enhanced}}}\right)} = 2$$

so,

calculating  $\text{fraction}_{\text{enhanced}}$

$$f_{\text{enhanced}} = \text{Speedup}_{\text{overall}} \times S_{\text{enhanced}} - S_{\text{enhanced}} / S_{\text{overall}}$$

$$= (2 \times 15 - 15) / (2 \times 15 - 2)$$

$$= \frac{15}{28} = 0.5357.$$

Therefore, the percentage vectorization needed to gain a speedup of 2 is 53.57%.

(b) % of time in new execution spent of encryption operation.

$$= \frac{\left(\frac{0.535}{15}\right)}{(1 - 0.535) + \left(\frac{0.5357}{15}\right)} = \frac{0.0357}{(0.4643) + (0.0357)}$$

$$= 0.071424$$

$$= 7.14\%$$

### Question (4)

(a)

Speedup for  $N$  processors. if 65% is parallelized.

so,

$$\text{speed up} = \frac{1}{0.35 + \frac{0.65}{N}}$$

(b) speedup with processors.  
with original execution time is.

$$\frac{1}{0.35 + 6 \times 0.004 + \frac{0.35}{6}} = 2.07325$$

## Question (6)

5) (a)

computing the speed up obtained from the fast mode we must work out the execution time without enhancement. we know that the accelerated execution time consisted of two halves:

unaccelerated phase & accelerated phase. (40%)  
(60%)

without enhancement, the unaccelerated phase would have taken 60%, but accelerated take 15 times more.  $600\% : (40 \times 15)\%$ .

So, relative time of execution without enhancement

$$\boxed{(60 + 600)\% = (660)\%}$$

Thus, overall speed up =  $\frac{\text{execution time}_{\text{unacc}}}{\text{execution time}_{\text{acc}}} = \frac{660\%}{100\%}$

$$\boxed{\text{Speedup}_{\text{overall}} = 6.6}$$

5)

b)

To find the percentage of original execution time which was accelerated,

we plug the

Amdahl's Law.

$$\text{fraction}_{\text{vectorized}} = \frac{\text{speedup}_{\text{overall}} \times \text{speedup}_{\text{acc}} - \text{speedup}_{\text{acc}}}{\text{speedup}_{\text{overall}} \times \text{speedup}_{\text{acc}} - \text{speedup}_{\text{overall}}}$$

$$= \frac{6.6 \times 15 - 15}{6.6 \times 15 - 6.6}$$

$$\text{fraction}_{\text{vectorized}} = \frac{84}{92.42} = 0.9088$$

$$= 90.88\%$$



## Question (2)

2) a)

Die yield of Phoenix chip:

faumulat:

$$\text{Die yield} = \text{wafer yield} \times \frac{1}{\left(1 + \frac{\text{Defects per unit}}{\text{die}} \times \text{Die}\right)^N}$$

Assume

wafer yield is 100% or 1.

Die area = 200 mm<sup>2</sup> or 2 cm<sup>2</sup>

Defects per unit area = 0.04

N = 14

$$\text{Die yield} = 1 \times \frac{1}{(1 + (2 \times 0.04))^{14}}$$

$$= \frac{1}{2.94} = 0.34$$

Thus, the die yield of phoenix chip is.

$$\boxed{0.34}$$

2)

b)

Pheonix is made-up in an old plant and in a lauger technology.

As the plants get old, Pheonix process get tuned and defect-rate gets decrease.