

Title: VTOL Project

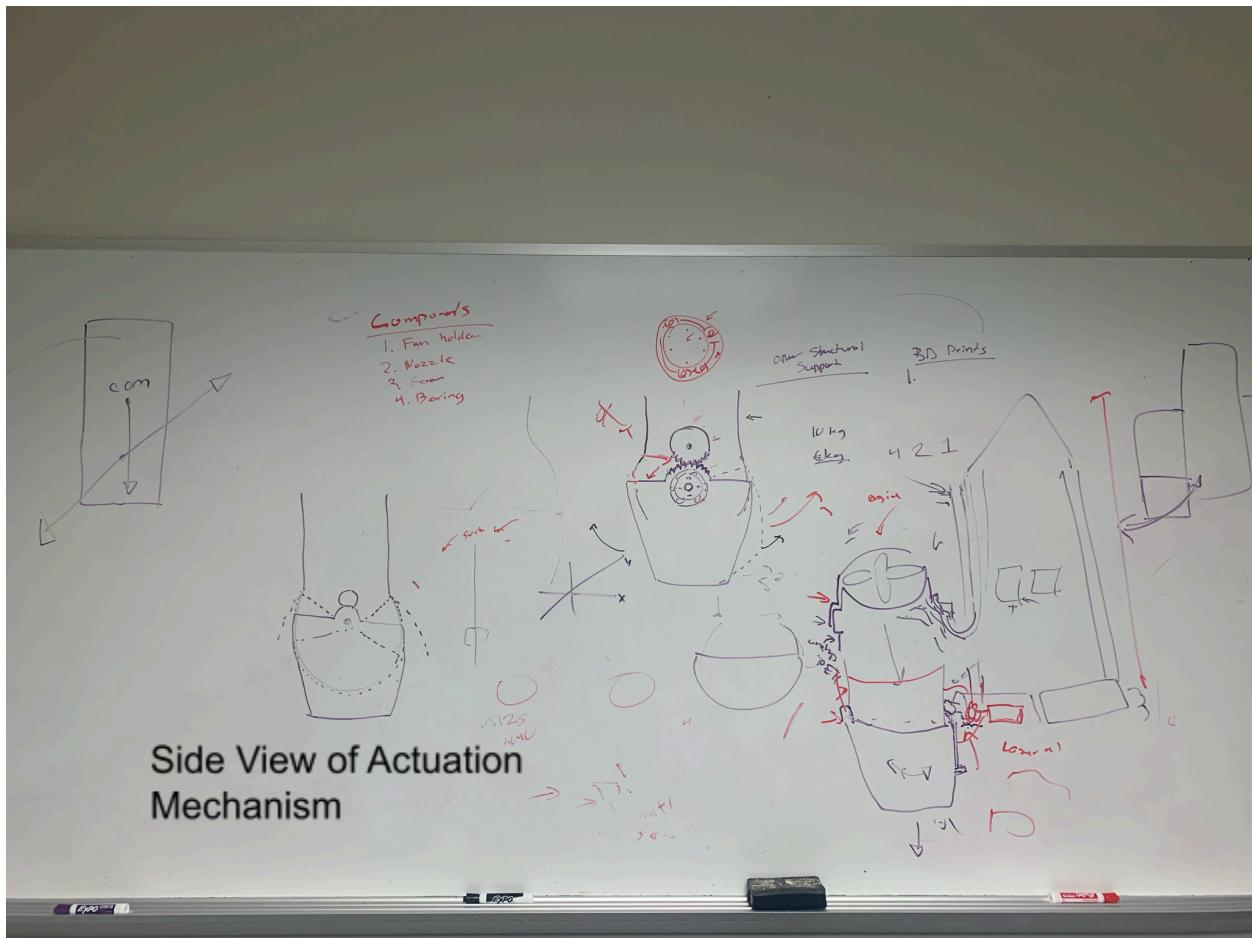
Date/Time: February 10, 2026 (7:30 p.m - 9 p..m)

Attendees: Everyone except Vivienne & Arsh

Phase: Phase 1 - Mechanical & Electrical Design of VTOL

Important:

Went over the design schematic of the mechanical design for the engine mounts, made a written-up layout of the circuit diagram, and continued Flight Sim



A) Current Build Status

Mechanical

In progress: - CAD Design based on initial schematics

Done - Order of initial key parts

Blocked: - None

Electronics

In progress: - Block Diagram of Electrical Schematic Diagram of wiring

Done - Order Parts

Blocked:

Software/Controls

In progress: - ArduPilot SITL Simulation

Also: Learning to read through the ArduPilot documentation to have the software prepared when the mechanical is ready

B) Decision Log

Decision #	Topic	Options Considered	Decision	Owner	Deadline
Decision - 1	Design	Actuation Design on Engine Mount with gear or with a lever arm.	Gear-based actuation with servo driving the gear.	Team	N/A
Decision - 2	Design	Components of the Engine Mount	Design Picture's attached at top for overview	Team	N/A

C) Build / Assembly / Design Work

Build Plan for Today :

- Assemble: N/A
- Fit-check: N/A
- Test: N/A
- Update CAD live: In Progress
- Update wiring diagram live: In Progress

Artifacts created/updated:

- CAD: In Progress
- Diagram: In Progress
- Test doc: N/A
- Photos/video: N/A

D) Tasks for Next Session

Task	Owner	Deliverable (link/file)	Acceptance Criteria ("done when...")	Due
Take Arsh's decision and turn it into a block diagram	Jeff			(ASAP) - This is a 10 min task
SITL Simulation work, also understanding Ardupilot's implementation, so we are ready with that once mechE is done	Ayaam & Vivienne			Continuous Process
CAD out Components in OnShape	Everyone else			Continuous Process

F) Next Meeting

Next meeting objective:

Required prep (must be done before next meeting):

- - Abstract Electrical Schematic is Done
- - Initial Engine Holder Component is Done, and Gear actuation is started and modeled out
- - Progress made on SITL Simulation