1X - Text 2X - Outline 3X - Clocks? 4X - Config 5X - USB Uart (See 17) 6 - I2C Mux + EEPROM 7X - BankO + Prog + Init 8X - Bank34 + Bank35 (DDR) Sheet: Power Sheet: Config_Debug Sheet: Decoupling Sheet: PCIe_interface 9X - Bank33 (DDR) 10X - DDR 11X - DDR Term. Resistors 12X - DDR Term. Resistors 13X - DIMM Decoupling 14 - Bank14 GPIO + SD + LCD 15X - 10/100/1000 PHY 16X - SI5324 Clock Recovery? 17 - Bank13 (HDMI) 18 - HDMI File: Decoupling.sch File: PCIe_interface.sch File: Power.sch File: Config_Debug.sch 19 - HDMI Con 20X - SFP+ Con Sheet: Connections Sheet: DDR2_1 Sheet: DDR2_2 Sheet: HDMI_VGA 21 - GPIO + LEDs 22X - Bank15 + Bank16 (FMC+XADC) 23X - Bank12 (HPC FMC) 24X - FMC 25X - FMC 26X - FMC 27X - FMC 28X - PCle Card Edge 29X - XADC 30X - Bank213 + Bank216 + Clock Mux 31X - MGTA Power + Cap File: HDMI_VGA.sch File: DDR2_1.sch File: DDR2_2.sch File: Connections.sch 32X - VCCs + Cap 33X - GND + Cap 34X - XADC OpAmps 35X - XADC OpAmps 36X - XADC OpAmps 37X - XADC Mux 38X - Power Con + PMBUS Hdr 39X - PMIC1 40X - VCCINT 41X - VCCAUX 42X - VCCBRAM 43X - FPGA_1.5V 44X - DIMM Power 45X - PMIC2 The Cooper Union for the Advancement of Science and Art 46X - VCCO Senior Projects 47X - VCC_1.8V Advisor: Professor Sable 48X - VCC 3.3V 49X - MGTAVCC 50X - MGTAVTT Brian Hong & Jasmine Tang 51X - Hardware Sheet: / File: RASAC.sch Title: Reconfigurable Application—Specific Accelerator Card Size: A Date: 2019-02-27 Rev: 1.0 KiCad E.D.A. kicad (5.0.2)-1 ld: 1/9















