

A

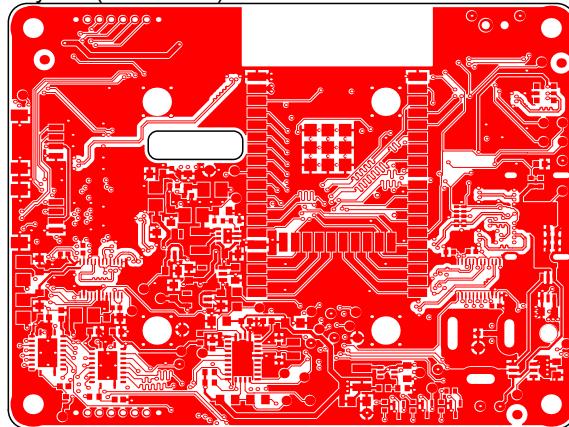
B

C

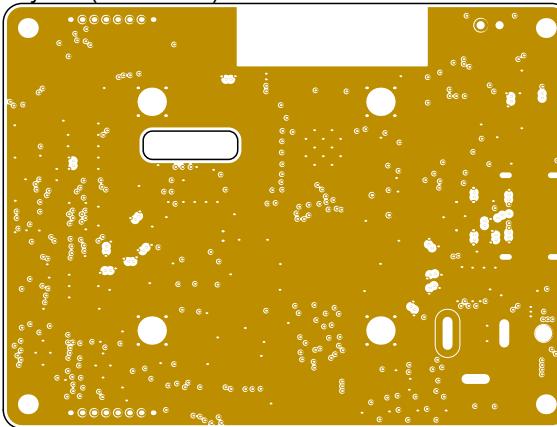
D

E

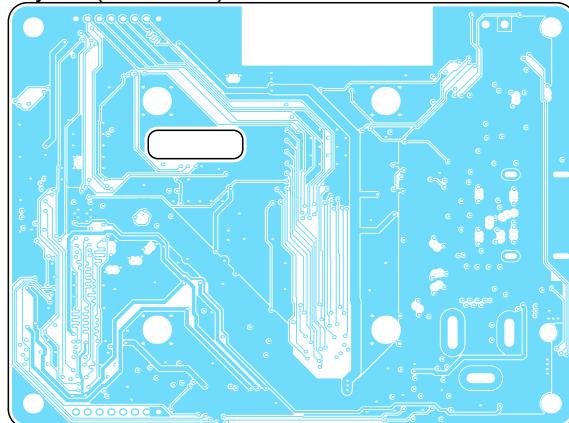
Layer 1 (Scale 1.25)



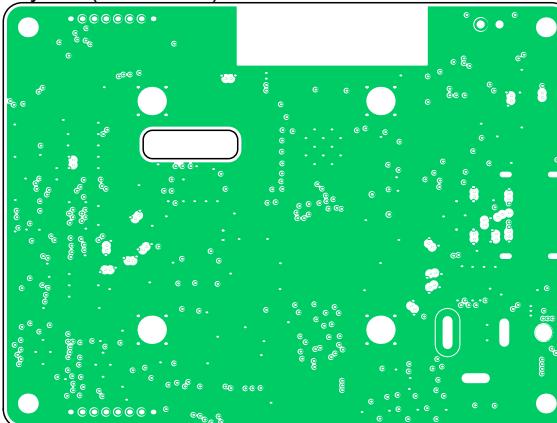
Layer 2 (Scale 1.25)



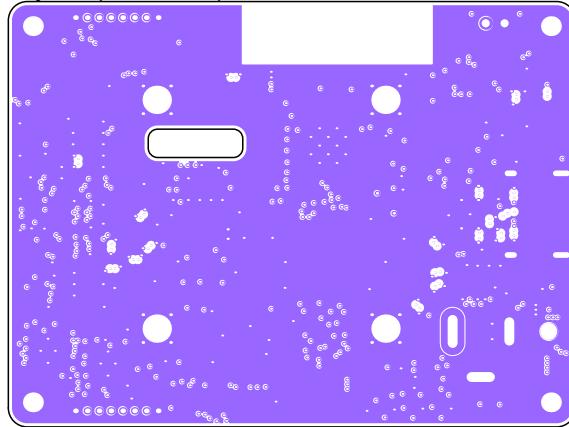
Layer 3 (Scale 1.25)



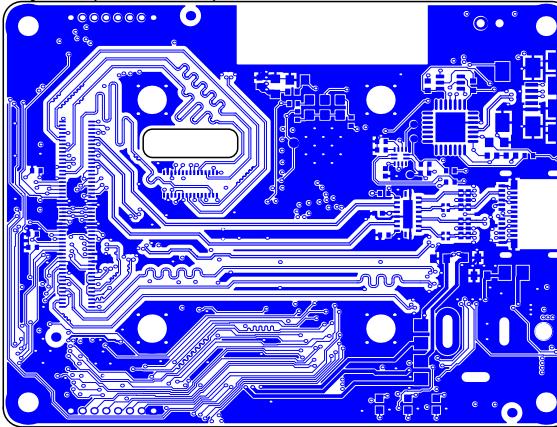
Layer 4 (Scale 1.25)



Layer 5 (Scale 1.25)



Layer 6 (Scale 1.25)

**FABRICATION NOTES:**

Fabricate per IPC-6011 & IPC-6012 CLASS 2
 Inspect per IPC-A-600 CLASS 2
 Test per IPC-TM-650

- * PCB has 6 copper layers
- * Copper thicknesses are finished and include base foil plus Cu plating on plated layers.
- * PCB thickness: 63mil +/- 3mil
- * Min. trace width/clearance: 4/4mil
- * Min. hole drill/ring: 8mil/16mil
- * Soldermask gang relief is allowed for pads in same footprint, if footprint is NSMD.
- * Silkscreen, non-conductive epoxy ink, color: white
- * Remove silkscreen as needed to prevent ink on any exposed copper
- * Surface finish: ENIG
- * Hole dimensions are finished size, +/-3mil
- * Linear board dimension tolerance: +/-10mil
- * Bow, twist, warp not to exceed 0.75% of greatest diagonal span
- * PCB shall be UL Recognized printed wiring board (ZPMV2), minimum flammability rating 94V-0
- * PCB shall be marked with fabricator company or trade name, UL mark, and date code using legend ink on secondary side
- * All PCBs shall be electrically tested for opens and shorts per gerber. Test marking shall be marked on secondary side.

Fabricator shall panelize the PCB using mouse bites and tab routing. V-scoring not allowed.

Controlled impedance differential pairs shall be within +/-10% for 100ohm targets, and +/-10% for 90ohm targets. See Sheet 3 for transmission line details and location of 90ohm differential pairs.

Title: **BW1092**

Number: DXXXX

Revision: R1M0
E1

Date: 11/5/2020

Sheet: 1 of 3

Drawn by: Brian Weinstein

LUXonjs

PROPRIETARY AND CONFIDENTIAL
 THE INFORMATION CONTAINED IN THIS DRAWING IS THE SOLE PROPERTY
 OF LUXONIS HOLDING CORPORATION. ANY REPRODUCTION IN PART OR
 AS A WHOLE WITHOUT THE WRITTEN PERMISSION IS PROHIBITED.

A

B

C

D

E

A

B

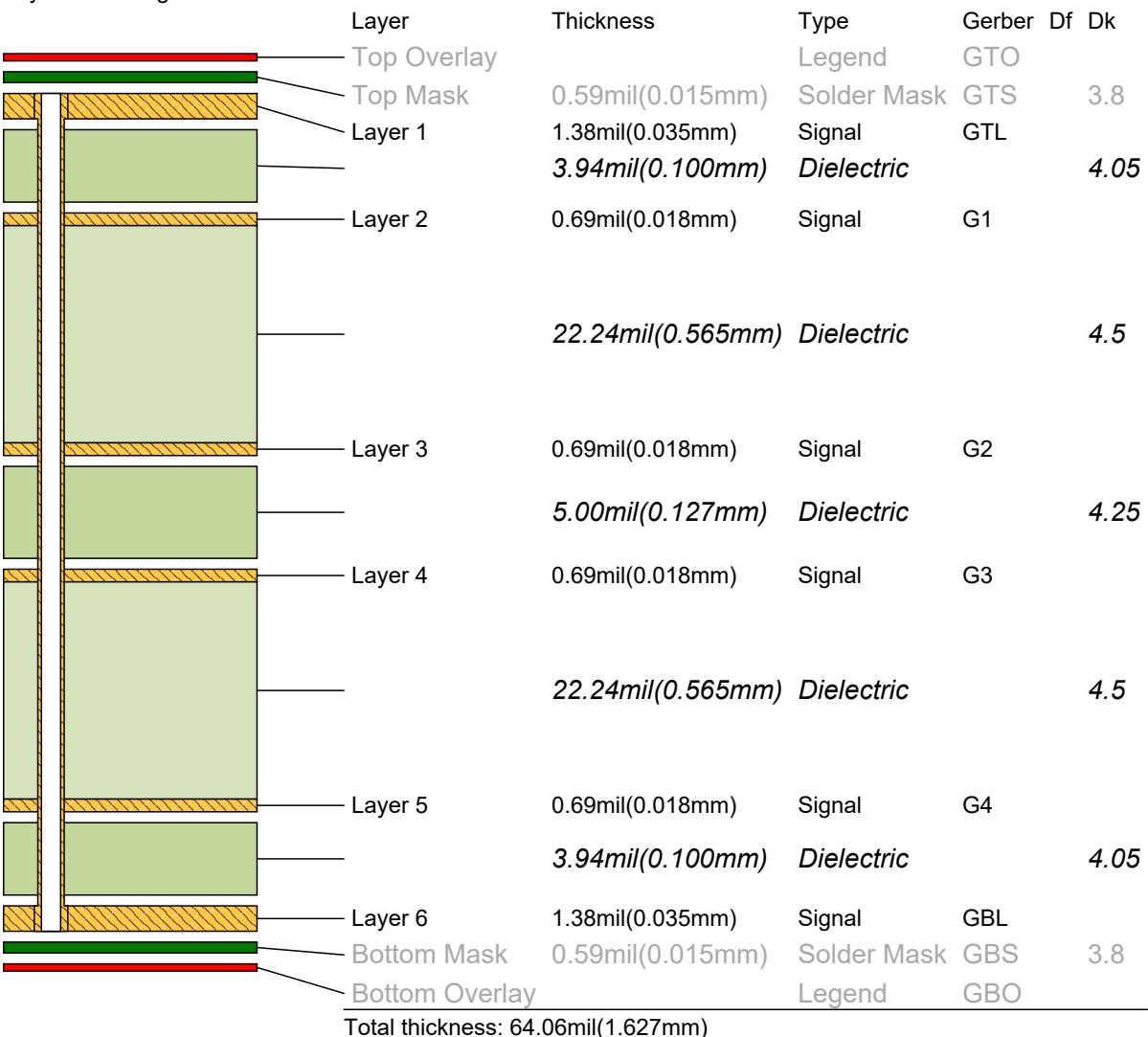
C

D

E

Drill Table				
Symbol	Count	Hole Size	Plated	Hole Tolerance
□	487	8.00mil(0.203mm)	Plated	None
☆	18	10.00mil(0.254mm)	Plated	None
×	16	19.69mil(0.500mm)	Plated	None
☆	4	23.62mil(0.600mm)	Plated	None
◇	2	33.86mil(0.860mm)	Plated	None
○	3	41.34mil(1.050mm)	Plated	None
◇	1	70.87mil(1.800mm)	Non-Plated	None
+	4	82.68mil(2.100mm)	Plated	None
☆	4	118.11mil(3.000mm)	Plated	None
539 Total				

Layer Stack Legend

Title: **BW1092**Number: DXXXX Revision: R1M0
E1

Date: 11/5/2020 Sheet: 2 of 3

Drawn by: Brian Weinstein

LUXonjs

THE INFORMATION CONTAINED IN THIS DRAWING IS THE SOLE PROPERTY OF LUXONIS HOLDING CORPORATION. ANY REPRODUCTION IN PART OR AS A WHOLE WITHOUT THE WRITTEN PERMISSION IS PROHIBITED.

A

B

C

D

E

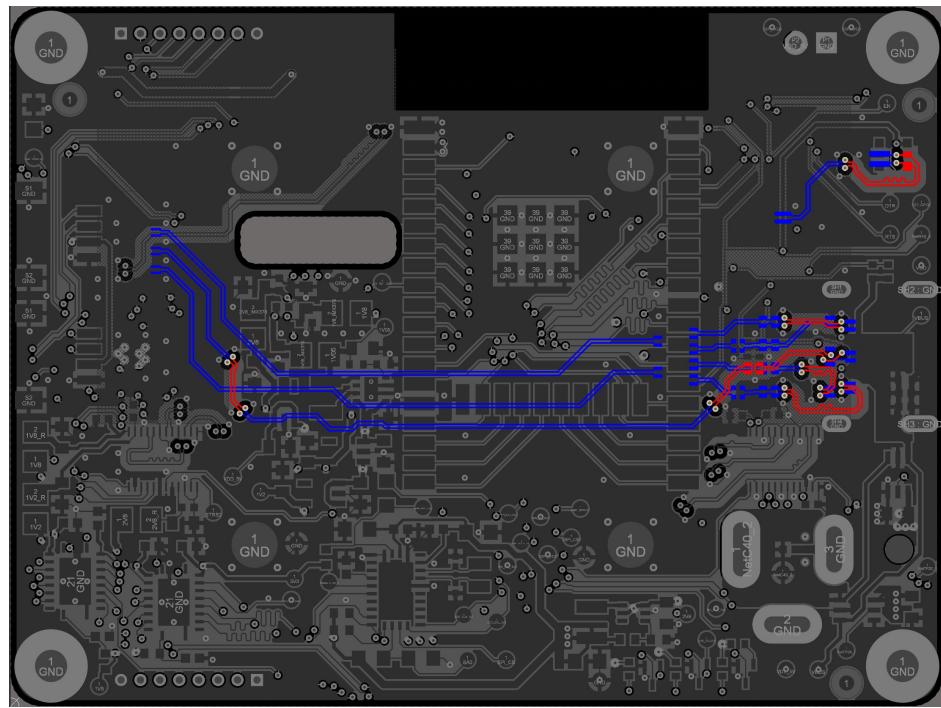
A

B

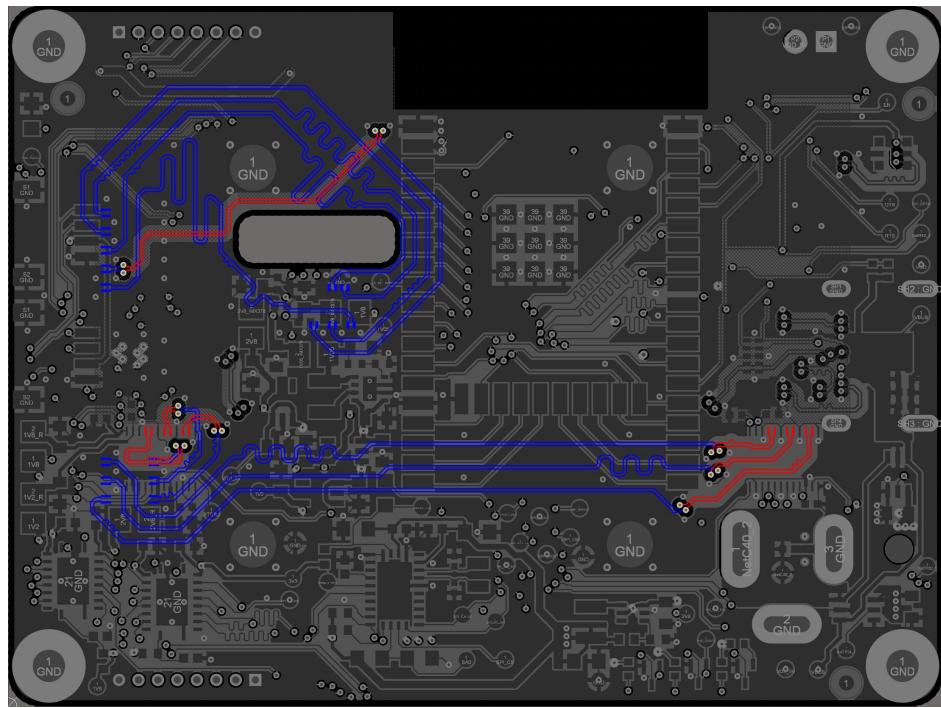
C

D

E



90 OHM (+/-10%) DIFF PAIRS



100 OHM (+/-10%) DIFF PAIRS

Transmission Line Structure Table

Impedance Id	Target Impedance	Calculated Impedance	Trace layer	Lower Trace Width	Upper Trace Width	Gap	Reference layers
1	90	90.89	Layer 1	5.0mil(0.127mm)	4.4mil(0.113mm)	4.0mil(0.102mm)	Layer 2
2	100	101.50	Layer 1	4.0mil(0.102mm)	3.4mil(0.088mm)	4.5mil(0.114mm)	Layer 2
7	90	90.89	Layer 6	5.0mil(0.127mm)	4.4mil(0.113mm)	4.0mil(0.102mm)	Layer 5
8	100	101.50	Layer 6	4.0mil(0.102mm)	3.4mil(0.088mm)	4.5mil(0.114mm)	Layer 5

Title: **BW1092**

Number: DXXXX Revision: R1M0
E1

Date: 11/5/2020 Sheet: 3 of 3

Drawn by: Brian Weinstein

LUXonjs

PROPRIETARY AND CONFIDENTIAL
THE INFORMATION CONTAINED IN THIS DRAWING IS THE SOLE PROPERTY
OF LUXONIS HOLDING CORPORATION. ANY REPRODUCTION IN PART OR
AS A WHOLE WITHOUT THE WRITTEN PERMISSION IS PROHIBITED.

A

B

C

D

E