# Exam Miniproject

# High Performance Computing and Programming

Roel Deckers roel@codingcat.nl 930830-T150

#### 1. Introduction

I have split up the program in several different functions, and will discuss the optimizations applied to each part of the code in their own section.

These functions can be catagorized into: Allocations, allocating memory for all structures; Creating the random array of stars; Sorting the array; Filling the matrix with the right values of starfunc and distances; Creating the values tallied in the histogram, that is the result of Neumann difference scheme on the matrix; and finally the creation of the histogram.

The project contains a single makefile which can build everything up to and including this report. Binaries are stored in the source folder and shared files in src/common. the verify executable runs the supplied reference file. Verify\_large and verify\_large\_refference run the optimized and refference code on the same larger test case to verify that the vector processing cases are handled correctly. Timmings and timmings\_reference create timing data for various N of each of the parts of the program for the optimized resp. refference solution.

All tests were run on an AMD FX-8120 system running on a Windows system with gcc version 5.3.0 (Rev5, Built by MSYS2 project). The compiler flags used are "-Ofast -flto -march=native". An overview of the final speedup can be seen in figure 1 and figure 2.

Performance was analyized using AMD's CodeXL profiler.

## 2. Allocations

This part of the code was not optimized as it is of approximately  $\mathcal{O}(1)$  order and mostly OS dependent. We could have slightly optimized it by allocating a large buffer in user-space and setting our own pointers in this space.

# 3. Create Array

This part of the code was modified to write a struct-of-arrays as opposed to an array-of-structs to allow for easier vectorization in the later parts and to not pollute cachelines with unused variables such as 'designation'. For the random number generation we used a 32-bit LCG which provides us with very fast and repeatable random number generation. Whether this is a realistic optimization depends on what one wishes to model and the requirements on the PRNG.

# 4. Sorting

For sorting the star array we used the quicksort algorithm. because this was an  $\mathcal{O}(n)$  process and the other functions were  $\mathcal{O}(n^2)$  we have saved optimizing this function for last. Unfortunately we were not able to fully optimize it due to time constraints, however the plan would have been to first precompute the array of distances and store it in a pact struct followed by the index of that particular star. Then, because we are only dealing with positive real numbers we can use a union with a unsigned 64 bit integer and sort that array as if it was an array of integers. Afterwards we read out the indices and sort the

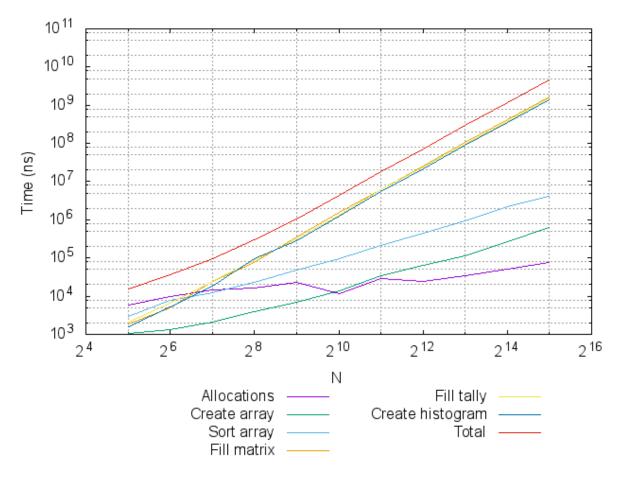


Figure 1: Performance after optimization.

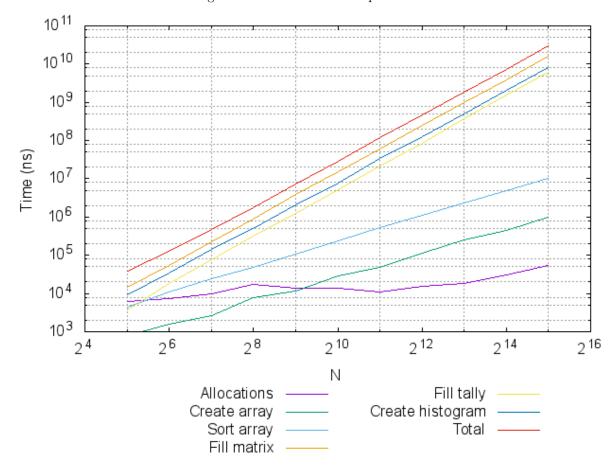


Figure 2: Performance before any optimizations

star array accordingly. Furthermore, it might have been possible to create several bins corresponding to different distances and then storing the (distance, index) pairs in the corresponding bin as we are computing the distances. This prefiltering should reduce the amount of iterations needed for the sorting algorithm to finish.

#### 5. Fill Matrix

This is where the bulk of the speed was gained, compared to the original it is almost  $10 \times$  faster, this is due to the 8 way vectorization and the linear-loads as a result of using a struct-of-arrays. There are virtually no cache-misses according to the profiler. One optimization which was tried but did not work was to use the much faster instruction for the reciprocal square root instead of the square root instruction. While this was much faster, the loss of accuracy led to NaN errors. It might be possible to further optimize this by using a different more accurate approximation for the root. We also rewrote the division in startfunc to be a multiplication and expressed it in such a way that it is clear to the compiler that we are performing a series of fused-multiply-add instructions.

We also saved a large amount of memory here, (and run-time) by noting that the matrix is symmetric we only have to store half of the points. We in fact store slightly more because we store this matrix in a block upper-diagonal matrix with a blocksize of  $8 \times 8$  to help with vectorization.

# 6. Fill tally

This function was the hardest to optimize, most notably due to vectorization and the fact that we store our previous matrix in block diagonal form. Nevertheless we got a significant speedup. Notably by vectorization and by noting that the contibutions between neighbouting points are symmetric and therefore we only have to compute them once for each pair instead of for each point. Several attempts (on paper) were made to efficiently vectorize this code, possibly by changing the data structure, but in the end I chose to apply a simple scheme which uses unalligned loads to line up the correct vector-lanes. According the AMD's CodeXL this instruction is now 75% of the runtime of the function, this is because unaligned loads are rather expensive on this particular CPU, but nevertheless performance was increased by adding vectorization. (and on later Intel CPUs the penalty for misaligned loads is minimal so performance gains will be even better there).

Here too, the resulting matrix is symmetrical and so we store it as a tridiagonal matrix (as a flat array). When computing the histogram we take care to only count the diagonal elements once but the other elements twice.

For added perfomance increase we could compute the min and max in this loop and pass that to the histogram algorithm, this gave a Performance boost before but was removed when I started to vectorize the code and has not yet been added back.

## 7. Create Histogram

Not much optimization was applied to this function yet, any speedup is due to the reduction of the size of the tally-matrix and by directly computing the index of an element instead of using a loop. I tried to replace the division by a multiplication, but there was no measurable difference. We could vectorize the index computation, but to the best of my knowledge there is no way to use the resulting indices directly in vector operations afterwards on the development machine.