Spring 2025 Operating Systems

CSSE 332 -- OPERATING SYSTEMS

Introduction to Memory Virtualization

N	Jame:
other words, the system.	oints) Consider a system where each process is mapped directly into memory. I he process can directly access memory without intervention from the operatin
What are some	e of the main challenges with this approach?
Question 2 . (5 po addresses?	oints) In your own words, describe what it means for a process to have virtue

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hardware) translates a ______ into a _____.

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mpts to ac		that proces	_	_	_			
5. Assume								
5. Assume								
y segment		_						_
points) D	escribe	e how an 8-b	oit addre	ess would b	e divided u	p to pe	rform a	address tra
tion. You	may us	se the bit-bo	x below.					
	7	6 5	4	3	2 1	0		
		when proces	is P_1 is 1	loaded into	memory, i	t is assi	gnmed	the follow
		Segment	Base	Bounds	Growth			
		Code	0x40	0x0f	+			
		Globals	0x50	0x0A	+]		
		Heap	0x60	0x10	+			
		Stack	0x7f	0x10	_			
1	y segment and heap. points) Description. You	y segmentation. and heap. points) Describe tion. You may us	y segmentation. Each process and heap. points) Describe how an 8-betion. You may use the bit-bout of the boundary of the bit-boundary of the bit-	y segmentation. Each process should and heap. points) Describe how an 8-bit addression. You may use the bit-box below. The segment is 1 segment table. Segment Base Code 0x40 Globals 0x50 Heap 0x60	y segmentation. Each process should have the and heap. points) Describe how an 8-bit address would be tion. You may use the bit-box below. The segment when process P_1 is loaded into gment table. Segment Base Bounds Code 0x40 0x0f Globals 0x50 0x0A Heap 0x60 0x10	y segmentation. Each process should have the generic formula heap. points) Describe how an 8-bit address would be divided upon tion. You may use the bit-box below. The process P_1 is loaded into memory, in generated and P_2 is loaded into memory, in generated by the process P_3 is loaded into memory, in generated by P_4 is loaded into memory, in P_4 is loaded into me	y segmentation. Each process should have the generic four sectand heap. points) Describe how an 8-bit address would be divided up to pertion. You may use the bit-box below. The following the following process P_1 is loaded into memory, it is assignment table. Segment Base Bounds Growth Globals $0x50$ $0x0A$ $+$ $0x60$ $0x10$	points) Describe how an 8-bit address would be divided up to perform a tion. You may use the bit-box below. The points of the bit-box below.

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iii.	(5 points) Assume P_1 attempts to access the virtual address $0x84$, what would be the corresponding physical address? (Write segmentation fault if the access is invalid).
iv.	(5 points) Assume P_1 attempts to access the virtual address $0xC8$, what would be the corresponding physical address? (Write segmentation fault if the access is invalid).
v.	(5 points) Assume P_1 attempts to access the virtual address $0xE4$, what would be the corresponding physical address? (Write segmentation fault if the access is invalid).
	t. (10 points) Please write down two sentences describing two new things that you in this session.
Question 7	7. (10 points) Please write down two things that you are still not very clear about, or
any ques	stions that you might have that the session did not go over or did not cover well.

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