

CSSE 332 -- OPERATING SYSTEMS

Introduction to Memory Virtualization

Name: _____

Question 1. (5 points) Consider a system where each process is mapped directly into memory. In other words, the process can directly access memory without intervention from the operating system.

What are some of the main challenges with this approach?

Question 2. (5 points) In your own words, describe what it means for a process to have *virtual addresses*?

Question 3. (5 points) Address translation is the process by which the operating system (and the hardware) translates a _____ into a _____.

Question 4. The questions below refer to the *base and bounds* memory translation approach.

- (a) (5 points) Assume that process P_1 gets assigned a base register `base_reg`. Write down the formula used to calculate the *physical address* (PA) from a given *virtual address* (VA).

- (b) (5 points) Assume that process P_1 gets assigned a base register 0x0048. When P_1 attempts to access address 0xff04, which physical address does it end up accessing?

Question 5. Assume we are running on an 8-bit architecture and we would like to implement memory segmentation. Each process should have the generic four sections: code, globals, stack, and heap.

- (a) (5 points) Describe how an 8-bit address would be divided up to perform address translation. You may use the bit-box below.

7	6	5	4	3	2	1	0

- (b) Assume now that when process P_1 is loaded into memory, it is assigned the following segment table.

Segment	Base	Bounds	Growth
Code	0x40	0x0f	+
Globals	0x50	0x0A	+
Heap	0x60	0x10	+
Stack	0x7f	0x10	-

- i. (5 points) Write down the formula used to translate a virtual address into a physical address using the segment table above.

- ii. (5 points) Assume P_1 attempts to access the virtual address 0x04, what would be the corresponding physical address? (Write segmentation fault if the access is invalid).

- iii. (5 points) Assume P_1 attempts to access the virtual address `0x84`, what would be the corresponding physical address? (Write segmentation fault if the access is invalid).

- iv. (5 points) Assume P_1 attempts to access the virtual address `0xC8`, what would be the corresponding physical address? (Write segmentation fault if the access is invalid).

- v. (5 points) Assume P_1 attempts to access the virtual address `0xE4`, what would be the corresponding physical address? (Write segmentation fault if the access is invalid).