Spring 2025 Operating Systems

## CSSE 332 -- OPERATING SYSTEMS

## Paging

	Name:
	on 1. (5 points) What is the main feature of paging that allows us to avoid external frag tation?
chun they mair	on 2. (5 points) In a paged memory architecture, memory is divided into equal sized also. In virtual space, those chunks are called, while in physical space are called, which is an array of  on 3. The following questions assume a 16-bit architecture, which means that all of our contents of the
addr	resses are 16 bits wide.  (5 points) If we would like to design our paging system to have <b>1KB</b> pages. How many bits should we reserve from the 16-bits address for the page offset?
(b)	(5 points) In that case, how many pages can each process have?
(c)	(5 points) In this bit-box below, show how the page number and the page offset are represented.

Tue Apr 01 2025 Page 1 of 3

Spring 2025 Operating Systems

(4)	(5 points) Write down the formula used to translate a virtual address (VA) into a physical address (PA).
(e)	(5 points) Assume that each PTE is 16 bits, what is the size of a page table (in bytes)?
(6)	(5 points) Assume that each 1 1E is 10 bits, what is the size of a page table (in bytes):
	on 4. (5 points) Assume now that we are working with a 32-bits RISC-V architecture. Cribe the content of a page table entry (PTE) in the bit box below.  31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
uestic	on 5. (5 points) Describe the meaning of the permission bits in RISC-V below.
1.	PTE_V:
2.	PTE_W:
	PTE_R:
	PTE_X:
5.	PTE_U:
uestic	on 6. Answer the following question about the xv6 operating system.
(a)	(5 points) Given a process represented by a struct proc pointer p. You can use to access p's page table in the kernel.
(b)	(5 points) When in kernel space executing on behalf of a user process (e.g., on a system call), you can use the function to access the current running <b>user</b> process.
(c)	(5 points) The function can be used to fetch a physical frame number given a virtual address. The lower bits of the returned physical frame number will always be

Tue Apr 01 2025 Page 2 of 3

Spring 2025 OPERATING SYSTEMS Question 7. (5 points) Describe some of the shortcomings of the discussed paging approach. Question 8. (10 points) Please write down two sentences describing two new things that you learned in this session. Question 9. (10 points) Please write down two things that you are still not very clear about, or any questions that you might have that the session did not go over or did not cover well.

Tue Apr 01 2025 Page 3 of 3