CPU Performance Analysis Report 4.2.1

Measured time	Sat Jul 9 20:27:47 2022
Node name	l28-3109c

Process no.	0
CMG no.	0
Measured region	axhelm_kernel, 1

Vector length (bit)	512
CPU frequency (GHz)	1.999

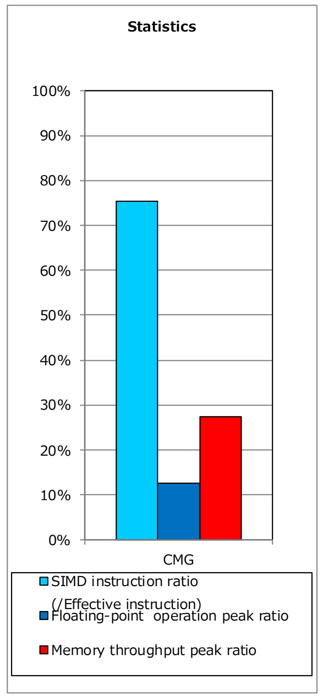
Statistics Process	Thread	Execution time (s)	GFLOPS	Floating-point operation peak ratio (%)	Memory throughput (GB/s)	Memory throughput peak ratio (%)	Effective instruction	Floating-point operation	SIMD instruction rate (%) (/Effective instruction)	SVE operation rate (%)	Floating-point pipeline Active element rate (%)	IPC	GIPS
0	0	1.09E-01	8.02	12.54%	5.80	27.47%	1.74E+08	8.77E+08	75.39%	100.00%	91.69%	0.79	1.59
0	1	1.09E-01	8.03	12.55%	5.87		1.73E+08	8.77E+08	75.41%	100.00%	91.75%	0.79	1.59
0	2	1.09E-01	8.03	12.55%	5.83		1.73E+08	8.77E+08	75.41%	100.00%	91.78%	0.79	1.59
0	3	1.09E-01	8.02	12.54%	5.77		1.73E+08	8.77E+08	75.41%	100.00%	91.78%	0.79	1.59
0	4	1.09E-01	8.03	12.55%	5.97		1.73E+08	8.77E+08	75.41%	100.00%	91.78%	0.79	1.59
0	5	1.09E-01	8.02	12.54%	5.83		1.73E+08	8.77E+08	75.41%	100.00%	91.78%	0.79	1.59
0	6	1.09E-01	8.02	12.54%	5.85		1.73E+08	8.77E+08	75.41%	100.00%	91.77%	0.79	1.59
0	7	1.09E-01	8.02	12.54%	5.90		1.73E+08	8.77E+08	75.41%	100.00%	91.77%	0.79	1.59
0	8	1.09E-01	8.03	12.55%	5.87		1.73E+08	8.77E+08	75.41%	100.00%	91.78%	0.79	1.59
0	9	1.09E-01	8.02	12.54%	5.85		1.73E+08	8.77E+08	75.41%	100.00%	91.77%	0.79	1.59
0	10	1.09E-01	8.02	12.54%	5.89		1.73E+08	8.77E+08	75.41%	100.00%	91.77%	0.79	1.59
0	11	1.09E-01	8.02	12.54%	5.88		1.73E+08	8.77E+08	75.41%	100.00%	91.78%	0.79	1.59
CMG 0 total		1.09E-01	96.30	12.54%	70.31	27.47%	2.08E+09	1.05E+10	75.41%	100.00%	91.77%	0.79	19.06

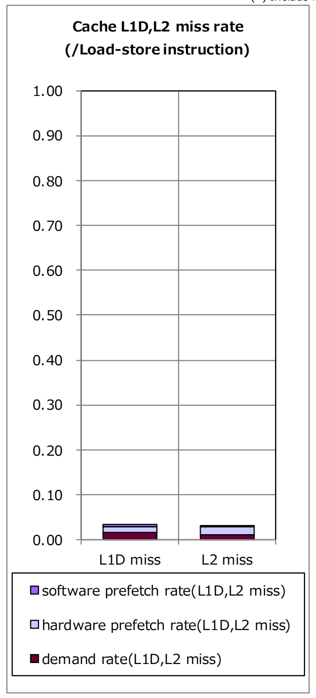
Cycle Accounting Process	Thread	Prefetch port busy wait: Prefetch port busy wait by hardware prefetch	Prefetch port busy wait by software prefetch	Memory access wait & Cache access wait: Integer load memory access wait	Floating-point load memory access wait	Integer load L2 cache access wait	Integer load L1D cache access wait	Floating-point load L2 cache access wait	Floating-point load L1D cache access wait (*)	Operation wait: Integer operation wait	Floating-point operation wait	Other wait: Branch instruction wait	Other wait	Store port busy wait	Instruction fetch wait	Barrier synchronization wait	1 instruction commit	Other instruction commit: 2 instruction commit	3 instruction commit	4 instruction commit	Other instruction commit	Total
0	0	0.00E+00	0.00E+00	9.04E-05	3.78E-02	4.15E-05	8.36E-05	2.15E-03	2.18E-03	5.37E-04	3.75E-02	2.43E-06	3.90E-06	0.00E+00	1.54E-05	1.51E-03	4.47E-03	3.42E-03	2.16E-03	1.73E-02	0.00E+00	1.09E-01
0	1	0.00E+00	0.00E+00	6.64E-05	3.55E-02	5.05E-05	6.31E-05	2.94E-03	3.08E-03	7.79E-04	3.74E-02	2.18E-06	1.10E-05	0.00E+00	8.45E-05	1.78E-03	4.52E-03	3.53E-03	2.13E-03	1.72E-02	1.50E-05	1.09E-01
0	2	0.00E+00	0.00E+00	6.02E-05	3.54E-02	5.03E-05	6.63E-05	3.18E-03	2.94E-03	7.18E-04	3.75E-02	2.00E-06	3.81E-06	0.00E+00	2.74E-05	1.92E-03	4.51E-03	3.52E-03	2.13E-03	1.72E-02	0.00E+00	1.09E-01
0	3	0.00E+00	0.00E+00	5.46E-05	3.58E-02	4.90E-05	6.88E-05	3.09E-03	2.98E-03	5.92E-04	3.75E-02	2.13E-06	5.69E-06	0.00E+00	4.02E-05	1.69E-03	4.52E-03	3.51E-03	2.14E-03	1.72E-02	6.43E-06	1.09E-01
0	4	0.00E+00	0.00E+00	5.80E-05	3.59E-02	5.04E-05	6.88E-05	3.40E-03	3.17E-03	5.99E-04	3.74E-02	2.00E-06	8.78E-06	0.00E+00	6.37E-05	1.11E-03	4.50E-03	3.51E-03	2.14E-03	1.72E-02	1.36E-05	1.09E-01
0	5	0.00E+00	0.00E+00	4.95E-05	3.58E-02	4.92E-05	8.23E-05	3.28E-03	2.85E-03	5.78E-04	3.76E-02	2.23E-06	3.95E-06	0.00E+00	2.96E-05	1.55E-03	4.52E-03	3.52E-03	2.14E-03	1.72E-02	0.00E+00	1.09E-01
0	6	0.00E+00	0.00E+00	5.14E-05	3.59E-02	5.14E-05	6.57E-05	3.37E-03	3.04E-03	6.02E-04	3.76E-02	1.97E-06	3.43E-06	0.00E+00	2.45E-05	1.20E-03	4.52E-03	3.54E-03	2.14E-03	1.72E-02	1.51E-05	1.09E-01
0	7	0.00E+00	0.00E+00	4.99E-05	3.61E-02	5.00E-05	8.86E-05	3.67E-03	2.87E-03	6.51E-04	3.75E-02	2.02E-06	5.58E-06	0.00E+00	4.06E-05	8.33E-04	4.51E-03	3.55E-03	2.14E-03	1.72E-02	1.60E-05	1.09E-01
0	8	0.00E+00	0.00E+00	5.05E-05	3.59E-02	5.06E-05	6.94E-05	3.69E-03	3.16E-03	6.06E-04	3.76E-02	1.97E-06	3.33E-06	0.00E+00	2.47E-05	7.00E-04	4.51E-03	3.56E-03	2.13E-03	1.72E-02	0.00E+00	1.09E-01
0	9	0.00E+00	0.00E+00	4.86E-05	3.62E-02	5.17E-05	6.36E-05	3.62E-03	2.97E-03	8.03E-04	3.75E-02	2.01E-06	5.33E-06	0.00E+00	3.90E-05	5.82E-04	4.52E-03	3.57E-03	2.17E-03	1.72E-02	0.00E+00	1.09E-01
0	10	0.00E+00	0.00E+00	4.07E-05	3.66E-02	6.01E-05	6.15E-05	3.55E-03	2.98E-03	7.98E-04	3.74E-02	1.97E-06	3.41E-06	0.00E+00	2.19E-05	3.42E-04	4.51E-03	3.52E-03	2.14E-03	1.72E-02	3.83E-06	1.09E-01
0	11	0.00E+00	0.00E+00	4.94E-05	3.62E-02	5.42E-05	8.72E-05	3.81E-03	2.93E-03	5.49E-04	3.76E-02	1.85E-06	3.12E-06	0.00E+00	2.46E-05	5.06E-04	4.51E-03	3.56E-03	2.12E-03	1.72E-02	7.76E-08	1.09E-01
CMG 0 total		0.00E+00	0.00E+00	5.58E-05	3.61E-02	5.07E-05	7.24E-05	3.31E-03	2.93E-03	6.51E-04	3.75E-02	2.06E-06	5.12E-06	0.00E+00	3.63E-05	1.14E-03	4.51E-03	3.53E-03	2.14E-03	1.72E-02	5.83E-06	1.09E-01

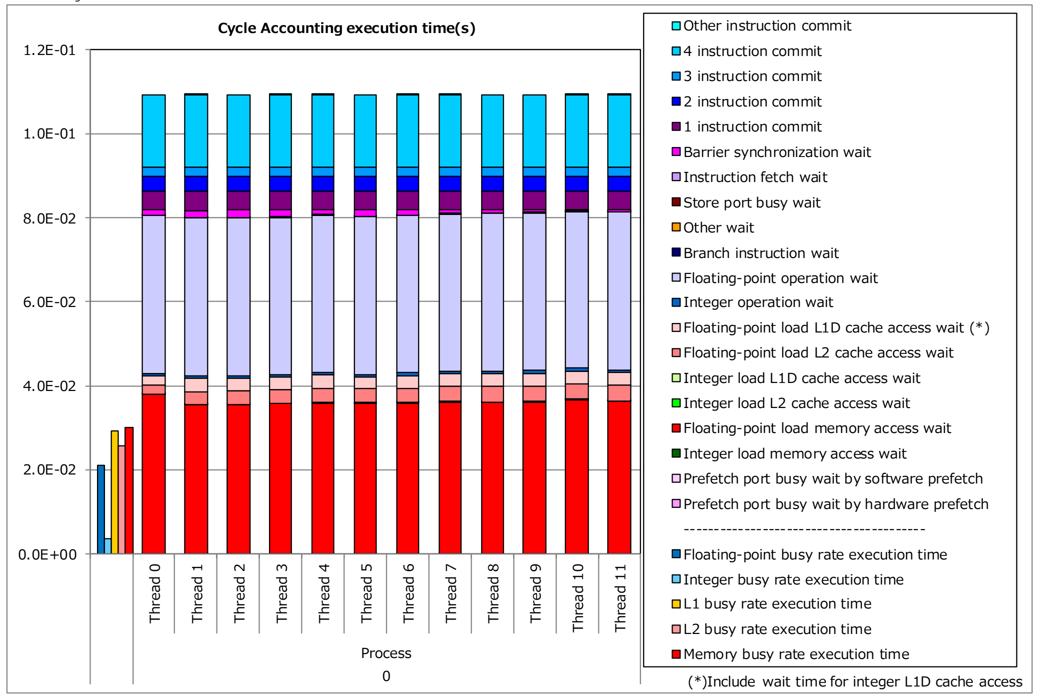
(*) Include wait time for integer L1D cache access

Busy Process	Thread	Floating-point operation pipeline A busy rate (%)	Floating-point operation pipeline B busy rate (%)	Integer operation pipeline A busy rate (%)	Integer operation pipeline B busy rate (%)	L1 busy rate (%)	L2 busy rate (%)	Memory busy rate (%)	Address calculation operation pipeline A busy rate (%)	Address calculation operation pipeline B busy rate (%)	Floating-point pipeline A Active element rate (%)	Floating-point pipeline B Active element rate (%)	L1 pipeline 0 Active element rate (%)	L1 pipeline 1 Active element rate (%)	SFI(Store Fetch Interlock) rate
0	0	22.26%	16.20%	2.05%	4.60%	26.06%	23.42%	27.47%	24.12%	15.97%	85.65%	100.00%	100.00%	100.00%	0.01
0	1	22.27%	16.25%	2.08%	4.68%	26.89%			24.24%	16.00%	85.74%	100.00%	100.00%	100.00%	0.01
0	2	22.25%	16.24%	2.03%	4.65%	26.83%			24.14%	15.94%	85.78%	100.00%	100.00%	100.00%	0.01
0	3	22.29%	16.22%	2.03%	4.64%	26.82%			24.14%	15.94%	85.80%	100.00%	100.00%	100.00%	0.01
0	4	22.25%	16.24%	2.04%	4.66%	26.84%			24.17%	15.97%	85.78%	100.00%	100.00%	100.00%	0.01
0	5	22.26%	16.24%	2.04%	4.64%	26.85%			24.14%	15.93%	85.78%	100.00%	100.00%	100.00%	0.01
0	6	22.28%	16.22%	2.05%	4.67%	26.92%			24.20%	15.96%	85.77%	100.00%	100.00%	100.00%	0.01
0	7	22.26%	16.24%	2.04%	4.67%	26.89%			24.20%	15.98%	85.77%	100.00%	100.00%	100.00%	0.01
0	8	22.26%	16.22%	2.02%	4.64%	26.86%			24.15%	15.94%	85.79%	100.00%	100.00%	100.00%	0.01
0	9	22.28%	16.21%	2.04%	4.65%	26.89%			24.19%	15.95%	85.78%	100.00%	100.00%	100.00%	0.01
0	10	22.27%	16.22%	2.06%	4.69%	26.87%			24.21%	15.96%	85.77%	100.00%	100.00%	100.00%	0.01
0	11	22.26%	16.23%	2.08%	4.69%	26.86%			24.24%	15.98%	85.78%	100.00%	100.00%	100.00%	0.01
CMG 0 total		22.27%	16.23%	2.05%	4.66%	26.80%	23.42%	27.47%	24.18%	15.96%	85.77%	100.00%	100.00%	100.00%	0.01

Cache Process	Thread	L1I miss rate (/Effective instruction)	Load-store instruction	L1D miss	L1D miss rate (/Load-store instruction)	L1D miss demand rate (%) (/L1D miss)	L1D miss hardware prefetch rate (%) (/L1D miss)	L1D miss software prefetch rate (%) (/L1D miss)	L2 miss	L2 miss rate (/Load-store instruction)	L2 miss demand rate (%) (/L2 miss)	L2 miss hardware prefetch rate (%) (/L2 miss)	L2 miss software prefetch rate (%) (/L2 miss)	L1D TLB miss rate (/Load-store instruction)	L2D TLB miss rate (/Load-store instruction)
0	0	0.00	7.30E+07	2.44E+06	0.03	50.51%	37.55%	11.94%	2.25E+06	0.03	35.05%	55.87%	9.08%	0.00000	0.00000
0	1	0.00	7.30E+07	2.48E+06	0.03	50.03%	37.37%	12.60%	2.25E+06	0.03	35.58%	55.28%	9.15%	0.00000	0.00000
0	2	0.00	7.30E+07	2.48E+06	0.03	49.97%	37.54%	12.49%	2.24E+06	0.03	35.72%	55.51%	8.77%	0.00000	0.00000
0	3	0.00	7.30E+07	2.48E+06	0.03	50.08%	37.38%	12.54%	2.24E+06	0.03	35.43%	55.29%	9.28%	0.00000	0.00000
0	4	0.00	7.30E+07	2.48E+06	0.03	50.10%	37.33%	12.56%	2.24E+06	0.03	35.79%	55.33%	8.89%	0.00000	0.00000
0	5	0.00	7.30E+07	2.48E+06	0.03	49.98%	37.36%	12.66%	2.24E+06	0.03	35.43%	55.40%	9.17%	0.00000	0.00000
0	6	0.00	7.30E+07	2.48E+06	0.03	49.93%	37.46%	12.61%	2.24E+06	0.03	35.54%	55.53%	8.93%	0.00000	0.00000
0	7	0.00	7.30E+07	2.48E+06	0.03	50.09%	37.28%	12.63%	2.24E+06	0.03	35.48%	55.31%	9.22%	0.00000	0.00000
0	8	0.00	7.30E+07	2.48E+06	0.03	50.06%	37.39%	12.55%	2.24E+06	0.03	35.66%	55.47%	8.87%	0.00000	0.00000
0	9	0.00	7.30E+07	2.48E+06	0.03	50.10%	37.35%	12.55%	2.24E+06	0.03	35.48%	55.31%	9.21%	0.00000	0.00000
0	10	0.00	7.30E+07	2.48E+06	0.03	50.21%	37.20%	12.59%	2.24E+06	0.03	36.01%	55.14%	8.85%	0.00000	0.00000
0	11	0.00	7.30E+07	2.48E+06	0.03	50.08%	37.31%	12.61%	2.24E+06	0.03	35.28%	55.49%	9.24%	0.00000	0.00000
CMG 0 total		0.00	8.76E+08	2.97E+07	0.03	50.09%	37.38%	12.53%	2.69E+07	0.03	35.54%	55.41%	9.05%	0.00000	0.00000







Instruction (Load-store instruction)

Process	Thread	Load SIMD: Single vector contiguous load instruction	Multiple vector contiguous structure load instruction	Non-contiguous gather load instruction	Broadcast load instruction	Floating-point register fill instruction	Predicate register fill instruction	First-fault load instruction	Load Non-SIMD: Non-SIMD load instruction	Store SIMD: Single vector contiguous store instruction	Multiple vector contiguous structure store instruction	Non-contiguous scatter store instruction	Floating-point register spill instruction	Predicate register spill instruction	Store Non-SIMD: Non-SIMD store instruction
0	0	2.10E+07	0.00E+00	1.02E+06	3.08E+06	2.34E+07	1.70E+01	0.00E+00	1.73E+07	5.12E+06	0.00E+00	0.00E+00	1.25E+06	0.00E+00	8.51E+05
0	1	2.10E+07	0.00E+00	1.02E+06	3.08E+06	2.34E+07	1.70E+01	0.00E+00	1.72E+07	5.12E+06	0.00E+00	0.00E+00	1.25E+06	0.00E+00	8.43E+05
0	2	2.10E+07	0.00E+00	1.02E+06	3.08E+06	2.34E+07	1.70E+01	0.00E+00	1.72E+07	5.12E+06	0.00E+00	0.00E+00	1.25E+06	0.00E+00	8.43E+05
0	3	2.10E+07	0.00E+00	1.02E+06	3.08E+06	2.34E+07	1.70E+01	0.00E+00	1.72E+07	5.12E+06	0.00E+00	0.00E+00	1.25E+06	0.00E+00	8.43E+05
0	4	2.10E+07	0.00E+00	1.02E+06	3.08E+06	2.34E+07	1.70E+01	0.00E+00	1.72E+07	5.12E+06	0.00E+00	0.00E+00	1.25E+06	0.00E+00	8.43E+05
0	5	2.10E+07	0.00E+00	1.02E+06	3.08E+06	2.34E+07	1.70E+01	0.00E+00	1.72E+07	5.12E+06	0.00E+00	0.00E+00	1.25E+06	0.00E+00	8.43E+05
0	6	2.10E+07	0.00E+00	1.02E+06	3.08E+06	2.34E+07	1.70E+01	0.00E+00	1.72E+07	5.12E+06	0.00E+00	0.00E+00	1.25E+06	0.00E+00	8.43E+05
0	7	2.10E+07	0.00E+00	1.02E+06	3.08E+06	2.34E+07	1.70E+01	0.00E+00	1.72E+07	5.12E+06	0.00E+00	0.00E+00	1.25E+06	0.00E+00	8.43E+05
0	8	2.10E+07	0.00E+00	1.02E+06	3.08E+06	2.34E+07	1.70E+01	0.00E+00	1.72E+07	5.12E+06	0.00E+00	0.00E+00	1.25E+06	0.00E+00	8.43E+05
0	9	2.10E+07	0.00E+00	1.02E+06	3.08E+06	2.34E+07	1.70E+01	0.00E+00	1.72E+07	5.12E+06	0.00E+00	0.00E+00	1.25E+06	0.00E+00	8.43E+05
0	10	2.10E+07	0.00E+00	1.02E+06	3.08E+06	2.34E+07	1.70E+01	0.00E+00	1.72E+07	5.12E+06	0.00E+00	0.00E+00	1.25E+06	0.00E+00	8.43E+05
0	11	2.10E+07	0.00E+00	1.02E+06	3.08E+06	2.34E+07	1.70E+01	0.00E+00	1.72E+07	5.12E+06	0.00E+00	0.00E+00	1.25E+06	0.00E+00	8.43E+05
CMG 0 total		2.52E+08	0.00E+00	1.23E+07	3.69E+07	2.81E+08	2.04E+02	0.00E+00	2.07E+08	6.14E+07	0.00E+00	0.00E+00	1.51E+07	0.00E+00	1.01E+07
CMG 0 total		8.76E+08													

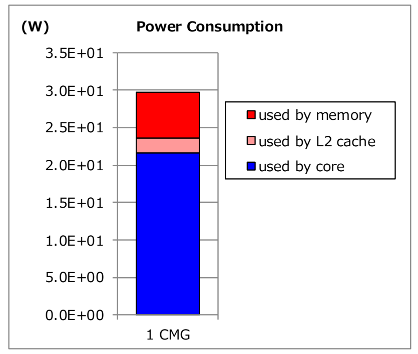
Process	Thread	Prefetch: Contiguous prefetch instruction	Gathering prefetch instruction	Scalar prefetch instruction	DCZVA instruction	Floating-point instruction: Floating-point instruction except FMA and reciprocal	FMA instruction	Floating-point reciprocal instruction	Floating-point move and conversion: Floating-point conversion instruction	Floating-point move instruction	Integer instruction	Branch instruction	Predicate instruction	Crypto-graphic instruction	Other instruction	Total
0	0	0.00E+00	0.00E+00	1.66E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	1.55E+07	0.00E+00	2.88E+05	1.01E+02	0.00E+00	2.26E+07	1.74E+08
0	1	0.00E+00	0.00E+00	1.66E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	1.55E+07	0.00E+00	2.84E+05	1.01E+02	0.00E+00	2.26E+07	1.73E+08
0	2	0.00E+00	0.00E+00	1.66E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	1.55E+07	0.00E+00	2.84E+05	1.01E+02	0.00E+00	2.26E+07	1.73E+08
0	3	0.00E+00	0.00E+00	1.66E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	1.55E+07	0.00E+00	2.84E+05	1.01E+02	0.00E+00	2.26E+07	1.73E+08
0	4	0.00E+00	0.00E+00	1.66E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	1.55E+07	0.00E+00	2.84E+05	1.01E+02	0.00E+00	2.26E+07	1.73E+08
0	5	0.00E+00	0.00E+00	1.66E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	1.55E+07	0.00E+00	2.84E+05	1.01E+02	0.00E+00	2.26E+07	1.73E+08
0	6	0.00E+00	0.00E+00	1.66E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	1.55E+07	0.00E+00	2.84E+05	1.01E+02	0.00E+00	2.26E+07	1.73E+08
0	7	0.00E+00	0.00E+00	1.66E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	1.55E+07	0.00E+00	2.84E+05	1.01E+02	0.00E+00	2.26E+07	1.73E+08
0	8	0.00E+00	0.00E+00	1.66E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	1.55E+07	0.00E+00	2.83E+05	1.01E+02	0.00E+00	2.26E+07	1.73E+08
0	9	0.00E+00	0.00E+00	1.66E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	1.55E+07	0.00E+00	2.84E+05	1.01E+02	0.00E+00	2.26E+07	1.73E+08
0	10	0.00E+00	0.00E+00	1.66E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	1.55E+07	0.00E+00	2.83E+05	1.01E+02	0.00E+00	2.26E+07	1.73E+08
0	11	0.00E+00	0.00E+00	1.66E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	1.55E+07	0.00E+00	2.83E+05	1.01E+02	0.00E+00	2.26E+07	1.73E+08
CMG 0 total		0.00E+00	0.00E+00	2.00E+07	9.60E+01	1.35E+08	5.90E+08	0.00E+00	0.00E+00	1.86E+08	0.00E+00	3.41E+06	1.21E+03	0.00E+00	2.71E+08	2.08E+09
CMG 0 total		2.00E+07			9.60E+01	7.25E+08			1.86E+08		0.00E+00	3.41E+06	1.21E+03	0.00E+00	2.71E+08	2.08E+09

Power Consumption (W) Process	Thread	Power consumption used by core	Power consumption used by L2 cache	Power consumption used by memory
0	0	1.80E+00	2.07E+00	6.10E+00
0	1	1.80E+00		
0	2	1.80E+00		
0	3	1.80E+00		
0	4	1.80E+00		
0	5	1.80E+00		
0	6	1.80E+00		
0	7	1.80E+00		
0	8	1.80E+00		
0	9	1.80E+00		
0	10	1.80E+00		
0	11	1.80E+00		
CMG 0 total		2.16E+01	2.07E+00	6.10E+00

Hardware Prefetch Rate (%) (/Hardware Prefetch) Process	Thread	L1: Stream mode prefetch rate	L1: Injection mode allocate prefetch rate	L1: Injection mode unallocate prefetch rate	L2: Stream mode prefetch rate	L2: Injection mode allocate prefetch rate	L2: Injection mode unallocate prefetch rate	L1/L2: Other hardware prefetch
0	0	36.70%	0.00%	0.00%	46.66%	0.00%	0.00%	16.64%
0	1	36.83%	0.00%	0.00%	50.21%	0.00%	0.00%	12.95%
0	2	36.79%	0.00%	0.00%	50.20%	0.00%	0.00%	13.01%
0	3	36.74%	0.00%	0.00%	50.34%	0.00%	0.00%	12.92%
0	4	36.76%	0.00%	0.00%	50.40%	0.00%	0.00%	12.84%
0	5	36.77%	0.00%	0.00%	50.14%	0.00%	0.00%	13.09%
0	6	36.80%	0.00%	0.00%	50.27%	0.00%	0.00%	12.93%
0	7	36.73%	0.00%	0.00%	50.29%	0.00%	0.00%	12.98%
0	8	36.78%	0.00%	0.00%	50.40%	0.00%	0.00%	12.82%
0	9	36.82%	0.00%	0.00%	50.26%	0.00%	0.00%	12.91%
0	10	36.68%	0.00%	0.00%	50.27%	0.00%	0.00%	13.05%
0	11	36.86%	0.00%	0.00%	50.21%	0.00%	0.00%	12.93%
CMG 0 total		36.77%	0.00%	0.00%	49.97%	0.00%	0.00%	13.26%

FLOPS Process	Thread	Double precision floating-point operation	Single precision floating-point operation	Half precision floating-point operation	GFLOPS by Active element rate
0	0	8.77.E+08	0.00.E+00	0.00.E+00	7.36
0	1	8.77.E+08	0.00.E+00	0.00.E+00	7.37
0	2	8.77.E+08	0.00.E+00	0.00.E+00	7.37
0	3	8.77.E+08	0.00.E+00	0.00.E+00	7.36
0	4	8.77.E+08	0.00.E+00	0.00.E+00	7.37
0	5	8.77.E+08	0.00.E+00	0.00.E+00	7.36
0	6	8.77.E+08	0.00.E+00	0.00.E+00	7.36
0	7	8.77.E+08	0.00.E+00	0.00.E+00	7.36
0	8	8.77.E+08	0.00.E+00	0.00.E+00	7.37
0	9	8.77.E+08	0.00.E+00	0.00.E+00	7.36
0	10	8.77.E+08	0.00.E+00	0.00.E+00	7.36
0	11	8.77.E+08	0.00.E+00	0.00.E+00	7.36
CMG 0 total		1.05.E+10	0.00.E+00	0.00.E+00	88.37

Extra Process	Thread	Gather instruction rate (%): 0 flow rate (%)	1 flow rate (%)	2 flows rate (%)	Instruction: Micro-operation instruction	Element manipulated instruction	Register manipulated instruction	MOVPRFX instruction	Math functional instruction	Micro decomposition instruction rate (%)	Branch prediction miss rate (%)
0	0	0.00%	25.00%	75.00%	1.74E+08	1.54E+07	1.30E+01	0.00E+00	0.00E+00	99.99%	0.18%
0	1	0.00%	25.00%	75.00%	1.74E+08	1.54E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.45%
0	2	0.00%	25.00%	75.00%	1.74E+08	1.54E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.37%
0	3	0.00%	25.00%	75.00%	1.74E+08	1.54E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.44%
0	4	0.00%	25.00%	75.00%	1.74E+08	1.54E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.30%
0	5	0.00%	25.00%	75.00%	1.74E+08	1.54E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.30%
0	6	0.00%	25.00%	75.00%	1.74E+08	1.54E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.19%
0	7	0.00%	25.00%	75.00%	1.74E+08	1.54E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.96%
0	8	0.00%	25.00%	75.00%	1.74E+08	1.54E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.33%
0	9	0.00%	25.00%	75.00%	1.74E+08	1.54E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.29%
0	10	0.00%	25.00%	75.00%	1.74E+08	1.54E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.25%
0	11	0.00%	25.00%	75.00%	1.74E+08	1.54E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.90%
CMG 0 total		0.00%	25.00%	75.00%	2.09E+09	1.84E+08	2.40E+01	0.00E+00	0.00E+00	99.99%	0.41%
CMG 0 total		25.00%			2.09E+09	1.84E+08		0.00E+00	0.00E+00	99.99%	0.41%



Data Transfer CMGs		Destination (GB/s): Own memory	Other memory	Tofu	PCI
CMG 0 total	read	6.31E+01	1.85E-03	0.00E+00	0.00E+00
	write	7.22E+00	8.28E-03	0.00E+00	0.00E+00