## **CPU Performance Analysis Report 4.2.1**

Measured time	Sat Jul 9 20:47:10 2022
Node name	e28-6209c

Process no.	0
CMG no.	0
leasured region	axhelm_kernel, 1

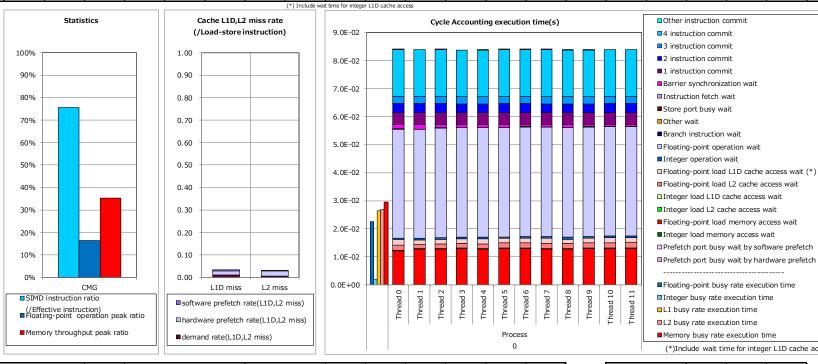
/ector length (bit)	512
CPU frequency (GHz)	1.999

s	Statis	stics	Execution time (s)	GFLOPS	Floating- point operation peak ratio (%)	Memory throughput (GB/s)	Memory throughput peak ratio (%)	Effective instruction	Floating- point operation	SIMD instruction rate (%) (/Effective instruction)	SVE operation rate (%)	point pipeline Active element rate	IPC	GIPS
Proces	SS	Thread												
	0	0	8.39E-02	10.45	16.34%	7.47		1.70E+08	8.77E+08	75.51%	100.00%	92.67%	1.01	2.03
	0	1	8.38E-02	10.46	16.35%	7.54	I	1.70E+08	8.77E+08	75.53%	100.00%	92.66%	1.01	2.03
	0	2	8.39E-02	10.45	16.34%	7.41	I	1.70E+08	8.77E+08	75.53%	100.00%	92.75%	1.01	2.03
	0	3	8.38E-02	10.46	16.35%	7.44	I	1.70E+08	8.77E+08	75.53%	100.00%	92.67%	1.01	2.03
	0	4	8.38E-02	10.46	16.35%	7.58	Ī	1.70E+08	8.77E+08	75.53%	100.00%	92.75%	1.01	2.03
	0	5	8.39E-02	10.45	16.34%	7.52	35.26%	1.70E+08	8.77E+08	75.53%	100.00%	92.75%	1.01	2.03
	0	6	8.39E-02	10.45	16.34%	7.56	35.26%	1.70E+08	8.77E+08	75.53%	100.00%	92.74%	1.01	2.03
	0	7	8.39E-02	10.45	16.34%	7.50	Ĩ	1.70E+08	8.77E+08	75.53%	100.00%	92.69%	1.01	2.03
	0	8	8.38E-02	10.46	16.35%	7.46	Ĩ	1.70E+08	8.77E+08	75.53%	100.00%	92.75%	1.01	2.03
	0	9	8.38E-02	10.46	16.34%	7.50	Ī	1.70E+08	8.77E+08	75.53%	100.00%	92.74%	1.01	2.03
	0	10	8.39E-02	10.45	16.34%	7.63	Ī	1.70E+08	8.77E+08	75.53%	100.00%	92.68%	1.01	2.03
	0	11	8.39E-02	10.45	16.34%	7.65	Ī	1.70E+08	8.77E+08	75.53%	100.00%	92.58%	1.01	2.03
		CMG 0 total	8.38E-02	125.45	16.34%	90.28	35.26%	2.04E+09	1.05E+10	75.53%	100.00%	92.70%	1.01	24.33

		Prefetch po	ort busy wait		Memo	ry access wait	& Cache acces	s wait		Operati	on wait	Othe	r wait	ſ					Other instruc	ction commit	ı	
Cycle Ad	ccounting	Prefetch port	Prefetch port	Integer load	Floating- point load memory access wait		Integer load	Floating- point load L2 cache access wait	point load L1D cache access wait	Integer operation wait	Floating- point operation wait	Branch instruction wait	Other wait	Store port busy wait	Instruction fetch wait	Barrier synchronizati on wait	1 instruction commit	2 instruction commit	3 instruction commit		Other instruction commit	Total
Process	Thread																					
(	0	0.00E+00	0.00E+00	9.44E-05	1.21E-02	5.19E-05	1.84E-04	1.70E-03	2.05E-03	5.19E-04	3.89E-02	2.48E-06	1.65E-05	0.00E+00	1.06E-04	1.50E-03	4.23E-03	3.20E-03	2.53E-03	1.67E-02	2.80E-06	8.39E-02
(	1	0.00E+00	0.00E+00	7.03E-05	1.26E-02	5.91E-05	1.42E-04	1.41E-03	1.63E-03	5.98E-04	3.90E-02	2.32E-06	4.75E-06	0.00E+00	1.86E-05	1.51E-03	4.30E-03	3.21E-03	2.57E-03	1.67E-02	0.00E+00	8.38E-02
(	) 2	0.00E+00	0.00E+00	5.84E-05	1.28E-02	5.65E-05	1.48E-04	1.62E-03	1.59E-03	5.70E-04	3.91E-02	2.23E-06	1.31E-05	0.00E+00	1.29E-04	1.04E-03	4.33E-03	3.18E-03	2.58E-03	1.67E-02	9.55E-08	8.39E-02
(	3	0.00E+00	0.00E+00	5.92E-05	1.29E-02	5.53E-05	1.42E-04	1.60E-03	1.71E-03	4.69E-04	3.91E-02	2.05E-06	1.31E-05	0.00E+00	1.27E-04	9.35E-04	4.30E-03	3.17E-03	2.58E-03	1.66E-02	0.00E+00	8.38E-02
(	4	1.65E-08	0.00E+00	6.09E-05	1.27E-02	5.79E-05	1.44E-04	1.66E-03	1.66E-03	6.18E-04	3.91E-02	2.11E-06	4.25E-06	0.00E+00	1.75E-05	1.04E-03	4.29E-03	3.19E-03	2.59E-03	1.66E-02	1.27E-05	8.38E-02
(	5	3.50E-09	0.00E+00	6.26E-05	1.28E-02	5.72E-05	1.51E-04	1.83E-03	1.64E-03	4.48E-04	3.91E-02	2.21E-06	4.71E-06	0.00E+00	1.81E-05	1.03E-03	4.29E-03	3.18E-03	2.58E-03	1.67E-02	2.01E-06	8.39E-02
(	6	0.00E+00	0.00E+00	6.27E-05	1.29E-02	5.86E-05	1.50E-04	1.76E-03	1.69E-03	6.67E-04	3.91E-02	2.27E-06	1.24E-05	0.00E+00	1.28E-04	6.56E-04	4.33E-03	3.18E-03	2.60E-03	1.66E-02	2.34E-06	8.39E-02
(	7	0.00E+00	0.00E+00	5.79E-05	1.27E-02	5.87E-05	1.43E-04	1.80E-03	1.92E-03	4.63E-04	3.91E-02	2.12E-06	6.93E-06	0.00E+00	5.49E-05	7.66E-04	4.30E-03	3.18E-03	2.62E-03	1.66E-02	1.37E-06	8.39E-02
(	8	0.00E+00	0.00E+00	5.79E-05	1.27E-02	5.94E-05	1.39E-04	1.83E-03	1.45E-03	7.99E-04	3.91E-02	2.25E-06	4.43E-06	0.00E+00	1.70E-05	1.01E-03	4.32E-03	3.17E-03	2.59E-03	1.66E-02	9.10E-06	8.38E-02
(	) 9	0.00E+00	0.00E+00	6.06E-05	1.28E-02	5.86E-05	1.49E-04	1.89E-03	1.64E-03	7.07E-04	3.91E-02	2.28E-06	4.56E-06	0.00E+00	2.19E-05	6.62E-04	4.29E-03	3.17E-03	2.58E-03	1.67E-02	1.64E-05	8.38E-02
(	10	1.10E-08	0.00E+00	6.23E-05	1.28E-02	6.05E-05	1.49E-04	1.99E-03	1.71E-03	6.93E-04	3.90E-02	2.16E-06	4.16E-06	0.00E+00	1.83E-05	5.93E-04	4.31E-03	3.21E-03	2.57E-03	1.67E-02	0.00E+00	8.39E-02
(	11	0.00E+00	0.00E+00	5.99E-05	1.29E-02	5.72E-05	1.47E-04	1.95E-03	1.74E-03	5.27E-04	3.91E-02	1.96E-06	4.55E-06	0.00E+00	1.65E-05	5.53E-04	4.31E-03	3.21E-03	2.56E-03	1.67E-02	0.00E+00	8.39E-02
	CMG 0 total	2.58F-09	0.00F+00	6 39F-05	1.27F-02	5.76F-05	1.49F-04	1.75F-03	1.70F-03	5.90F-04	3.91F-02	2.20F-06	7.79F-06	0.00F+00	5.61F-05	9.42F-04	4.30F-03	3.19E-03	2.58F-03	1.67F-02	3.91F-06	8.38F-02

Bu	,	point operation pipeline A busy rate (%)	point point operation pipeline B busy rate (%)	Integer operation pipeline A busy rate (%)	Integer operation pipeline B busy rate (%)	L1 busy rate (%)	L2 busy rate (%)	Memory busy rate (%)	Address calculation operation pipeline A busy rate	Address calculation operation pipeline B busy rate	Floating- point pipeline A Active element rate (%)	Floating- point pipeline B Active element rate (%)	L1 pipeline 0 Active element rate (%)	L1 pipeline 1 Active element rate (%)	SFI(Store Fetch Interlock) rate
Process	Thread								(%)	(%)					
0	0	32.31%	21.70%	1.46%	3.20%	31.76%			27.36%	20.21%	87.75%	100.00%	100.00%	100.00%	0.02
0	1	32.34%	21.71%	1.42%	3.23%	31.47%			27.17%	20.01%	87.73%	100.00%	100.00%	100.00%	0.01
0	2	32.28%	21.70%	1.43%	3.23%	31.48%			27.16%	20.01%	87.87%	100.00%	100.00%	100.00%	0.02
0	3	32.35%	21.66%	1.43%	3.21%	31.54%			27.20%	20.00%	87.77%	100.00%	100.00%	100.00%	0.01
0	4	32.27%	21.71%	1.45%	3.27%	31.44%			27.33%	20.09%	87.87%	100.00%	100.00%	100.00%	0.01
0	5	32.29%	21.70%	1.46%	3.27%	31.46%	31.94%	35.26%	27.30%	20.13%	87.87%	100.00%	100.00%	100.00%	0.01
0	6	32.32%	21.67%	1.43%	3.23%	31.48%	31.5470	33.20%	27.15%	20.02%	87.88%	100.00%	100.00%	100.00%	0.01
0	7	32.34%	21.66%	1.42%	3.21%	31.56%			27.19%	19.98%	87.79%	100.00%	100.00%	100.00%	0.02
0	8	32.30%	21.69%	1.40%	3.20%	31.50%			27.19%	20.02%	87.87%	100.00%	100.00%	100.00%	0.02
0	9	32.30%	21.67%	1.45%	3.27%	31.49%			27.42%	20.13%	87.88%	100.00%	100.00%	100.00%	0.02
0	10	32.35%	21.68%	1.42%	3.21%	31.56%			27.22%	20.00%	87.77%	100.00%	100.00%	100.00%	0.01
0	11	32.36%	21.72%	1.45%	3.29%	31.71%			27.39%	20.12%	87.60%	100.00%	100.00%	100.00%	0.02
	CMG 0 total	32.32%	21.69%	1.43%	3.24%	31.54%	31.94%	35.26%	27.26%	20.06%	87.80%	100.00%	100.00%	100.00%	0.02

U	10	32.35%	21.00%	1.42%	3.2170	31.50%		l L	27.2270	20.00%	07.//70	100.00%	100.00%	100.00%	0.01
0	11	32.36%	21.72%	1.45%	3.29%	31.71%			27.39%	20.12%	87.60%	100.00%	100.00%	100.00%	0.02
	CMG 0 total	32.32%	21.69%	1.43%	3.24%	31.54%	31.94%	35.26%	27.26%	20.06%	87.80%	100.00%	100.00%	100.00%	0.02
Cad	che	L1I miss rate (/Effective instruction)	Load-store instruction	L1D miss	L1D miss rate (/Load- store instruction)	L1D miss demand rate (%) (/L1D miss)	L1D miss hardware prefetch rate (%) (/L1D miss)	L1D miss software prefetch rate (%) (/L1D miss)	L2 miss	L2 miss rate (/Load-store instruction)	L2 miss demand rate (%) (/L2 miss)	L2 miss hardware prefetch rate (%) (/L2 miss)	L2 miss software prefetch rate (%) (/L2 miss)	rate (/Load-	L2D TLB miss rate (/Load- store instruction)
Process	Thread														
0	0	0.00	7.07E+07	2.41E+06	0.03		59.90%	10.80%	2.22E+06				6.77%		
0	1	0.00	7.07E+07	2.37E+06	0.03	29.19%	59.45%	11.36%	2.21E+06				7.69%		
0	2	0.00	7.07E+07	2.37E+06	0.03	29.41%	59.25%	11.34%	2.21E+06				7.67%		
0	3	0.00	7.07E+07	2.37E+06	0.03	29.47%	59.18%	11.34%	2.21E+06				7.64%		
0	4	0.00	7.07E+07	2.37E+06	0.03	29.22%	59.43%	11.35%	2.21E+06	0.03	15.16%	77.02%	7.82%	0.00000	0.00000
0	5	0.00	7.07E+07	2.37E+06	0.03	29.27%	59.52%	11.21%	2.21E+06	0.03	15.26%	76.90%	7.84%	0.00000	0.00000
0	6	0.00	7.07E+07	2.37E+06	0.03	29.22%	59.44%	11.34%	2.21E+06	0.03	15.22%	76.99%	7.78%	0.00000	0.00000
0	7	0.00	7.07E+07	2.37E+06	0.03	29.31%	59.37%	11.32%	2.20E+06	0.03	15.40%	76.78%	7.83%	0.00000	0.00000
0	8	0.00	7.07E+07	2.37E+06	0.03	29.27%	59.40%	11.33%	2.21E+06	0.03	15.26%	76.86%	7.87%	0.00000	0.00000
0	9	0.00	7.07E+07	2.37E+06	0.03	29.26%	59.37%	11.37%	2.21E+06	0.03	15.07%	77.02%	7.91%	0.00000	0.00000
0	10	0.00	7.07E+07	2.37E+06	0.03	29.26%	59.19%	11.55%	2.20E+06	0.03	15.35%	76.84%	7.81%	0.00000	0.00000
0	11	0.00	7.07E+07	2.37E+06	0.03	29.38%	59.31%	11.30%	2.21E+06	0.03	15.50%	76.65%	7.85%	0.00000	0.00000
	CMG 0 total	0.00	8.48E+08	2.85E+07	0.03	29.30%	59.40%	11.30%	2.65E+07	0.03	15.48%	76.81%	7.71%	0.00000	0.00000



								Load-store	instruction							Pre	fetch instructi	ion		Floati	ing-point instr	uction	Floating-poi	nt move and			ĺ			
					Load ins	struction						Store ins	truction										conversion	instruction						
					SIMD				Non-SIMD			SIMD			Non-SIMD					Floating-										
Instru	tion	Single vector contiguous	vector contiguous	Non- contiguous	Broadcast	Floating- point	Predicate	First-fault	Non-SIMD	Single vector contiguous	vector contiguous	Non- contiguous	Floating- point	Predicate	Non-SIMD	Contiguous prefetch	Gathering prefetch	Scalar prefetch	DCZVA instruction	point instruction except FMA	FMA instruction	Floating- point reciprocal	Floating- point	Floating- point move	Integer instruction	Branch instruction	Predicate instruction	Crypto- graphic instruction	Other instruction	Total
		load instruction	structure	gather load instruction	load instruction	register fill instruction	register fill instruction	load instruction	load instruction	store	structure store		register spill instruction	register spill instruction	store instruction	instruction	instruction	instruction		and reciprocal		instruction	conversion instruction	instruction						
Process	Thread		instruction				ļ		_		instruction																			
0	0	1.94E+07	0.00E+00	0.00E+00	3.59E+06	1.55E+07	1.70E+01	0.00E+00	2.51E+07	5.12E+06	0.00E+00	0.00E+00	8.70E+05	0.00E+00	1.17E+06	0.00E+00	0.00E+00	1.41E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	2.36E+07	0.00E+00	2.89E+05	1.01E+02	0.00E+00	1.37E+07	1.70E+08
0	1	1.94E+07	0.00E+00	0.00E+00	3.59E+06	1.55E+07	1.70E+01	0.00E+00	2.51E+07	5.12E+06	0.00E+00	0.00E+00	8.70E+05	0.00E+00	1.15E+06	0.00E+00	0.00E+00	1.41E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	2.36E+07	0.00E+00	2.85E+05	1.01E+02	0.00E+00	1.37E+07	1.70E+08
0	2	1.94E+07	0.00E+00	0.00E+00	3.59E+06	1.55E+07	1.70E+01	0.00E+00	2.51E+07	5.12E+06	0.00E+00	0.00E+00	8.70E+05	0.00E+00	1.15E+06		0.00E+00	1.41E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	2.36E+07	0.00E+00	2.85E+05	1.01E+02	0.00E+00	1.37E+07	1.70E+08
0	3	1.94E+07	0.00E+00	0.00E+00	3.59E+06	1.55E+07	1.70E+01	0.00E+00	2.51E+07	5.12E+06	0.00E+00	0.00E+00	8.70E+05	0.00E+00	1.15E+06	0.00E+00	0.00E+00	1.41E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	2.36E+07	0.00E+00	2.85E+05	1.01E+02	0.00E+00	1.37E+07	1.70E+08
0	4	1.94E+07	0.00E+00	0.00E+00	3.59E+06	1.55E+07	1.70E+01	0.00E+00	2.51E+07	5.12E+06	0.00E+00	0.00E+00	8.70E+05	0.00E+00	1.15E+06	0.00E+00	0.00E+00	1.41E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	2.36E+07	0.00E+00	2.85E+05	1.01E+02	0.00E+00	1.37E+07	1.70E+08
0	5	1.94E+07	0.00E+00	0.00E+00	3.59E+06	1.55E+07	1.70E+01	0.00E+00	2.51E+07	5.12E+06	0.00E+00	0.00E+00	8.70E+05	0.00E+00	1.15E+06	0.00E+00	0.00E+00	1.41E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	2.36E+07	0.00E+00	2.85E+05	1.01E+02	0.00E+00	1.37E+07	1.70E+08
0	6	1.94E+07	0.00E+00	0.00E+00	3.59E+06	1.55E+07	1.70E+01	0.00E+00	2.51E+07	5.12E+06	0.00E+00	0.00E+00	8.70E+05	0.00E+00	1.15E+06	0.00E+00	0.00E+00	1.41E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	2.36E+07	0.00E+00	2.85E+05	1.01E+02	0.00E+00	1.37E+07	1.70E+08
0	7	1.94E+07	0.00E+00	0.00E+00	3.59E+06	1.55E+07	1.70E+01	0.00E+00	2.51E+07	5.12E+06	0.00E+00	0.00E+00	8.70E+05	0.00E+00	1.15E+06	0.00E+00	0.00E+00	1.41E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	2.36E+07	0.00E+00	2.85E+05	1.01E+02	0.00E+00	1.37E+07	1.70E+08
0	8	1.94E+07	0.00E+00	0.00E+00	3.59E+06	1.55E+07	1.70E+01	0.00E+00	2.51E+07	5.12E+06	0.00E+00	0.00E+00	8.70E+05	0.00E+00	1.15E+06	0.00E+00	0.00E+00	1.41E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	2.36E+07	0.00E+00	2.85E+05	1.01E+02	0.00E+00	1.37E+07	1.70E+08
0	9	1.94E+07	0.00E+00	0.00E+00	3.59E+06	1.55E+07	1.70E+01	0.00E+00	2.51E+07	5.12E+06	0.00E+00	0.00E+00	8.70E+05	0.00E+00	1.15E+06	0.00E+00	0.00E+00	1.41E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	2.36E+07	0.00E+00	2.85E+05	1.01E+02	0.00E+00	1.37E+07	1.70E+08
0	10	1.94E+07	0.00E+00	0.00E+00	3.59E+06	1.55E+07	1.70E+01	0.00E+00	2.51E+07	5.12E+06	0.00E+00	0.00E+00	8.70E+05	0.00E+00	1.15E+06	0.00E+00	0.00E+00	1.41E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	2.36E+07	0.00E+00	2.85E+05	1.01E+02	0.00E+00	1.37E+07	1.70E+08
0	11	1.94E+07	0.00E+00	0.00E+00	3.59E+06	1.55E+07	1.70E+01	0.00E+00	2.51E+07	5.12E+06	0.00E+00	0.00E+00	8.70E+05	0.00E+00	1.15E+06	0.00E+00	0.00E+00	1.41E+06	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	2.36E+07	0.00E+00	2.85E+05	1.01E+02	0.00E+00	1.37E+07	1.70E+08
	CMG 0 total	2.32E+08	0.00E+00	0.00E+00	4.31E+07	1.86E+08	2.04E+02	0.00E+00	3.01E+08	6.14E+07	0.00E+00	0.00E+00	1.04E+07	0.00E+00	1.38E+07	0.00E+00	0.00E+00	1.69E+07	9.60E+01	1.35E+08	5.90E+08	0.00E+00	0.00E+00	2.83E+08	0.00E+00	3.42E+06	1.21E+03	0.00E+00	1.64E+08	2.04E+09
	Crid 0 total							8.48	E+08	,	•	,	•	•			1.69E+07	, and the second	9.60E+01		7.25E+08	•	2.83	E+08	0.00E+00	3.42E+06	1.21E+03	0.00E+00	1.64E+08	2.04E+09

Power Con (V		Power consumption used by core	Power consumption used by L2 cache	Power consumption used by memory		
Process	Thread					
0	0	1.83E+00				
0	1	1.83E+00				
0	2	1.82E+00				
0	3	1.83E+00				
0	4	1.83E+00				
0	5	1.82E+00	2.31E+00	7.31E+00		
0	6	1.82E+00	2.31E+00	7.31E+00		
0	7	1.82E+00				
0	8	1.83E+00				
0	9	1.83E+00				
0	10	1.82E+00				
0	11	1.83E+00				
	CMG 0 total	2.19E+01	2.31E+00	7.31E+00		

■1 instruction commit

 $\blacksquare \\ Instruction fetch wait$ ■Store port busy wait

■ Barrier synchronization wait

■Floating-point load L1D cache access wait (\*)

□Prefetch port busy wait by software prefetch

(\*)Include wait time for integer L1D cache access

Hardware	Prefetch		L1			L2		L1/L2
Rate (/Hard Prefe	dware etch)	Stream mode prefetch rate	Injection mode allocate prefetch rate	Injection mode unallocate prefetch rate	Stream mode prefetch rate	Injection mode allocate prefetch rate	Injection mode unallocate prefetch rate	Other hardware prefetch
Process	Thread							
0	0	36.85%	0.00%	0.00%	54.93%	0.00%	0.00%	8.22%
0	1	35.64%	0.00%	0.00%	56.85%	0.00%	0.00%	7.51%
0	2	35.74%	0.00%	0.00%	56.66%	0.00%	0.00%	7.60%
0	3	35.86%	0.00%	0.00%	56.72%	0.00%	0.00%	7.42%
0	4	36.12%	0.00%	0.00%	56.51%	0.00%	0.00%	7.37%
0	5	36.24%	0.00%	0.00%	56.31%	0.00%	0.00%	7.46%
0	6	36.38%	0.00%	0.00%	56.20%	0.00%	0.00%	7.43%
0	7	36.45%	0.00%	0.00%	56.13%	0.00%	0.00%	7.43%
0	8	36.25%	0.00%	0.00%	56.24%	0.00%	0.00%	7.51%
0	9	36.37%	0.00%	0.00%	56.25%	0.00%	0.00%	7.38%
0	10	36.05%	0.00%	0.00%	56.56%	0.00%	0.00%	7.39%
0	11	36.30%	0.00%	0.00%	56.26%	0.00%	0.00%	7.45%
	CMG 0 total	36.19%	0.00%	0.00%	56.30%	0.00%	0.00%	7.51%

FLC	OPS	Double precision floating- point operation	Single precision floating- point operation	Half precision floating- point operation	GFLOPS by Active element rate
Process	Thread	-			
0	0	8.77.E+08	0.00.E+00	0.00.E+00	9.69
0	1	8.77.E+08	0.00.E+00	0.00.E+00	9.69
0	2	8.77.E+08	0.00.E+00	0.00.E+00	9.69
0	3	8.77.E+08	0.00.E+00	0.00.E+00	9.69
0	4	8.77.E+08	0.00.E+00	0.00.E+00	9.70
0	5	8.77.E+08	0.00.E+00	0.00.E+00	9.69
0	6	8.77.E+08	0.00.E+00	0.00.E+00	9.69
0	7	8.77.E+08	0.00.E+00	0.00.E+00	9.69
0	8	8.77.E+08	0.00.E+00	0.00.E+00	9.70
0	9	8.77.E+08	0.00.E+00	0.00.E+00	9.70
0	10	8.77.E+08	0.00.E+00	0.00.E+00	9.69
0	11	8.77.E+08	0.00.E+00	0.00.E+00	9.68
	CMG 0 total	1.05.E+10	0.00.E+00	0.00.E+00	116.30

		Gather	r instruction ra	te (%)			Instru	uction			
Ext	ra Thread	0 flow rate (%)	1 flow rate (%)	2 flows rate (%)	Micro- operation instruction	Element manipulated instruction	Register manipulated instruction	MOVPRFX instruction	Math functional instruction	Micro decompositio n instruction rate (%)	Branch prediction miss rate (%)
0	0	0.00%	0.00%	0.00%	1.70E+08	2.36E+07	6.41E+03	0.00E+00	0.00E+00	99,99%	0.19%
0	1	0.00%	0.00%		1.70E+08	2.36E+07	1.00E+00		0.00E+00		0.28%
0	2	0.00%	0.00%	0.00%	1.70E+08	2.36E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.21%
0	3	0.00%	0.00%	0.00%	1.70E+08	2.36E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.20%
0	4	0.00%	0.00%	0.00%	1.70E+08	2.36E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.18%
0	5	0.00%	0.00%	0.00%	1.70E+08	2.36E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.20%
0	6	0.00%	0.00%	0.00%	1.70E+08	2.36E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.19%
0	7	0.00%	0.00%	0.00%	1.70E+08	2.36E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.18%
0	8	0.00%	0.00%	0.00%	1.70E+08	2.36E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.20%
0	9	0.00%	0.00%	0.00%	1.70E+08	2.36E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.19%
0	10	0.00%	0.00%	0.00%	1.70E+08	2.36E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.17%
0	11	0.00%	0.00%	0.00%	1.70E+08	2.36E+07	1.00E+00	0.00E+00	0.00E+00	99.99%	0.17%
	CMG 0 total	0.00%	0.00%	0.00%	2.04E+09	2.83E+08	6.42E+03	0.00E+00	0.00E+00	99.99%	0.19%
	Cirio 0 total		0.00%		2.04E+09	2.83	E+08	0.00E+00	0.00E+00	99.99%	0.19%

(W)	Power Consumption				
3.5E+01					
3.0E+01					
2.5E+01		used by memory			
		used by L2 cache			
2.0E+01	_	■used by core			
1.5E+01					
1.0E+01	_				
5.0E+00					
0.0E+00 +					
	1 CMG				

Data Transfer CMGs		Destination (GB/s)				
		Own memory	Other memory	Tofu	PCI	
CMG 0 total	read	8.09E+01	3.27E-03	0.00E+00	0.00E+00	
	write	9.41E+00	4.38E-03	0.00E+00	0.00E+00	