## CPU Performance Analysis Report 4.2.1

Measured time	Sat Jul 9 20:47:09 2022
Node name	k28-5009c

Process no.	0
CMG no.	0
Measured region	axhelm_kernel, 1

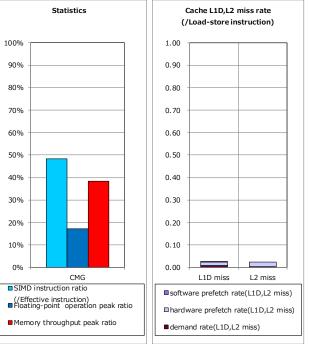
Vector length (bit)	512
CPU frequency (GHz)	1.999

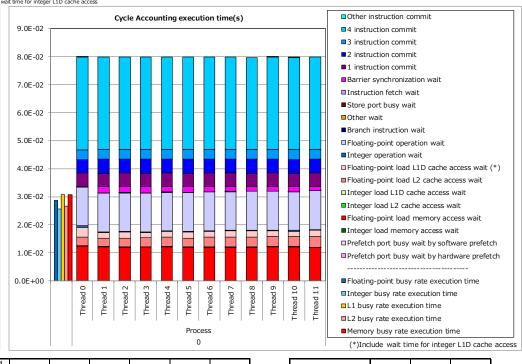
Sta	atistics	Execution time (s)	GFLOPS	Floating- point operation peak ratio (%)	Memory throughput (GB/s)	Memory throughput peak ratio (%)	Effective instruction	Floating- point operation	SIMD instruction rate (%) (/Effective instruction)	SVE operation rate (%)	point pipeline Active element rate	IPC	GIPS
Process	Thread												
	0	7.98E-02	10.98	17.17%	8.35		3.20E+08	8.77E+08	48.40%	100.00%	95.54%	2.01	4.0
	0	7.98E-02	10.98	17.17%	8.14	I	3.20E+08	8.77E+08	48.40%	100.00%	95.55%	2.01	4.0
	0	7.98E-02	10.98	17.17%	8.10	I	3.20E+08	8.77E+08	48.40%	100.00%	95.55%	2.01	4.0
	0	7.98E-02	10.98	17.17%	8.21	I	3.20E+08	8.77E+08	48.40%	100.00%	95.55%	2.01	4.0
	0	4 7.98E-02	10.99	17.18%	8.34	Ī	3.20E+08	8.77E+08	48.40%	100.00%	95.55%	2.01	4.0
	0	7.98E-02	10.98	17.17%	8.16	38,50%	3.20E+08	8.77E+08	48.40%	100.00%	95.55%	2.01	4.0
	0	6 7.98E-02	10.99	17.18%	8.19	36.50%	3.20E+08	8.77E+08	48.40%	100.00%	95.55%	2.01	4.0
	0	7.98E-02	10.99	17.18%	8.19	Ĩ	3.20E+08	8.77E+08	48.40%	100.00%	95.55%	2.01	4.0
	0	7.97E-02	10.99	17.18%	8.16	Ĩ	3.20E+08	8.77E+08	48.40%	100.00%	95.55%	2.01	4.0
	0	9 7.98E-02	10.98	17.17%	8.22	Ī	3.20E+08	8.77E+08	48.40%	100.00%	95.55%	2.01	4.0
	0 1	7.98E-02	10.99	17.18%	8.36	Ī	3.20E+08	8.77E+08	48.40%	100.00%	95.55%	2.01	4.0
	0 1	7.98E-02	10.98	17.17%	8.17	Ī	3.20E+08	8.77E+08	48.40%	100.00%	95.55%	2.01	4.0
	CMG 0 tota	7.98E-02	131.85	17.17%	98.57	38.50%	3.84E+09	1.05E+10	48.40%	100.00%	95.55%	2.01	48.2

		Prefetch po	ort busy wait		Memo	ory access wait	& Cache acces	s wait		Operati	on wait	Other	r wait						Other instruc	ction commit		
Cycle Ac	counting	Prefetch port busy wait by hardware prefetch	Prefetch port busy wait by software prefetch	Integer load memory access wait	Floating- point load memory access wait	Integer load L2 cache access wait	Integer load L1D cache access wait	Floating- point load L2 cache access wait	point load L1D cache access wait	Integer operation wait	Floating- point operation wait	Branch instruction wait	Other wait	Store port busy wait	Instruction fetch wait	Barrier synchronizati on wait	1 instruction commit	2 instruction commit	3 instruction commit	4 instruction commit	Other instruction commit	Total
Process	Thread																					
0	C	2.25E-08	0.00E+00	9.01E-05	1.23E-02	4.87E-05	7.87E-05	2.98E-03	3.45E-03	6.97E-04	1.37E-02	1.03E-05	2.31E-05	0.00E+00	1.12E-04	7.51E-05	4.85E-03	4.83E-03	3.51E-03	3.30E-02	2.74E-06	7.98E-02
0	1	0.00E+00	0.00E+00	6.48E-05	1.21E-02	5.56E-05	5.62E-05	2.92E-03	2.12E-03	2.54E-04	1.38E-02	1.16E-05	1.33E-05	0.00E+00	7.12E-05	2.22E-03	4.80E-03	4.95E-03	3.54E-03	3.29E-02	0.00E+00	7.98E-02
0	2	0.00E+00	0.00E+00	6.50E-05	1.19E-02	5.28E-05	6.03E-05	3.04E-03	2.25E-03	2.32E-04	1.38E-02	1.22E-05	1.14E-05	0.00E+00	6.13E-05	2.18E-03	4.78E-03	4.95E-03	3.55E-03	3.29E-02	0.00E+00	7.98E-02
0	3	4.90E-09	0.00E+00	6.01E-05	1.20E-02	5.58E-05	5.86E-05	3.13E-03	1.98E-03	2.32E-04	1.38E-02	1.11E-05	1.28E-05	0.00E+00	6.57E-05	2.22E-03	4.79E-03	4.94E-03	3.56E-03	3.29E-02	0.00E+00	7.98E-02
0	4	6.36E-09	0.00E+00	5.69E-05	1.21E-02	5.41E-05	6.26E-05	3.29E-03	2.05E-03	2.42E-04	1.38E-02	1.16E-05	1.39E-05	0.00E+00	7.57E-05	1.90E-03	4.79E-03	4.93E-03	3.55E-03	3.29E-02	0.00E+00	7.98E-02
0	5	0.00E+00	0.00E+00	5.76E-05	1.19E-02	5.35E-05	5.82E-05	3.18E-03	2.21E-03	2.50E-04	1.38E-02	1.20E-05	1.21E-05	0.00E+00	6.56E-05	2.00E-03	4.79E-03	4.95E-03	3.57E-03	3.29E-02	0.00E+00	7.98E-02
0	6	0.00E+00	0.00E+00	5.79E-05	1.19E-02	5.47E-05	5.95E-05	3.43E-03	2.12E-03	2.35E-04	1.38E-02	1.09E-05	1.36E-05	0.00E+00	6.58E-05	1.87E-03	4.78E-03	4.91E-03	3.56E-03	3.29E-02	0.00E+00	7.98E-02
0	7	0.00E+00	0.00E+00	5.76E-05	1.19E-02	5.47E-05	5.83E-05	3.47E-03	2.25E-03	2.12E-04	1.38E-02	1.03E-05	1.26E-05	0.00E+00	6.53E-05	1.76E-03	4.76E-03	4.91E-03	3.56E-03	3.29E-02	0.00E+00	7.98E-02
0	8	0.00E+00	0.00E+00	5.86E-05	1.19E-02	5.30E-05	6.31E-05	3.50E-03	2.30E-03	2.11E-04	1.38E-02	1.03E-05	1.47E-05	0.00E+00	7.09E-05	1.67E-03	4.77E-03	4.92E-03	3.56E-03	3.29E-02	0.00E+00	7.97E-02
0	g	9.31E-09	0.00E+00	5.42E-05	1.21E-02	5.81E-05	5.54E-05	3.52E-03	2.00E-03	2.18E-04	1.38E-02	9.36E-06	1.39E-05	0.00E+00	6.56E-05	1.68E-03	4.78E-03	4.92E-03	3.55E-03	3.29E-02	2.47E-06	7.98E-02
0	10	1.17E-08	0.00E+00	5.75E-05	1.21E-02	5.46E-05	5.81E-05	3.50E-03	1.97E-03	2.35E-04	1.38E-02	9.47E-06	1.54E-05	0.00E+00	7.09E-05	1.78E-03	4.77E-03	4.90E-03	3.55E-03	3.29E-02	8.63E-06	7.98E-02
0	11	2.01E-08	0.00E+00	5.80E-05	1.18E-02	5.49E-05	5.84E-05	3.73E-03	2.35E-03	2.20E-04	1.38E-02	1.03E-05	1.36E-05	0.00E+00	6.44E-05	1.47E-03	4.77E-03	4.91E-03	3.57E-03	3.29E-02	0.00E+00	7.98E-02
	CMG 0 total	6.24E-09	0.00E+00	6.15E-05	1.20E-02	5.42E-05	6.06E-05	3.31E-03	2.25E-03	2.70E-04	1.38E-02	1.08E-05	1.42E-05	0.00E+00	7.12E-05	1.74E-03	4.79E-03	4.92E-03	3.55E-03	3,29E-02	1.15E-06	7.98E-02

Bu	sy	point operation pipeline A busy rate (%)	point operation pipeline B busy rate (%)	Integer operation pipeline A busy rate (%)	Integer operation pipeline B busy rate (%)	L1 busy rate (%)	L2 busy rate (%)	Memory busy rate (%)	Address calculation operation pipeline A busy rate	Address calculation operation pipeline B busy rate		Floating- point pipeline B Active element rate	L1 pipeline 0 Active element rate (%)	L1 pipeline 1 Active element rate (%)	SFI(Store Fetch Interlock) rate
Process	Thread								(%)	(%)	(%)	(%)			
0	0	47.38%	24.57%	27.46%	36.68%	38.55%			40.25%	40.69%	93.22%	100.00%	100.00%	100.00%	0.00
0	1	47.42%	24.58%	27.62%	36.72%	38.42%			40.23%	40.61%	93.23%	100.00%	100.00%	100.00%	0.00
0	2	47.42%	24.57%	27.64%	36.73%	38.42%			40.21%	40.61%	93.24%	100.00%	100.00%	100.00%	0.00
0	3	47.43%	24.57%	27.60%	36.79%	38.43%			40.22%	40.62%	93.24%	100.00%	100.00%	100.00%	0.00
0	4	47.42%	24.57%	27.61%	36.77%	38.44%			40.16%	40.61%	93.24%	100.00%	100.00%	100.00%	0.00
0	5	47.42%	24.57%	27.63%	36.78%	38.44%	33.31%	38.50%	40.17%	40.61%	93.24%	100.00%	100.00%	100.00%	0.00
0	6	47.42%	24.56%	27.63%	36.83%	38.45%	33.31%	36.50%	40.13%	40.59%	93.23%	100.00%	100.00%	100.00%	0.00
0	7	47.45%	24.56%	27.63%	36.83%	38.47%			40.12%	40.62%	93.24%	100.00%	100.00%	100.00%	0.00
0	8	47.44%	24.55%	27.61%	36.82%	38.46%			40.11%	40.61%	93.24%	100.00%	100.00%	100.00%	0.00
0	9	47.45%	24.56%	27.64%	36.87%	38.47%			40.12%	40.59%	93.24%	100.00%	100.00%	100.00%	0.00
0	10	47.45%	24.56%	27.66%	36.86%	38.48%			40.14%	40.60%	93.24%	100.00%	100.00%	100.00%	0.00
0	11	47.46%	24.57%	27.59%	36.84%	38.48%			40.11%	40.61%	93.24%	100.00%	100.00%	100.00%	0.00
	CMG 0 total	47.43%	24.57%	27.61%	36.79%	38.46%	33.31%	38.50%	40.16%	40.61%	93.24%	100.00%	100.00%	100.00%	0.00

miss dware	L2 miss software	L1D TLB miss rate (/Load-	L2D TLB miss
100.00%	100.00%	100.00%	0.00
100.00%		100.00%	0.00
100.00%		100.00%	0.00
100.00%		100.00%	0.00
100.00%		100.00%	0.00
100.00% 100.00%	100.00% 100.00%	100.00% 100.00%	0.00
100.00%	100.00%	100.00%	0.00
100.00%	100.00%	100.00%	0.00
100.00%		100.00%	0.00
100.00%	100.00%	100.00%	0.00
100.00%		100.00%	0.00
100.00%	100,00%	100,00%	0.00





Process T	Thread 0						-	miss)			miss)	miss)	miss)	instruction)	instruction)
0	0														
0		0.00	9.47E+07	2.38E+06	0.03	29.45%	70.55%	0.00%	2.32E+06	0.02	15.40%	87.18%	0.00%	0.00000	0.00000
	1	0.00	9.47E+07	2.35E+06	0.02	29.22%	70.77%	0.01%	2.30E+06	0.02	15.04%	87.67%	0.00%	0.00000	0.00000
0	2	0.00	9.47E+07	2.35E+06	0.02	29.20%	70.79%	0.01%	2.30E+06	0.02	14.87%	87.65%	0.00%	0.00000	0.00000
0	3	0.00	9.47E+07	2.35E+06	0.02	29.13%	70.83%	0.04%	2.30E+06	0.02	14.98%	87.44%	0.00%	0.00000	0.00000
0	4	0.00	9.47E+07	2.35E+06	0.02	29.17%	70.81%	0.02%	2.30E+06	0.02	14.67%	87.70%	0.00%	0.00000	0.00000
0	5	0.00	9.47E+07	2.35E+06	0.02	29.20%	70.79%	0.01%	2.30E+06	0.02	14.98%	87.55%	0.00%	0.00000	0.00000
0	6	0.00	9.47E+07	2.35E+06	0.02	29.15%	70.83%	0.02%	2.30E+06	0.02	14.69%	87.71%	0.00%	0.00000	0.00000
0	7	0.00	9.47E+07	2.35E+06	0.02	29.14%	70.84%	0.03%	2.30E+06	0.02	14.89%	87.62%	0.00%	0.00000	0.00000
0	8	0.00	9.47E+07	2.35E+06	0.02	29.18%	70.83%	-0.01%	2.30E+06	0.02	14.87%	87.57%	0.00%	0.00000	0.00000
0	9	0.00	9.47E+07	2.35E+06	0.02	29.18%	70.83%	-0.01%	2.30E+06	0.02	14.99%	87.52%	0.00%	0.00000	0.00000
0	10	0.00	9.47E+07	2.35E+06	0.02	29.17%	70.80%	0.03%	2.30E+06	0.02	14.93%	87.54%	0.00%	0.00000	0.00000
0	11	0.00	9.47E+07	2.35E+06	0.02	29.19%	70.78%	0.02%	2.30E+06	0.02	14.91%	87.48%	0.00%	0.00000	0.00000
CN	MG 0 total	0.00	1.14E+09	2.83E+07	0.02	29.20%	70.79%	0.01%	2.76E+07	0.02	14.94%	87.55%	0.00%	0.00000	0.00000

								Load-store	instruction							Pret	fetch instructi	on		Floati	ng-point instru	ction	Floating-poin	t move and						
					Load ins	struction						Store ins	truction										conversion i	nstruction						
					SIMD				Non-SIMD			SIMD			Non-SIMD					Floating-										
Instru	tion	Single vector	vector	Non-	Broadcast	Floating-	Predicate	First-fault	Non-SIMD	Single vector	vector	Non-	Floating-	Predicate	Non-SIMD	Contiguous prefetch	Gathering prefetch	Scalar prefetch	DCZVA instruction	point instruction	FMA	Floating- point	Floating- point	Floating-	Integer instruction	Branch instruction	Predicate instruction	Crypto- graphic	Other instruction	Total
		contiguous	contiguous	contiguous	load	point	register fill	load	load		ontiguous	contiguous	point	register spill	store	instruction	instruction	instruction		except FMA	instruction	reciprocal	conversion	point move				instruction		
		load instruction	structure load	gather load instruction	instruction	register fill instruction	instruction	instruction	instruction	store instruction	structure store	scatter store instruction	register spill instruction	instruction	instruction					and reciprocal		instruction	instruction	instruction						
Process	Thread	instruction	instruction	instruction		instruction				instruction	netruction	instruction	instruction							recipiocai										
0	0	3.99E+07	0.00E+00	0.00E+00	0.00E+00	4.48E+05	1.70E+01	0.00E+00	4.92E+07	5.12E+06	0.00E+00	0.00E+00	1.20E+03	0.00E+00	3.02E+04	0.00E+00	0.00E+00	2.07E+02	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	4.92E+07	1.60E+04	1.11E+06	1.01E+02	0.00E+00	1.15E+08	3.20E+08
0	1	3.99E+07	0.00E+00	0.00E+00	0.00E+00	4.48E+05	1.70E+01	0.00E+00	4.92E+07	5.12E+06	0.00E+00	0.00E+00	1.20E+03	0.00E+00	1.33E+04	0.00E+00	0.00E+00	2.07E+02	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	4.92E+07	1.60E+04	1.10E+06	1.01E+02	0.00E+00	1.15E+08	3.20E+08
0	2	3.99E+07	0.00E+00	0.00E+00	0.00E+00	4.48E+05	1.70E+01	0.00E+00	4.92E+07	5.12E+06	0.00E+00	0.00E+00	1.20E+03	0.00E+00	1.33E+04	0.00E+00	0.00E+00	2.07E+02	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	4.92E+07	1.60E+04	1.10E+06	1.01E+02	0.00E+00	1.15E+08	3.20E+08
0	3	3.99E+07	0.00E+00	0.00E+00	0.00E+00	4.48E+05	1.70E+01	0.00E+00	4.92E+07	5.12E+06	0.00E+00	0.00E+00	1.20E+03	0.00E+00	1.33E+04	0.00E+00	0.00E+00	2.07E+02	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	4.92E+07	1.60E+04	1.10E+06	1.01E+02	0.00E+00	1.15E+08	3.20E+08
0	4	3.99E+07	0.00E+00	0.00E+00	0.00E+00	4.48E+05	1.70E+01	0.00E+00	4.92E+07	5.12E+06	0.00E+00	0.00E+00	1.20E+03	0.00E+00	1.33E+04	0.00E+00	0.00E+00	2.07E+02	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	4.92E+07	1.60E+04	1.10E+06	1.01E+02	0.00E+00	1.15E+08	3.20E+08
0	5	3.99E+07	0.00E+00	0.00E+00	0.00E+00	4.48E+05	1.70E+01	0.00E+00	4.92E+07	5.12E+06	0.00E+00	0.00E+00	1.20E+03	0.00E+00	1.33E+04	0.00E+00	0.00E+00	2.07E+02	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	4.92E+07	1.60E+04	1.10E+06	1.01E+02	0.00E+00	1.15E+08	3.20E+08
0	6	3.99E+07	0.00E+00	0.00E+00	0.00E+00	4.48E+05	1.70E+01	0.00E+00	4.92E+07	5.12E+06	0.00E+00	0.00E+00	1.20E+03	0.00E+00	1.33E+04	0.00E+00	0.00E+00	2.07E+02	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	4.92E+07	1.60E+04	1.10E+06	1.01E+02	0.00E+00	1.15E+08	3.20E+08
0	7	3.99E+07	0.00E+00	0.00E+00	0.00E+00	4.48E+05	1.70E+01	0.00E+00	4.92E+07	5.12E+06	0.00E+00	0.00E+00	1.20E+03	0.00E+00	1.33E+04	0.00E+00	0.00E+00	2.07E+02	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	4.92E+07	1.60E+04	1.10E+06	1.01E+02	0.00E+00	1.15E+08	3.20E+08
0	8	3.99E+07	0.00E+00	0.00E+00	0.00E+00	4.48E+05	1.70E+01	0.00E+00	4.92E+07	5.12E+06	0.00E+00	0.00E+00	1.20E+03	0.00E+00	1.33E+04	0.00E+00	0.00E+00	2.07E+02	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	4.92E+07	1.60E+04	1.10E+06	1.01E+02	0.00E+00	1.15E+08	3.20E+08
0	9	3.99E+07	0.00E+00	0.00E+00	0.00E+00	4.48E+05	1.70E+01	0.00E+00	4.92E+07	5.12E+06	0.00E+00	0.00E+00	1.20E+03	0.00E+00	1.33E+04	0.00E+00	0.00E+00	2.07E+02	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	4.92E+07	1.60E+04	1.10E+06	1.01E+02	0.00E+00	1.15E+08	3.20E+08
0	10	3.99E+07	0.00E+00	0.00E+00	0.00E+00	4.48E+05	1.70E+01	0.00E+00	4.92E+07	5.12E+06	0.00E+00	0.00E+00	1.20E+03	0.00E+00	1.33E+04	0.00E+00	0.00E+00	2.07E+02	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	4.92E+07	1.60E+04	1.10E+06	1.01E+02	0.00E+00	1.15E+08	3.20E+08
0	11	3.99E+07	0.00E+00	0.00E+00	0.00E+00	4.48E+05	1.70E+01	0.00E+00	4.92E+07	5.12E+06	0.00E+00	0.00E+00	1.20E+03	0.00E+00	1.33E+04	0.00E+00	0.00E+00	2.07E+02	8.00E+00	1.13E+07	4.92E+07	0.00E+00	0.00E+00	4.92E+07	1.60E+04	1.10E+06	1.01E+02	0.00E+00	1.15E+08	3.20E+08
	CMG 0 total	4.79E+08	0.00E+00	0.00E+00	0.00E+00	5.38E+06	2.04E+02	0.00E+00	5.90E+08	6.14E+07	0.00E+00	0.00E+00	1.44E+04	0.00E+00	1.77E+05	0.00E+00	0.00E+00	2.48E+03	9.60E+01	1.35E+08	5.90E+08	0.00E+00	0.00E+00	5.90E+08	1.92E+05	1.32E+07	1.21E+03	0.00E+00	1.38E+09	3.84E+09
	Ci-io o total							1.14	E+09								2.48E+03		9.60E+01		7.25E+08		5.90E	+08	1.92E+05	1.32E+07	1.21E+03	0.00E+00	1.38E+09	3.84E+09

Power Con (V		Power consumption used by core	Power consumption used by L2 cache	Power consumption used by memory
Process	Thread			
0	0	2.06E+00		
0	1	2.06E+00		
0	2	2.06E+00		
0	3	2.06E+00		
0	4	2.06E+00	Ī	
0	5	2.06E+00	2.36E+00	7.82F+00
0	6	2.06E+00	2.30E+00	7.62E+00
0	7	2.06E+00		
0	8	2.06E+00		
0	9	2.06E+00	Ī	
0	10	2.06E+00	1	
0	11	2.06E+00		
	CMG 0 total	2.47E+01	2.36E+00	7.82E+00

Hardware	Prefetch		L1			L2		L1/L2
Rate (/Hard Prefe	lware etch)	Stream mode prefetch rate	Injection mode allocate prefetch rate	Injection mode unallocate prefetch rate	Stream mode prefetch rate	Injection mode allocate prefetch rate	Injection mode unallocate prefetch rate	Other hardware prefetch
Process	Thread							
0	0	40.32%	0.00%	0.00%	48.90%	0.00%	0.00%	10.78%
0	1	40.01%	0.00%	0.00%	51.21%	0.00%	0.00%	8.78%
0	2	40.04%	0.00%	0.00%	51.10%	0.00%	0.00%	8.86%
0	3	40.16%	0.00%	0.00%	51.00%	0.00%	0.00%	8.84%
0	4	40.22%	0.00%	0.00%	50.97%	0.00%	0.00%	8.80%
0	5	40.12%	0.00%	0.00%	50.99%	0.00%	0.00%	8.88%
0	6	40.34%	0.00%	0.00%	50.91%	0.00%	0.00%	8.75%
0	7	40.33%	0.00%	0.00%	50.84%	0.00%	0.00%	8.83%
0	8	40.37%	0.00%	0.00%	50.87%	0.00%	0.00%	8.76%
0	9	40.47%	0.00%	0.00%	50.81%	0.00%	0.00%	8.72%
0	10	40.29%	0.00%	0.00%	50.85%	0.00%	0.00%	8.86%
0	11	40.53%	0.00%	0.00%	50.78%	0.00%	0.00%	8.69%
	CMG 0 total	40.27%	0.00%	0.00%	50.77%	0.00%	0.00%	8.96%

	FLC	PS	Double precision floating- point operation	Single precision floating- point operation	Half precision floating- point operation	GFLOPS by Active element rate
L	Process	Thread				
L	0	0	8.77.E+08	0.00.E+00	0.00.E+00	10.49
L	0	1	8.77.E+08	0.00.E+00	0.00.E+00	10.50
L	0	2	8.77.E+08	0.00.E+00	0.00.E+00	10.50
ſ	0	3	8.77.E+08	0.00.E+00	0.00.E+00	10.50
ſ	0	4	8.77.E+08	0.00.E+00	0.00.E+00	10.50
ſ	0	5	8.77.E+08	0.00.E+00	0.00.E+00	10.50
ſ	0	6	8.77.E+08	0.00.E+00	0.00.E+00	10.50
ſ	0	7	8.77.E+08	0.00.E+00	0.00.E+00	10.50
ľ	0	8	8.77.E+08	0.00.E+00	0.00.E+00	10.50
Г	0	9	8.77.E+08	0.00.E+00	0.00.E+00	10.50
E	0	10	8.77.E+08	0.00.E+00	0.00.E+00	10.50
	0	11	8.77.E+08	0.00.E+00	0.00.E+00	10.50
Е		CMG 0 total	1.05.E+10	0.00.E+00	0.00.E+00	125.97

Extra		Gather instruction rate (%)			Instruction					ĺ	
		0 flow rate (%)	1 flow rate (%)	2 flows rate (%)	Micro- operation instruction	Element manipulated instruction	Register manipulated instruction	MOVPRFX instruction	Math functional instruction	Micro decompositio n instruction rate (%)	Branch prediction miss rate (%)
Process	Thread										
0	0	0.00%	0.00%	0.00%	3.20E+08	4.92E+07	6.41E+03	0.00E+00	0.00E+00	100.00%	1.44
0	1	0.00%	0.00%	0.00%	3.20E+08	4.92E+07	1.00E+00	0.00E+00	0.00E+00	100.00%	1.60
0	2	0.00%	0.00%	0.00%	3.20E+08	4.92E+07	1.00E+00	0.00E+00	0.00E+00	100.00%	1.61
0	3	0.00%	0.00%	0.00%	3.20E+08	4.92E+07	1.00E+00	0.00E+00	0.00E+00	100.00%	1.60
0	4	0.00%	0.00%	0.00%	3.20E+08	4.92E+07	1.00E+00	0.00E+00	0.00E+00	100.00%	1.61
0	5	0.00%	0.00%	0.00%	3.20E+08	4.92E+07	1.00E+00	0.00E+00	0.00E+00	100.00%	1.61
0	6	0.00%	0.00%	0.00%	3.20E+08	4.92E+07	1.00E+00	0.00E+00	0.00E+00	100.00%	1.62
0	7	0.00%	0.00%	0.00%	3.20E+08	4.92E+07	1.00E+00	0.00E+00	0.00E+00	100.00%	1.61
0	8	0.00%	0.00%	0.00%	3.20E+08	4.92E+07	1.00E+00	0.00E+00	0.00E+00	100.00%	1.60
0	9	0.00%	0.00%	0.00%	3.20E+08	4.92E+07	1.00E+00	0.00E+00	0.00E+00	100.00%	1.63
0	10	0.00%	0.00%	0.00%	3.20E+08	4.92E+07	1.00E+00	0.00E+00	0.00E+00	100.00%	1.60
0	11	0.00%	0.00%	0.00%	3.20E+08	4.92E+07	1.00E+00	0.00E+00	0.00E+00	100.00%	1.59
CMG 0 total		0.00%	0.00%	0.00%	3.85E+09	5.90E+08	6.42E+03	0.00E+00	0.00E+00	100.00%	1.59
			0.00%		3.85E+09	5.90	E+08	0.00E+00	0.00E+00	100.00%	1.5

(W)	Power Cons	umption
4.0E+01 T		
3.5E+01		
3.0E+01		used by memory
2.5E+01		used by L2 cache
2.0E+01		■used by core
1.5E+01		
1.0E+01		
5.0E+00		
0.0E+00	1 CMG	

Data Transfer CMGs			Destination (GB/s)						
			Own memory	Other	Tofu	PC			
			Own memory	memory	Toru	PC			
CMC	tetal	read	8.87E+01	8.44E-03	0.00E+00	0.00			
CMG 0 tota	lotai	write	9.89E+00	1.07E-02	0.00E+00	0.00			