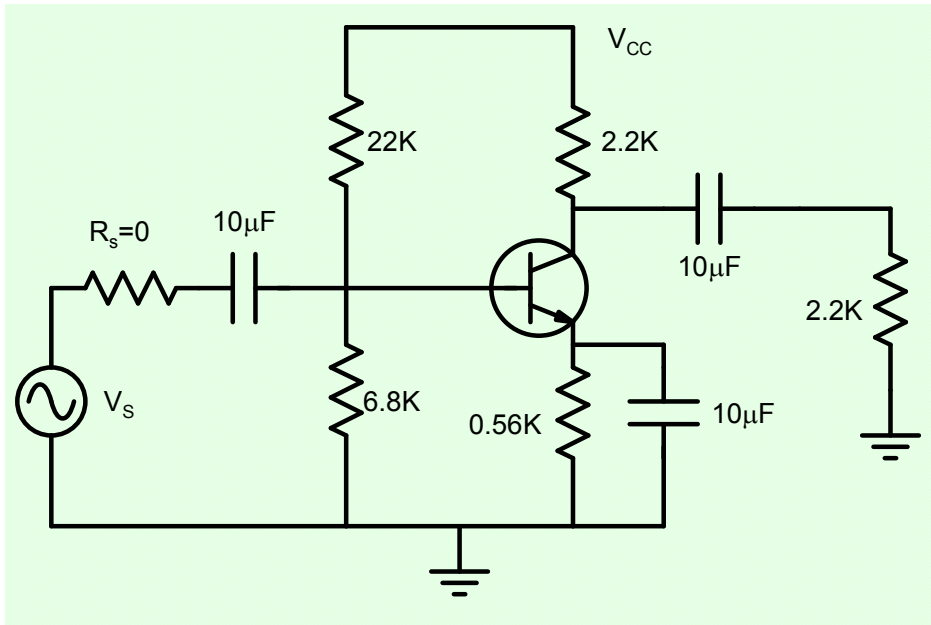


EE210: Microelectronics-I

Lecture-24 :Cascode Amplifier

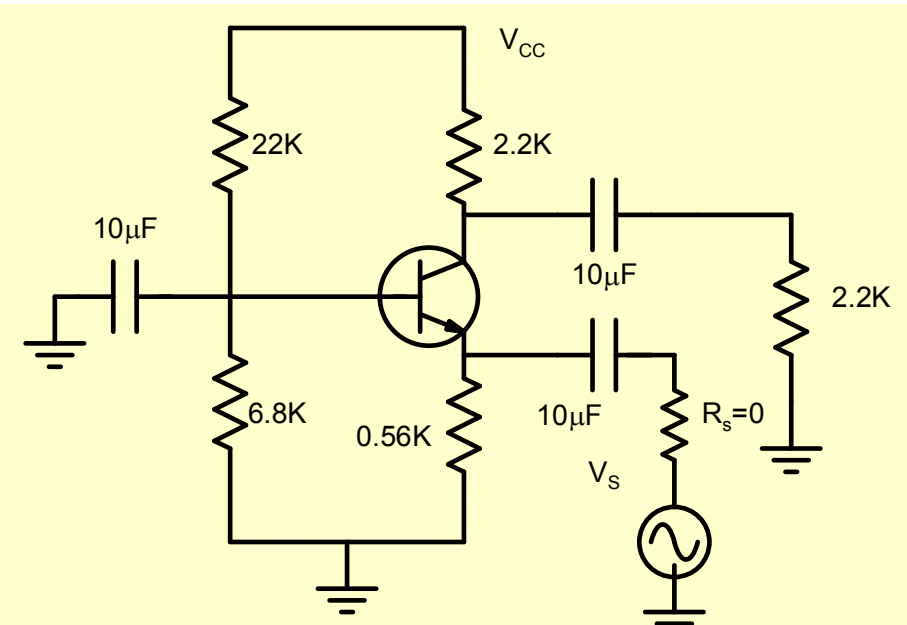
Instructor - Y. S. Chauhan

Slides - B. Mazhari
Dept. of EE, IIT Kanpur



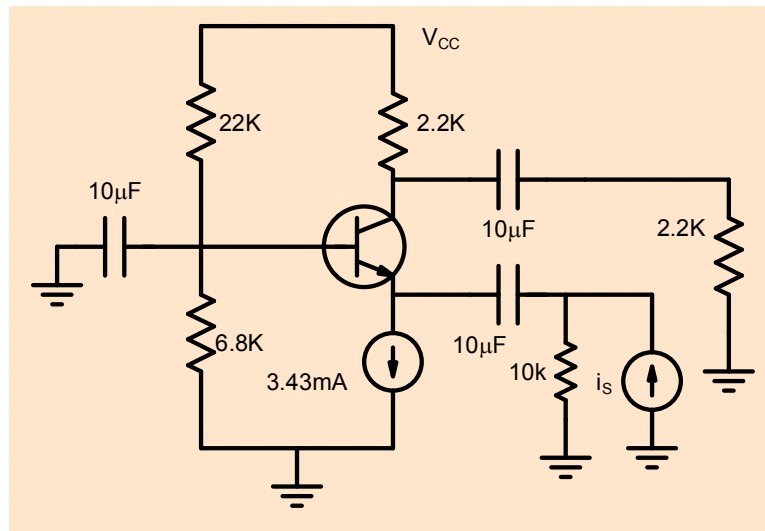
$$A_v = -110.7; R_{in} = 0.82 k\Omega; R_o = 2.2 k\Omega$$

$$f_H = 5.8 MHz$$



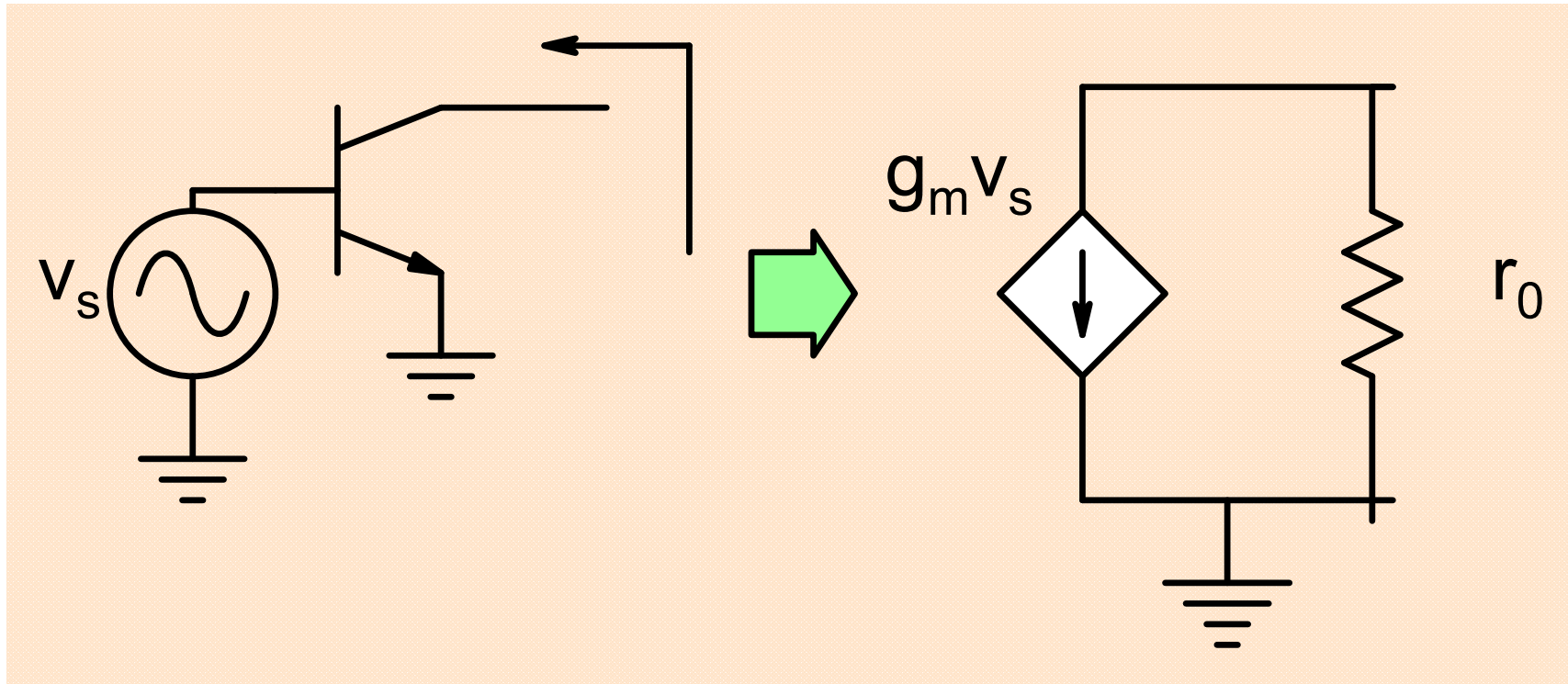
$$A_v = 110.7; R_{in} = 9.66 \Omega; R_o = 2.2 k\Omega$$

$$f_H = 5.8 MHz$$



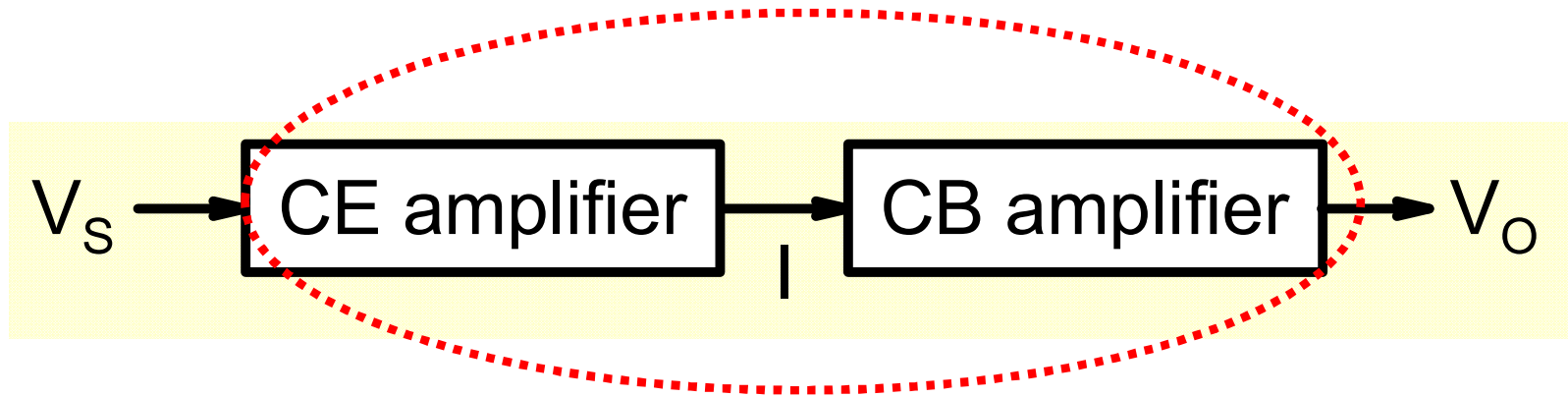
$$CB : A_v = 0.1; f_H = 73.5 MHz$$

CE amplifier

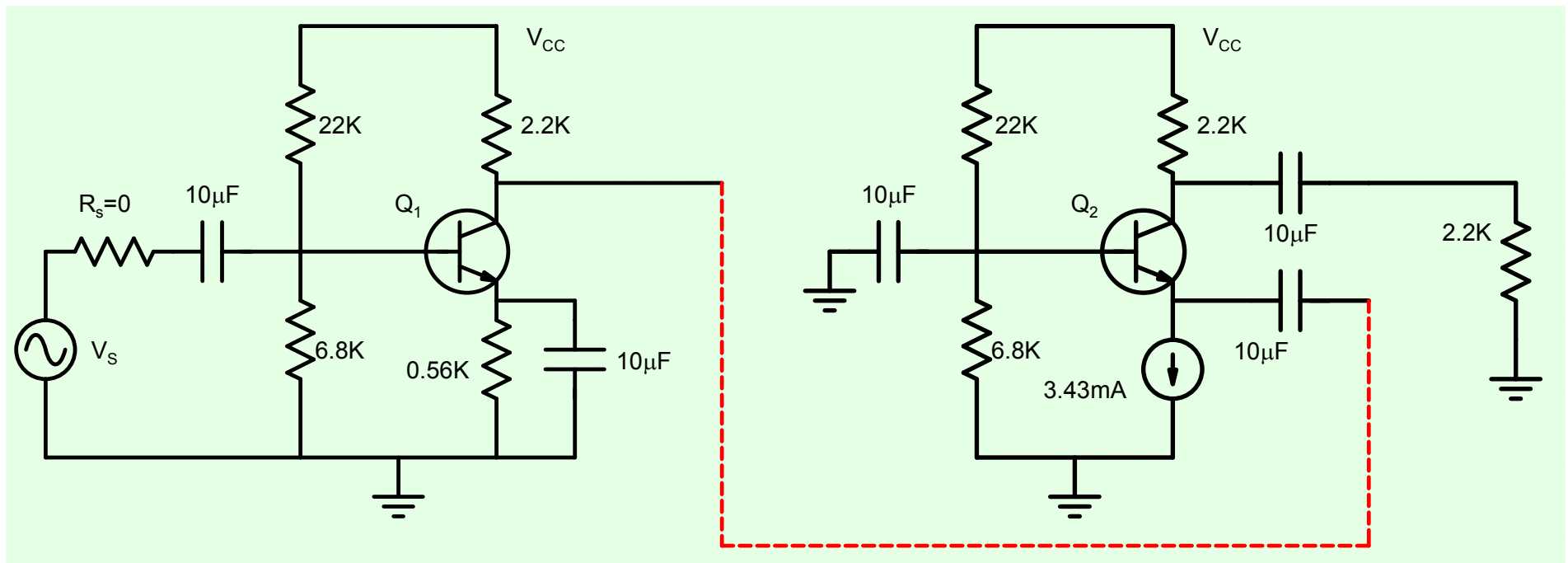
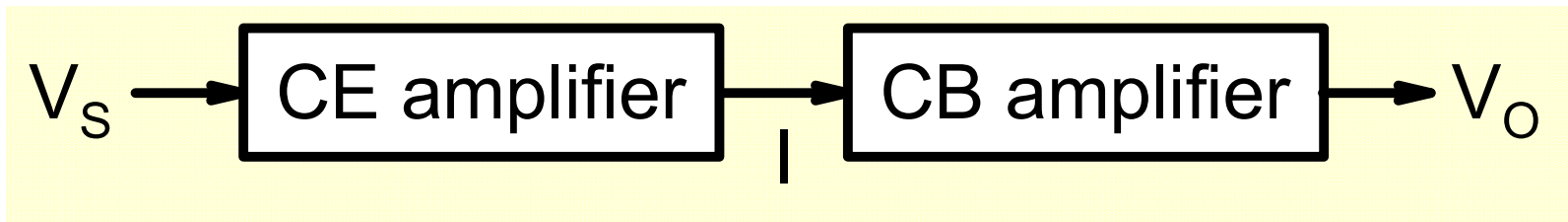


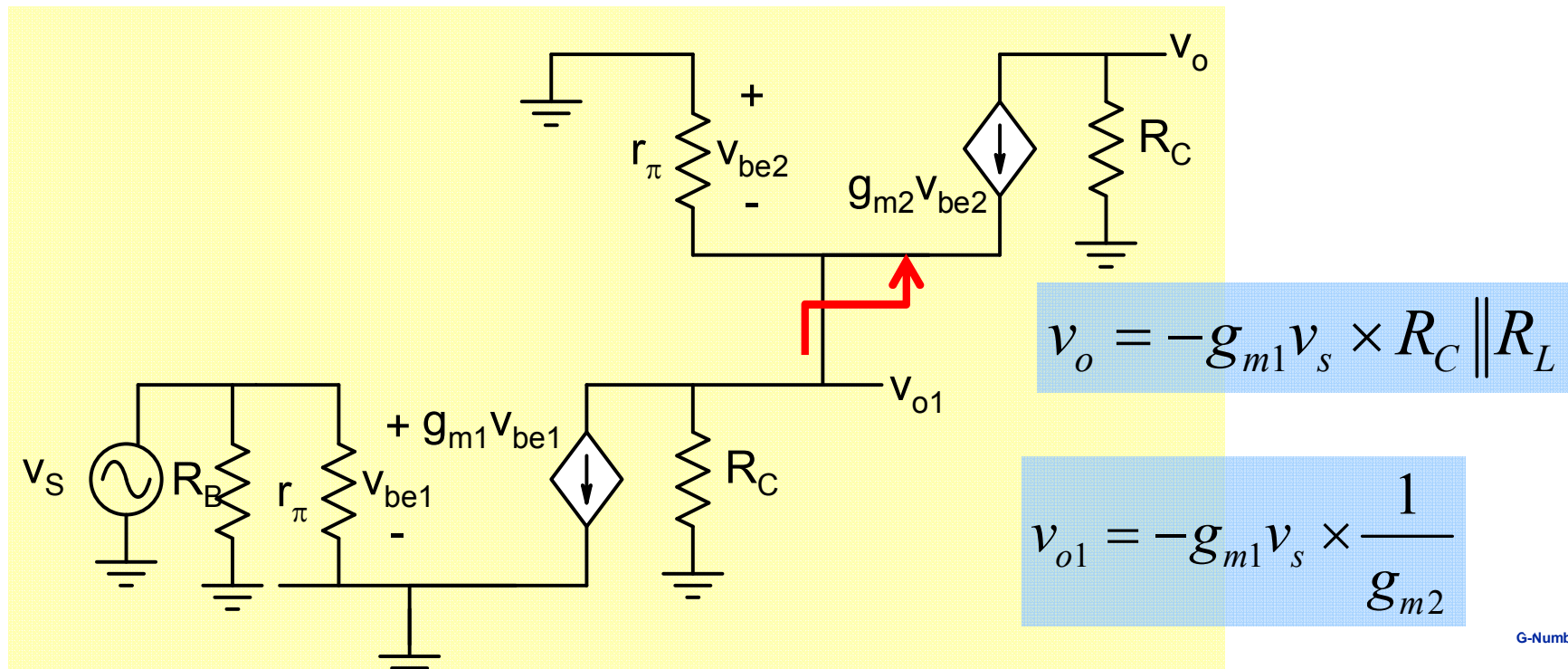
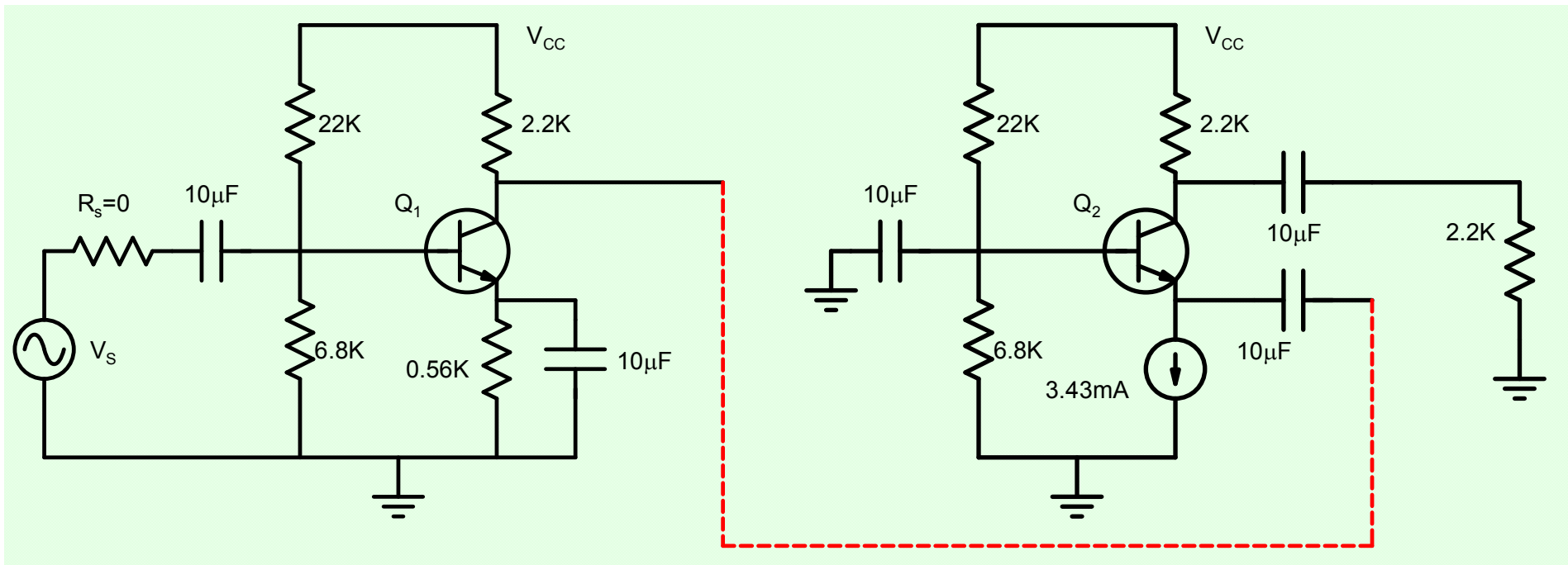
A CE amplifier is a good voltage to current converter !

Strategy

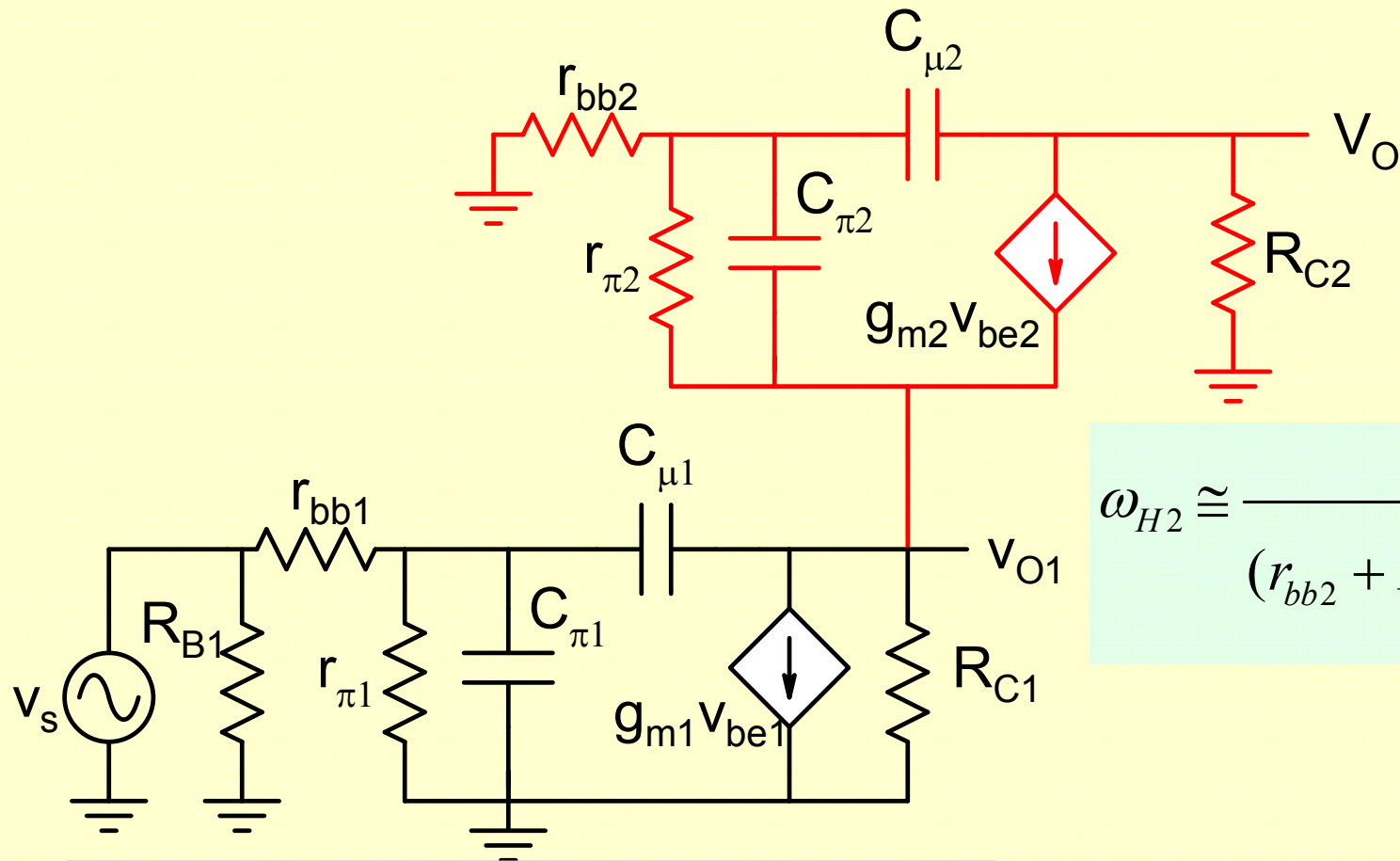


Cascode



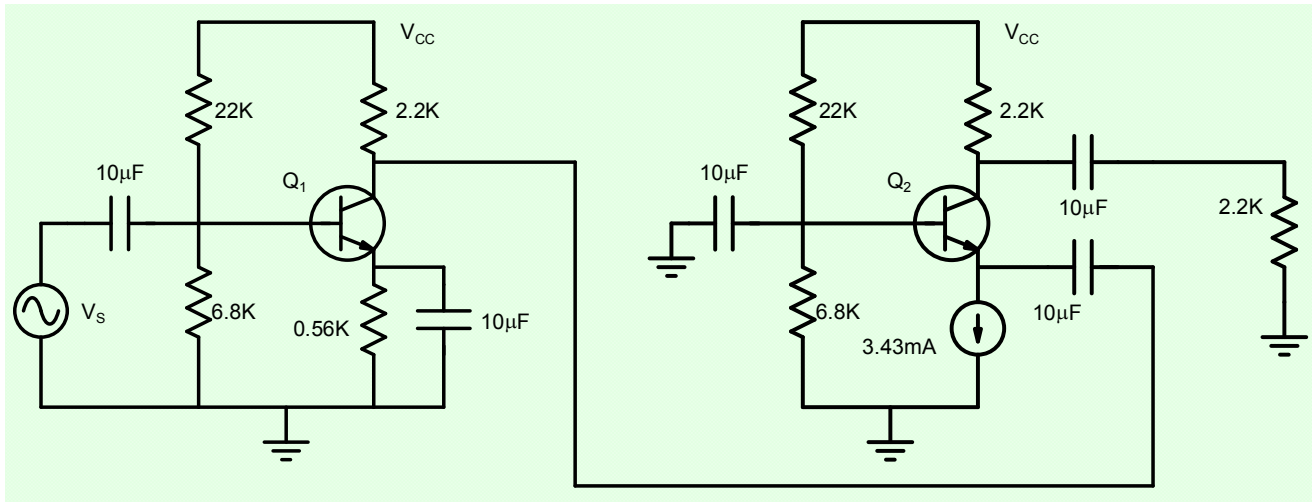


$$CE : \omega_H \cong \frac{1}{(R'_S \parallel r_\pi) \{C_\pi + C_\mu (1 + g_m R'_C)\} + R'_C C_\mu}$$



$$\omega_{H2} \cong \frac{1}{(r_{bb2} + R'_{C2})C_{\mu2} + \frac{C_{\pi2}}{g_{m2}}}$$

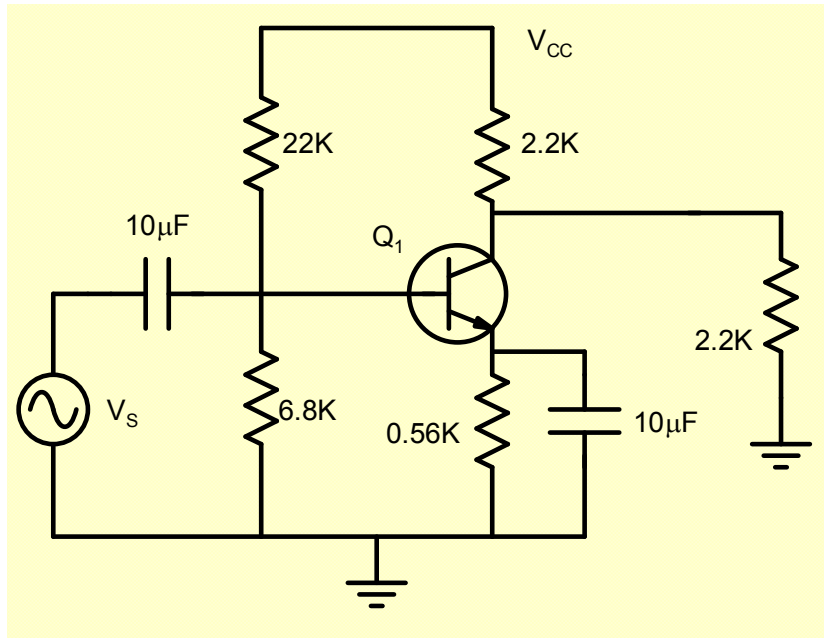
$$\omega_{H1} \cong \frac{1}{(r_{bb1} \parallel r_{\pi1}) \{C_{\pi1} + C_{\mu1} (1 - A_{v1})\} + \frac{C_{\mu1}}{g_{m2}}}$$



$$A_V = 113$$

$$R_{in} = 0.82k\Omega; R_o = 2.2k\Omega$$

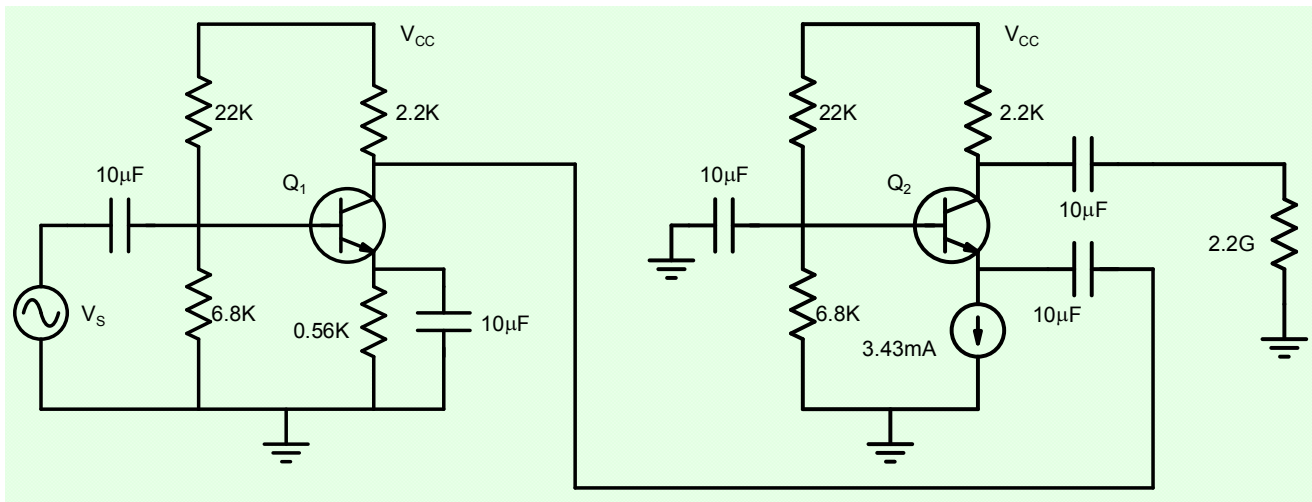
$$f_H = 7.36MHz$$



$$A_V = 110.7$$

$$R_{in} = 0.82k\Omega; R_o = 2.2k\Omega$$

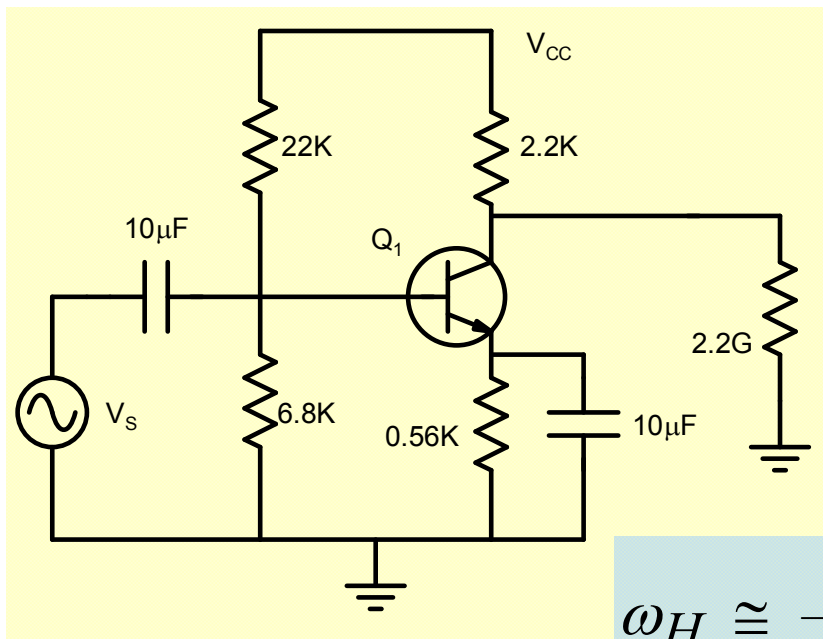
$$f_H = 5.8MHz$$



$$A_V = 226$$

$$R_{in} = 0.82k\Omega; R_o = 2.2k\Omega$$

$$f_H = 7.28MHz$$



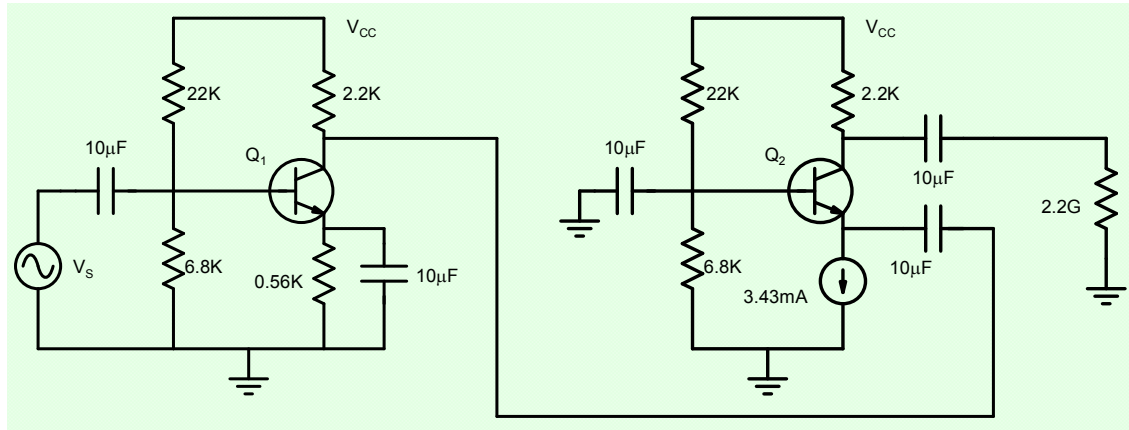
$$A_V = 213.6$$

$$R_{in} = 0.82k\Omega; R_o = 2.2k\Omega$$

$$f_H = 4.88MHz$$

$$\omega_H \cong \frac{1}{(R'_S \parallel r_\pi) \{C_\pi + C_\mu (1 + g_m R'_C)\} + R'_C C_\mu}$$

$$\omega_H \cong \frac{1}{(R'_S \parallel r_\pi) \{C_\pi + C_\mu(1 + g_m R'_C)\} + R'_C C_\mu} \quad \tau_F \text{ reduced from } 1\text{ns to } 0.1\text{ns}$$



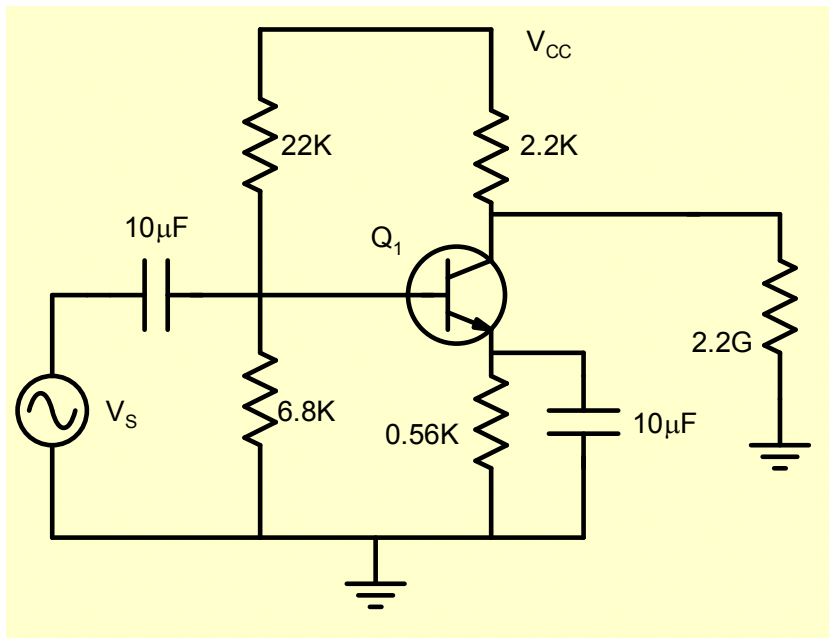
$$R_L = \infty$$

$$I_{CQ} = 2\text{mA}; R_C = 2.2\text{k};$$

$$A_V = 145.4; f_H = 46.44\text{MHz}$$

$$I_{CQ} = 3.4\text{mA}; R_C = 2.2\text{k};$$

$$A_V = 226.1; f_H = 35.3\text{MHz}$$



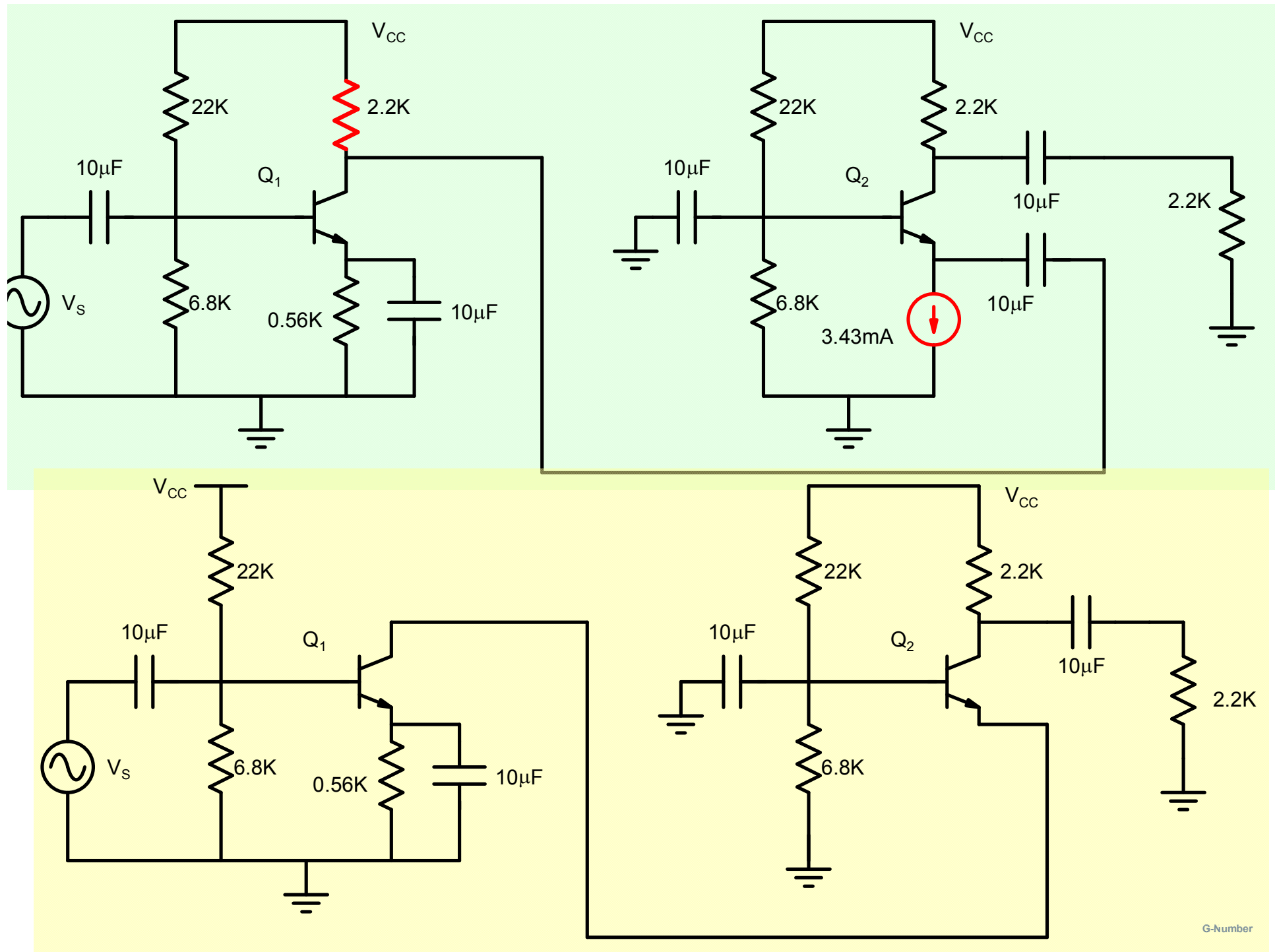
$$R_L = \infty$$

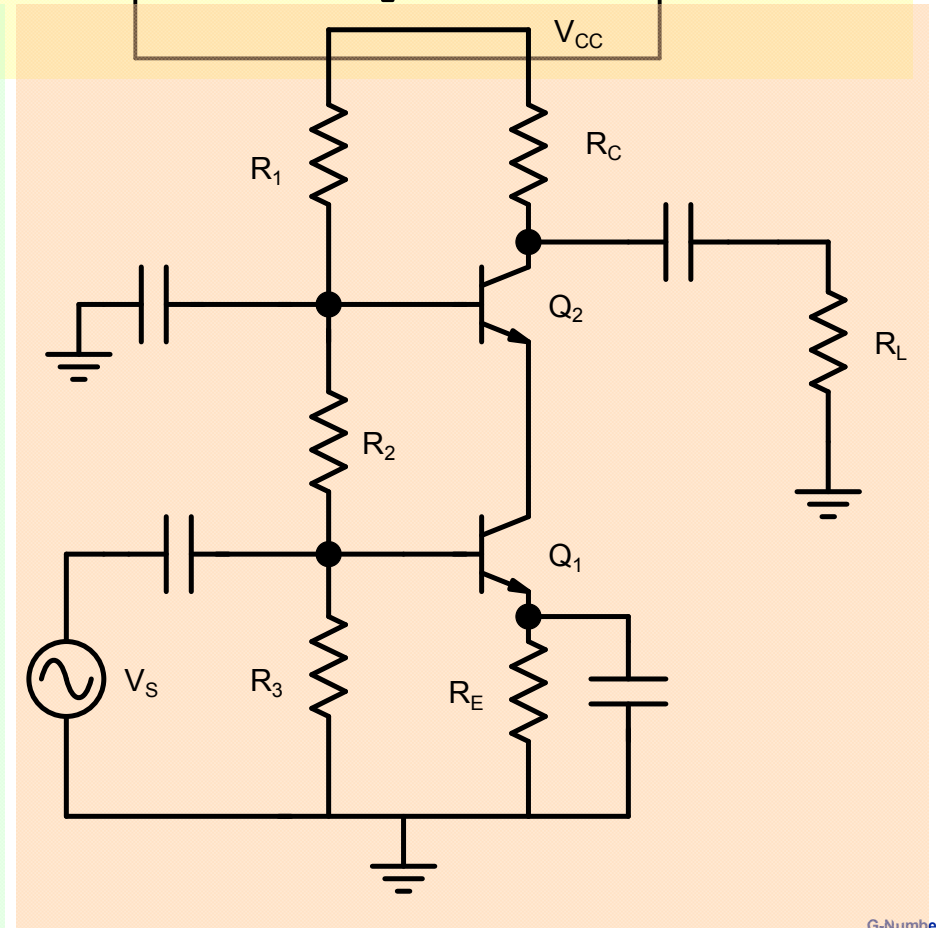
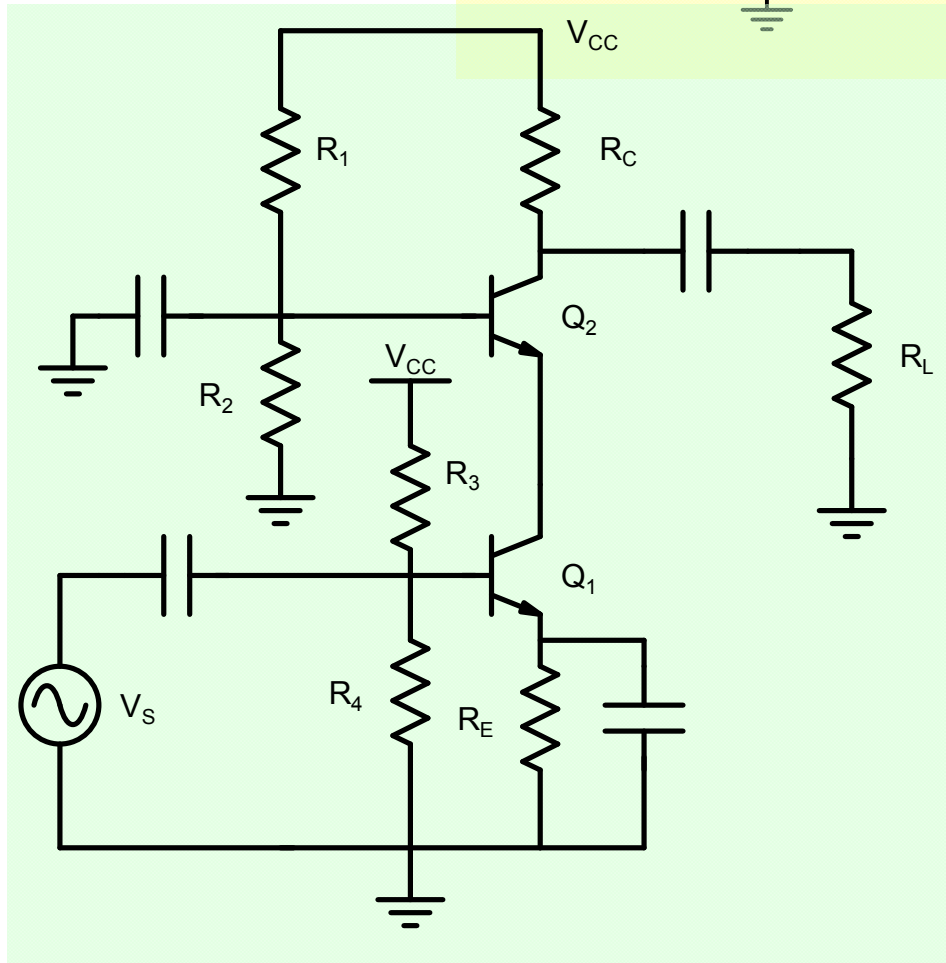
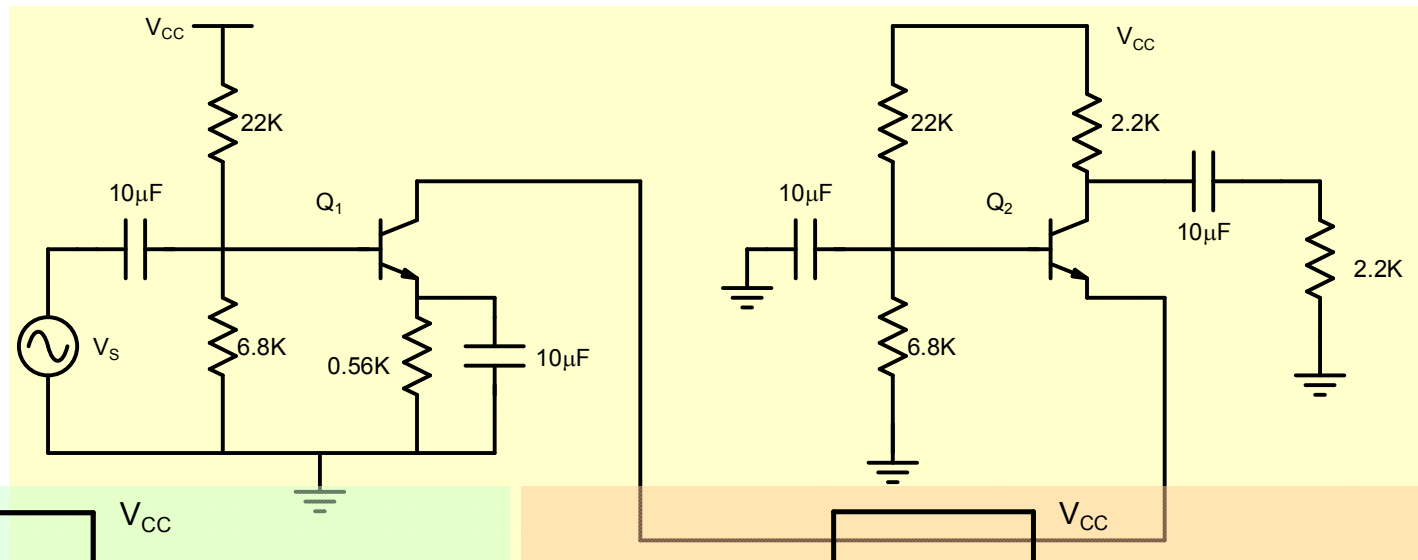
$$I_{CQ} = 2\text{mA}; R_C = 2.2\text{k};$$

$$A_V = 141.9; f_H = 17.1\text{MHz}$$

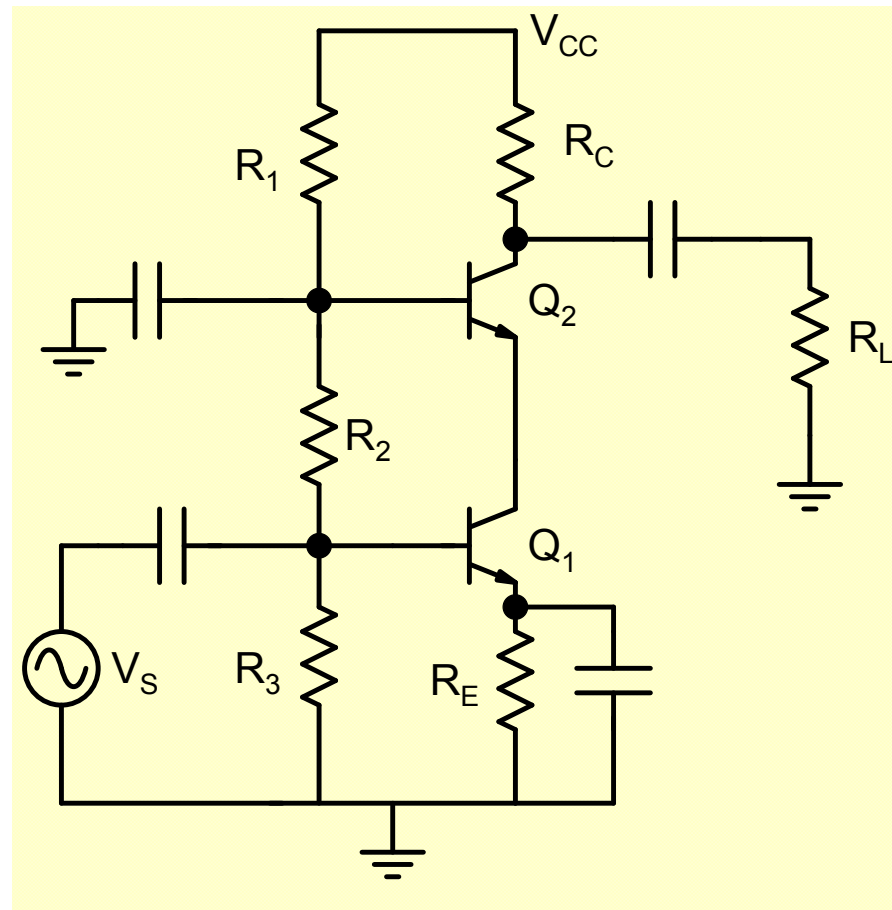
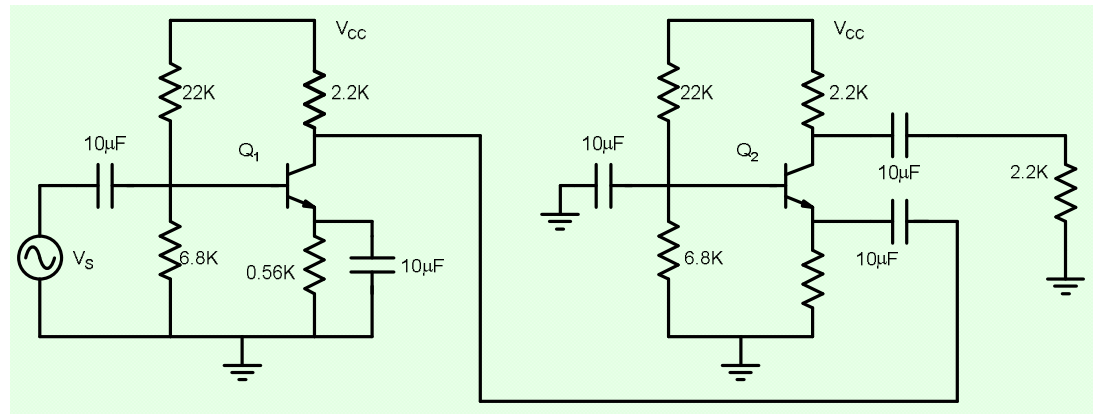
$$I_{CQ} = 3.4\text{mA}; R_C = 2.2\text{k};$$

$$A_V = 213.6; f_H = 9.4\text{MHz}$$

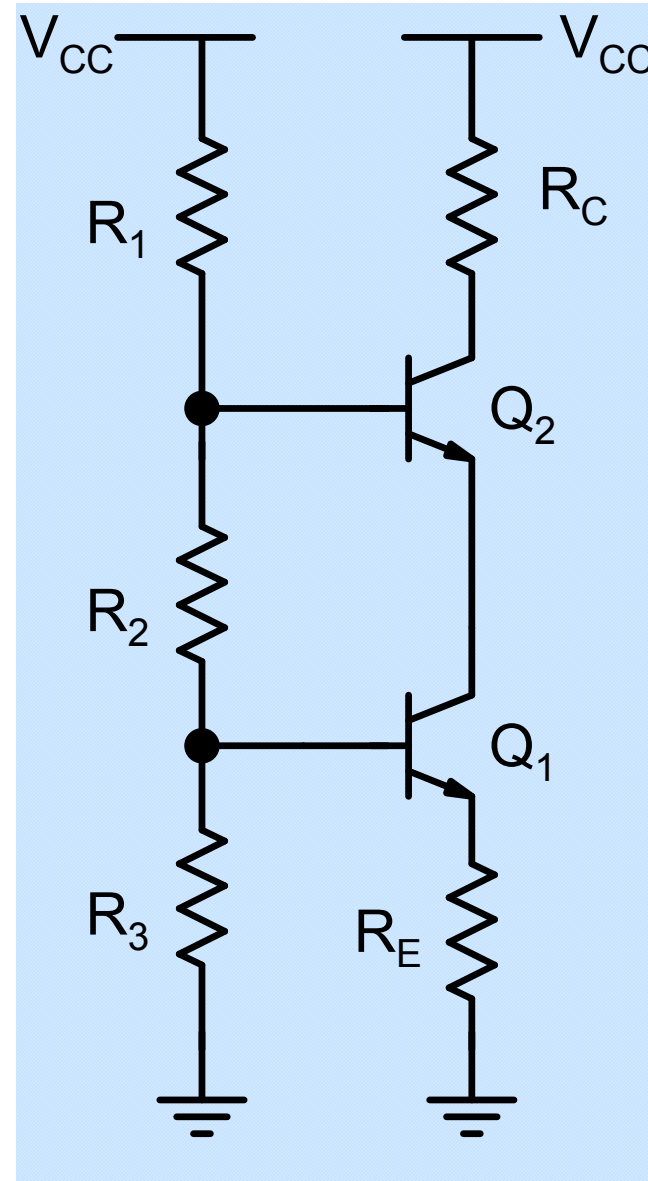
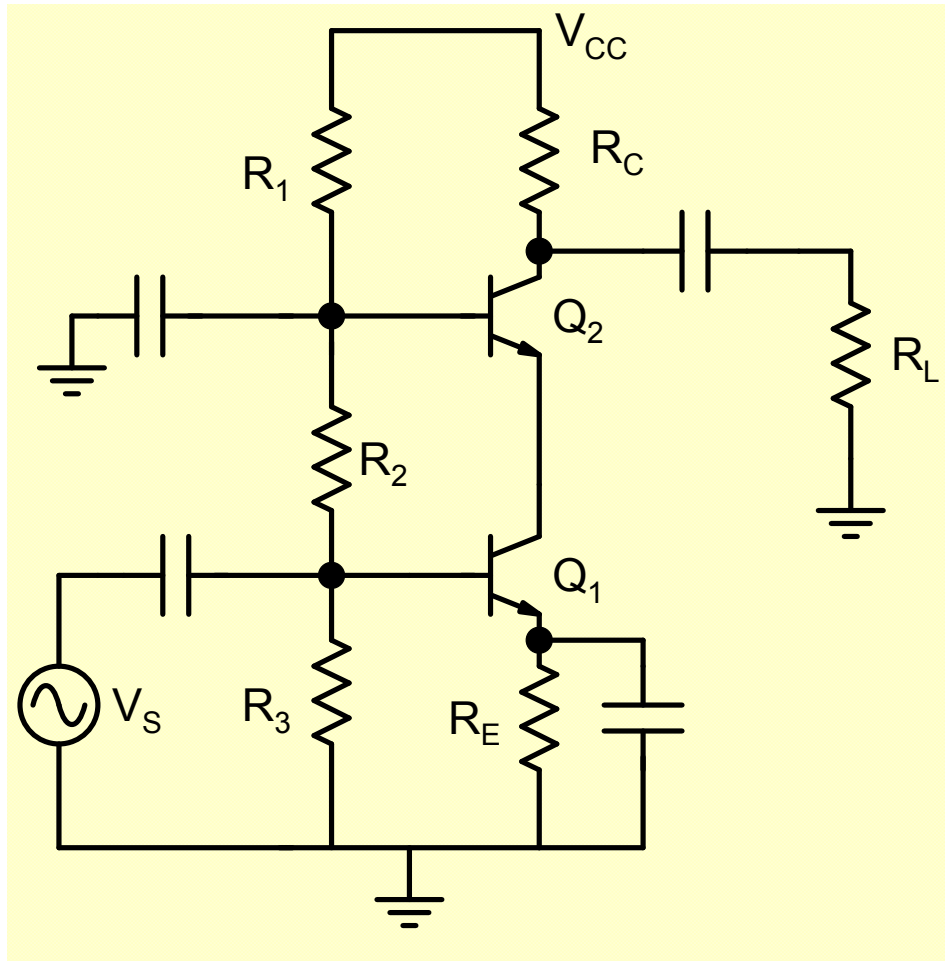


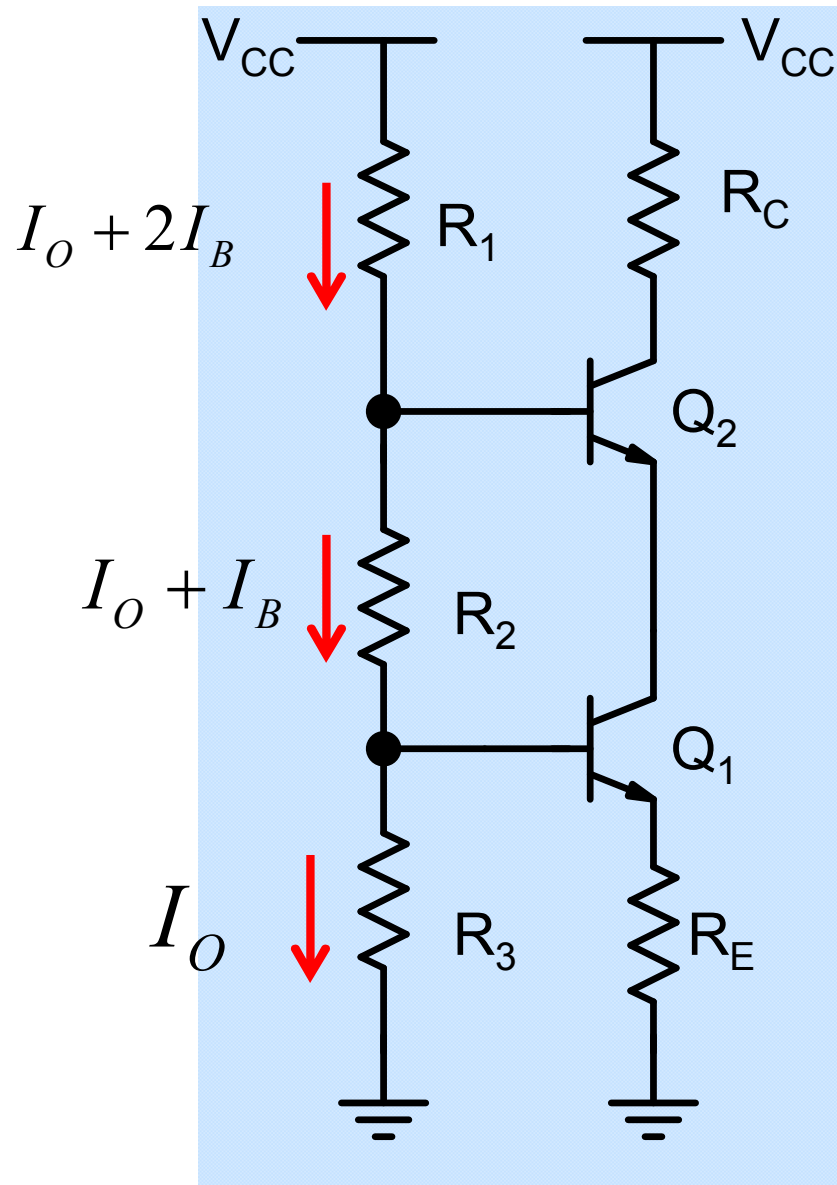


Cascode Amplifier



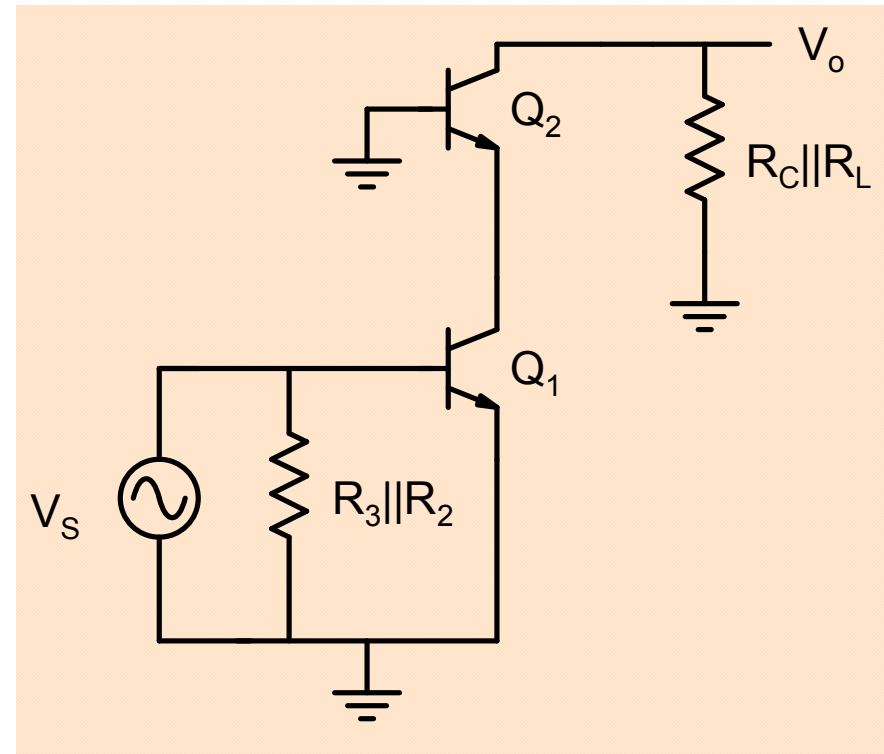
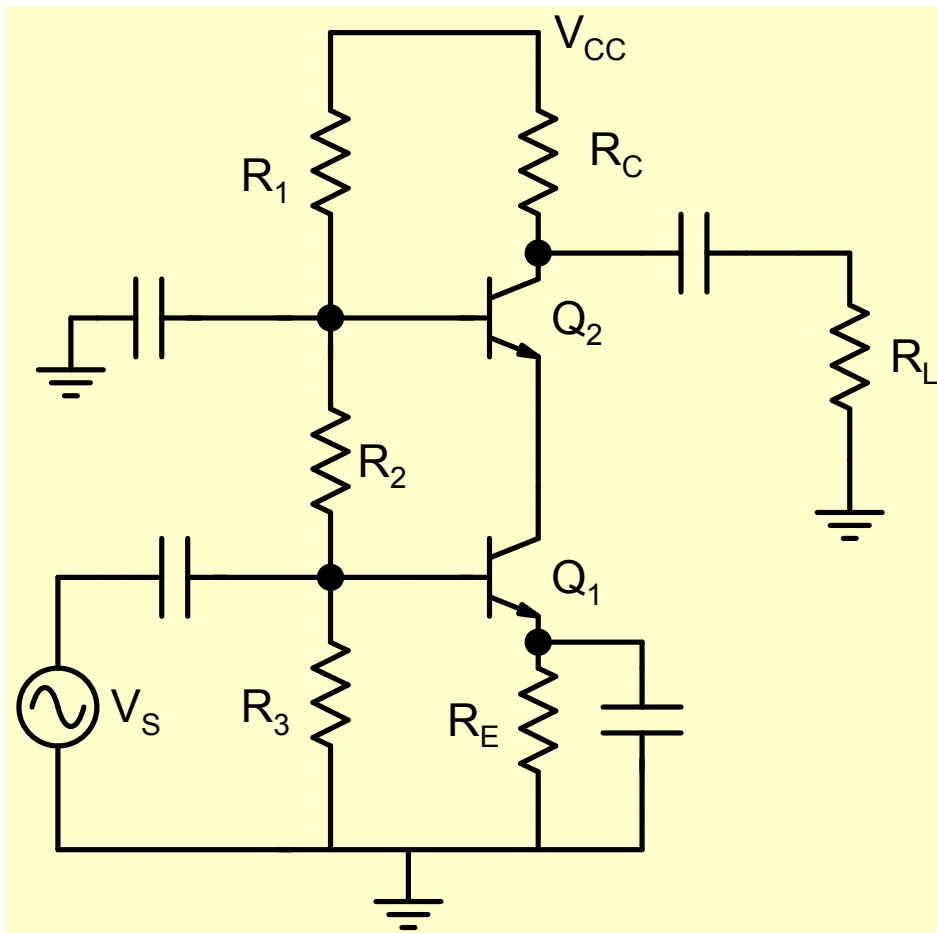
Bias point Analysis

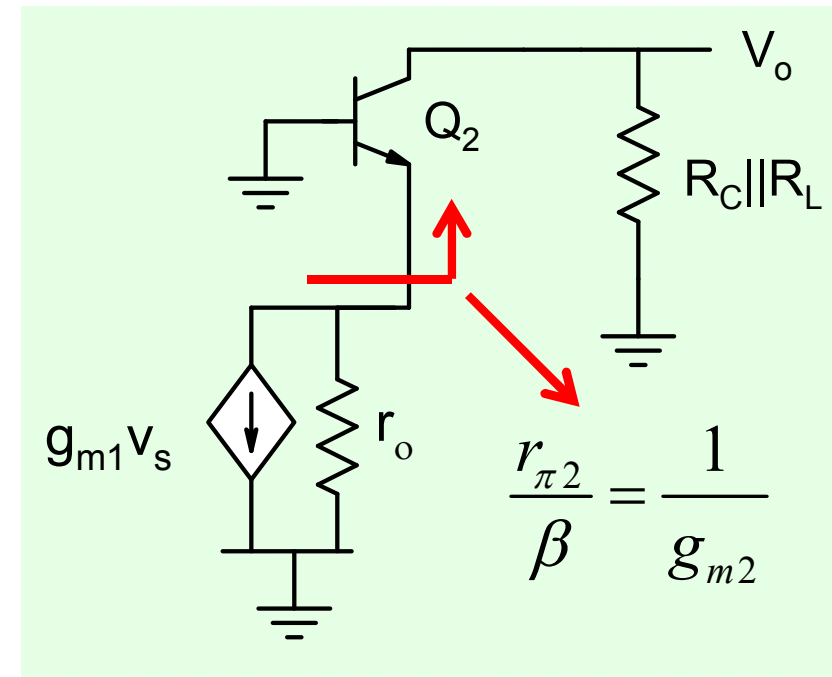
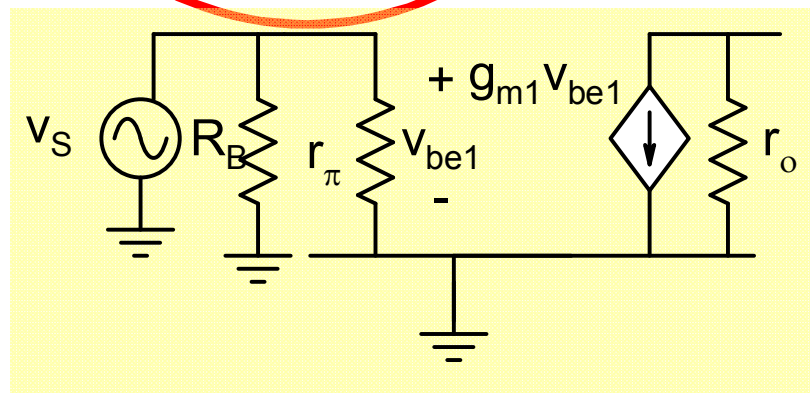
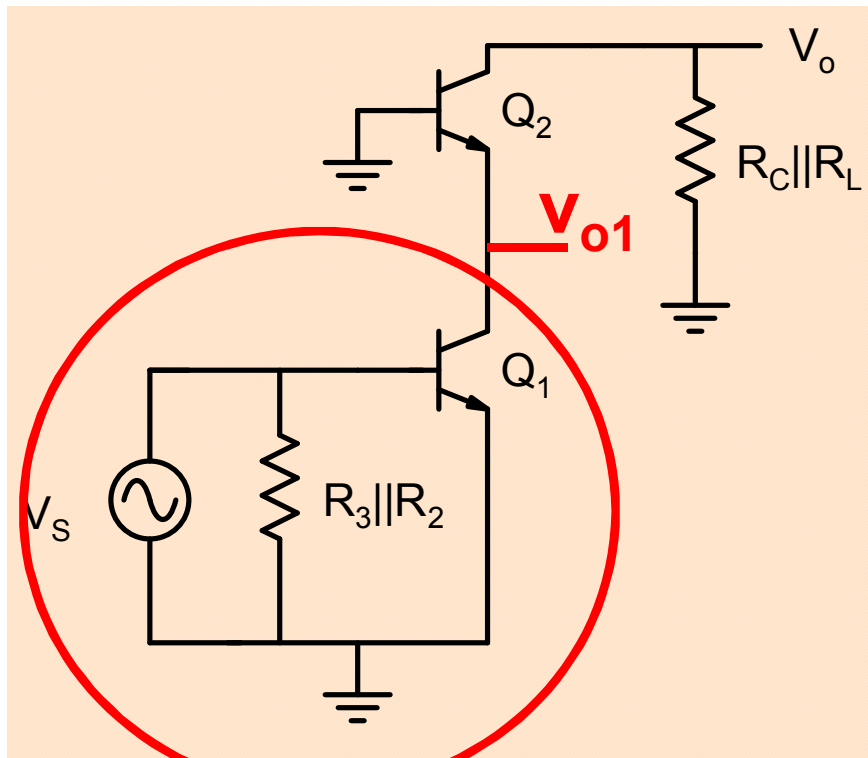




$$V_{CC} = (I_O + 2I_B) \times R_1 + (I_O + I_B) \times R_2 + I_O \times R_3$$

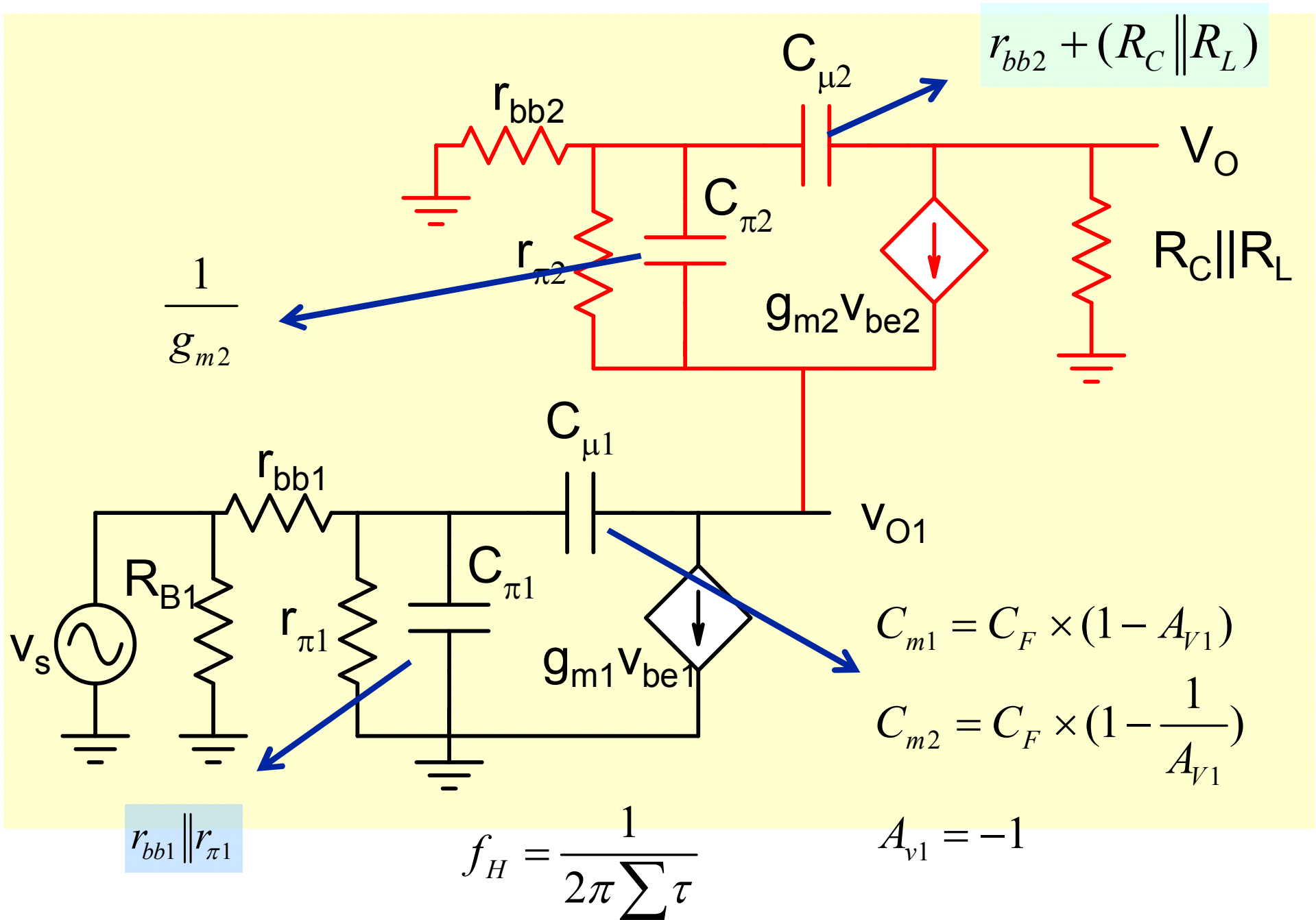
$$I_O \times R_3 = V_{BE} + I_E \times R_E$$



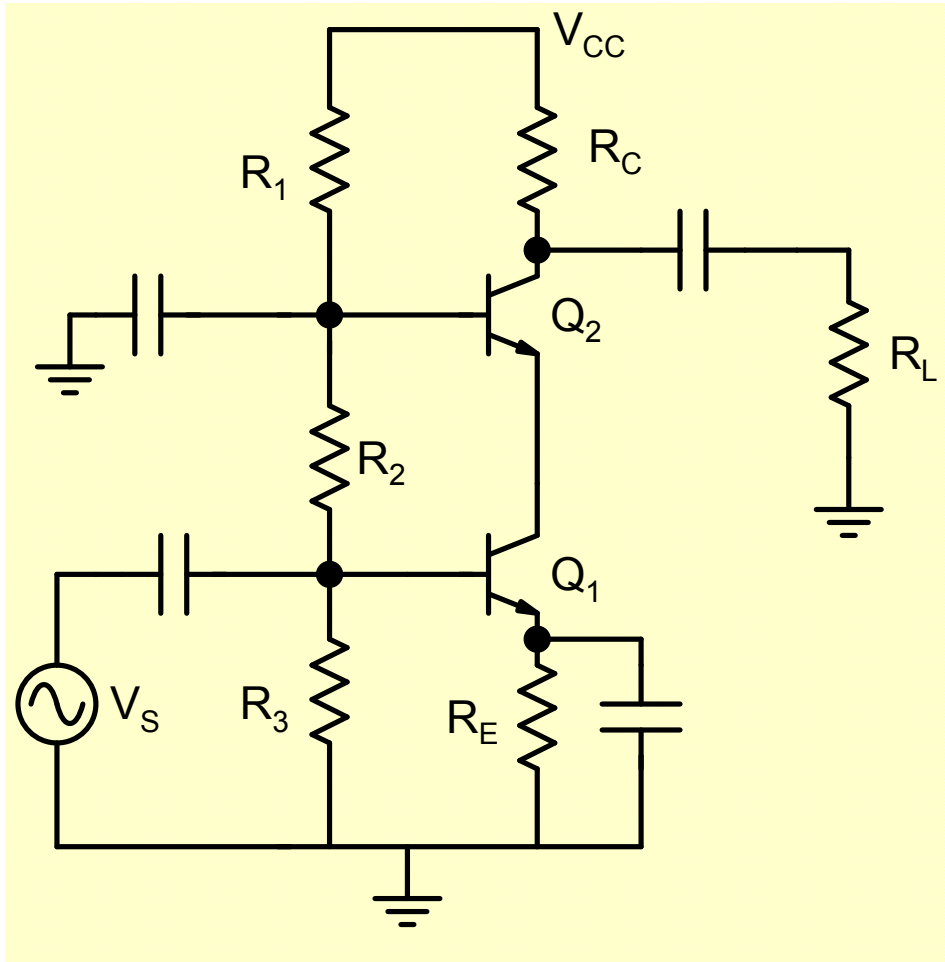


$$\frac{v_o}{v_s} = -g_{m1} \times R_C || R_L$$

$$\frac{v_{o1}}{v_s} = -g_{m1} \times \frac{1}{g_{m2}} = -1$$

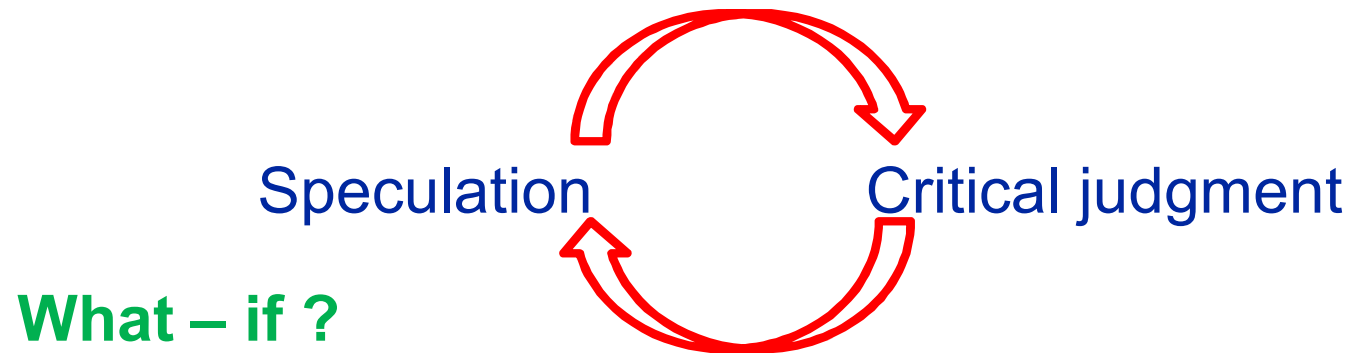


Cascode Amplifier

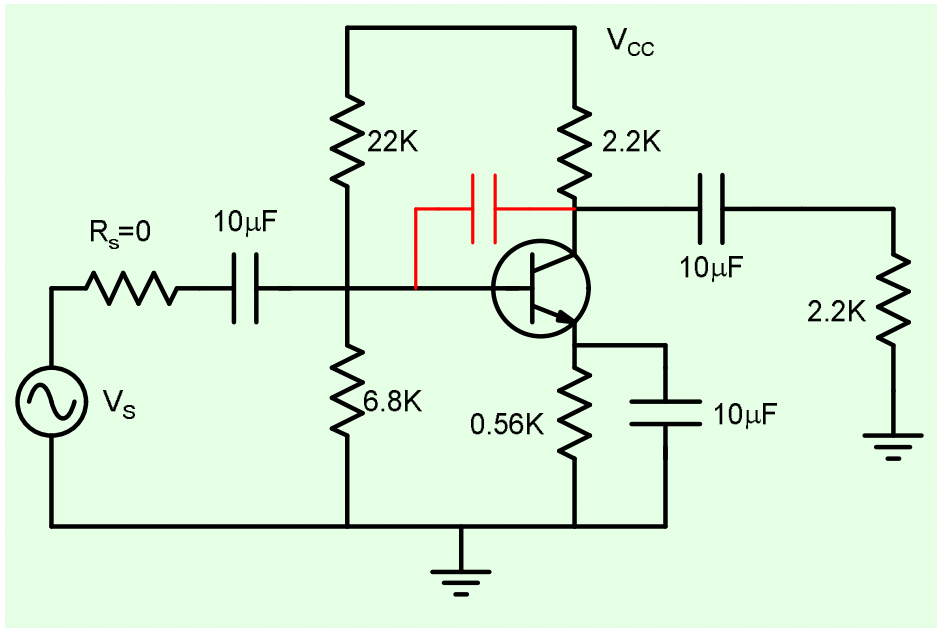


1. Same voltage gain, input and output resistance as a CE amplifier
2. Improved upper cutoff frequency
3. Reduced sensitivity of upper cutoff frequency to increase in voltage gain

Common-Base Amplifier: Lessons in design



- Understand the current solution and its limitations



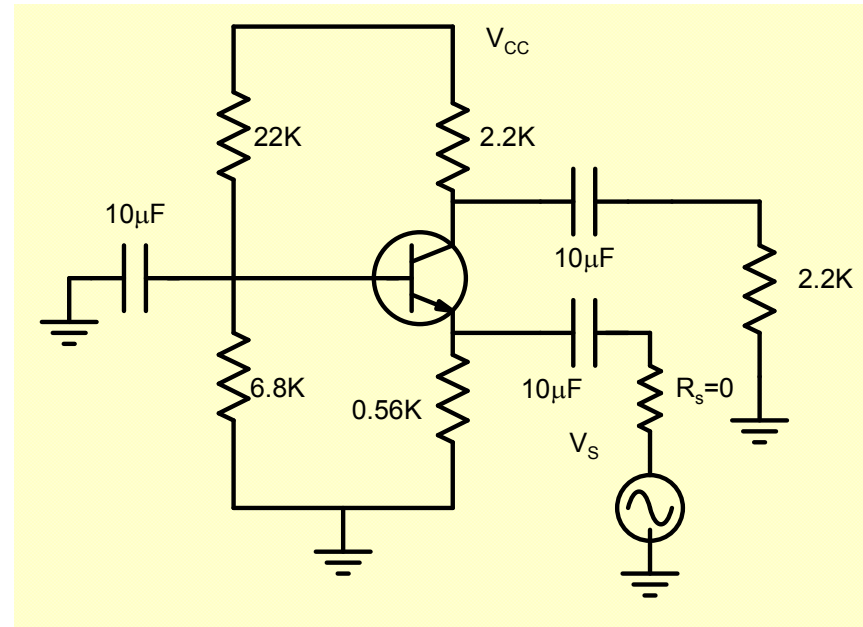
$$A_v = -110.7$$

$$R_{in} = 0.82 k\Omega; R_o = 2.2 k\Omega$$

$$\frac{A_v \times R_{in}}{R_o} = 41 < \beta$$

$$f_H = 5.8 MHz$$

- What – if ?



- Critical analysis

$$A_v = 110.7$$

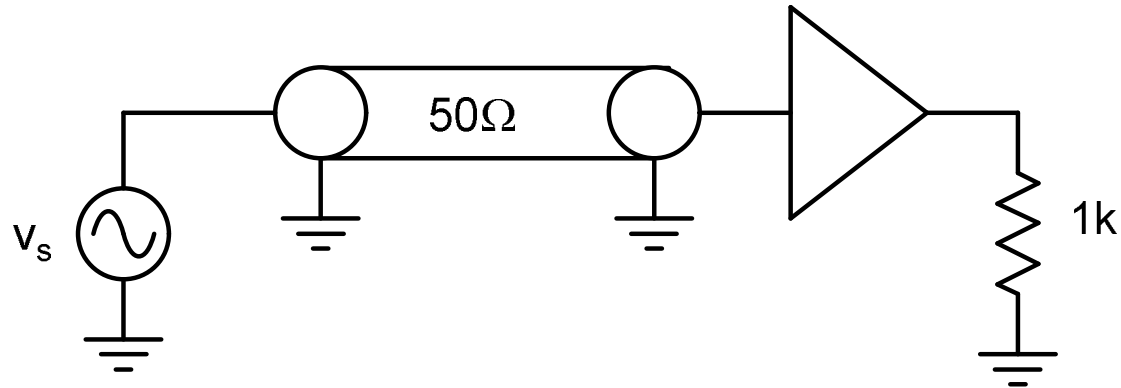
$$R_{in} = 9.66 \Omega; R_o = 2.2 k\Omega$$

$$\frac{A_v \times R_{in}}{R_o} = 0.48$$

$$f_H = 5.8 MHz$$

Sometimes, a perceived disadvantage
could actually be advantageous

Application of low input resistance

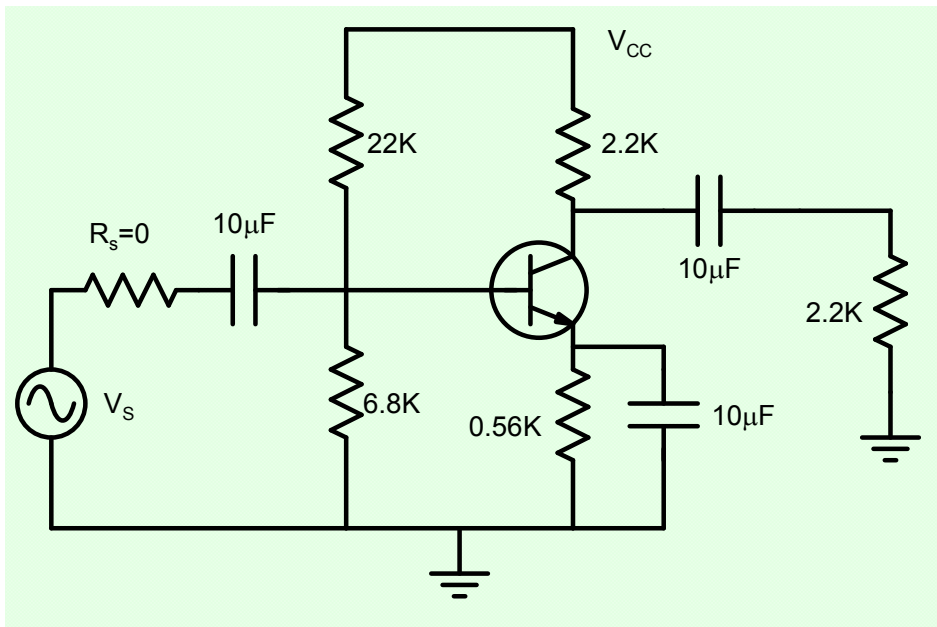


$$\text{CE : } R_{in} \sim r_{\pi} = \frac{V_T}{I_{CQ}} \beta = 50\Omega$$

$$I_{CQ} = 52\text{mA for } \beta = 100$$

$$\text{CB : } R_{in} \sim \frac{r_{\pi}}{\beta} = \frac{V_T}{I_{CQ}} = 50\Omega$$

$$I_{CQ} = 0.52\text{ mA for } \beta = 100$$

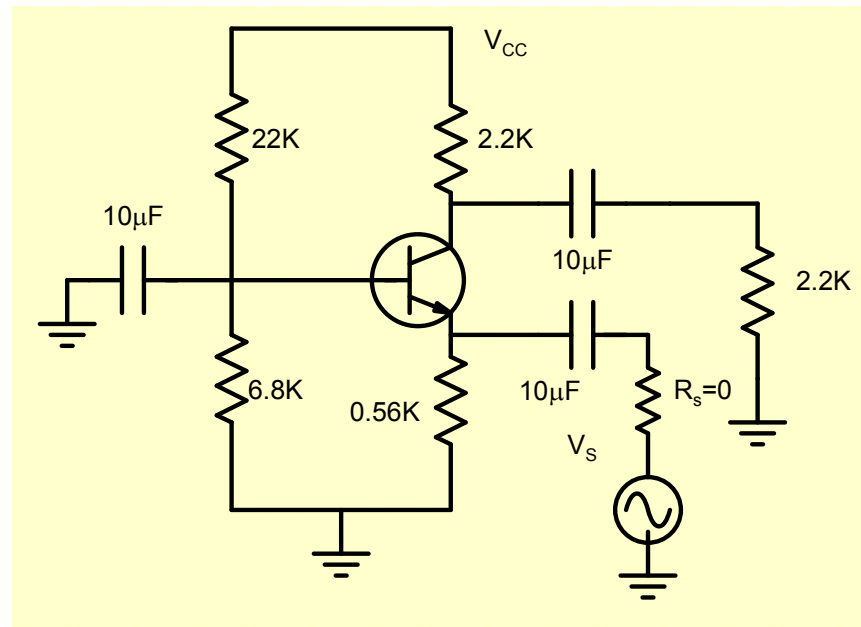


$$A_v = -110.7$$

$$R_{in} = 0.82k\Omega; R_o = 2.2k\Omega$$

$$\frac{A_v \times R_{in}}{R_o} = 41 < \beta$$

$$f_H = 5.8MHz$$



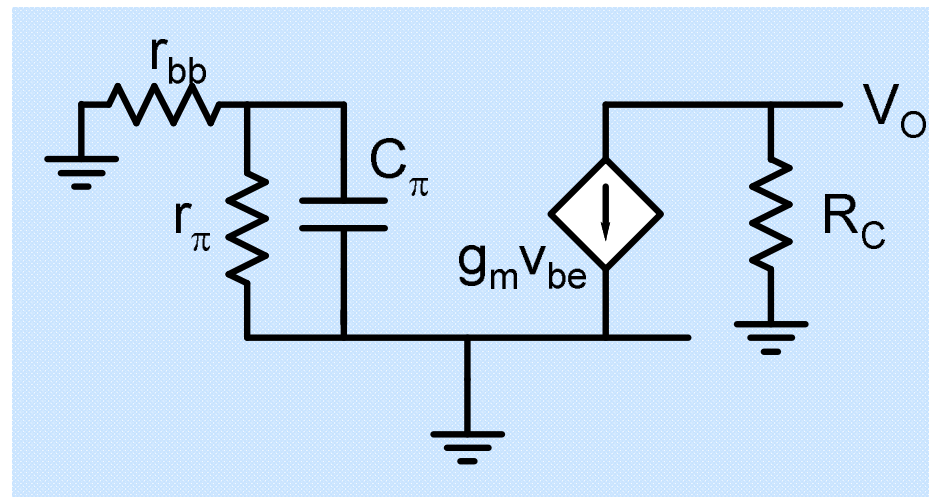
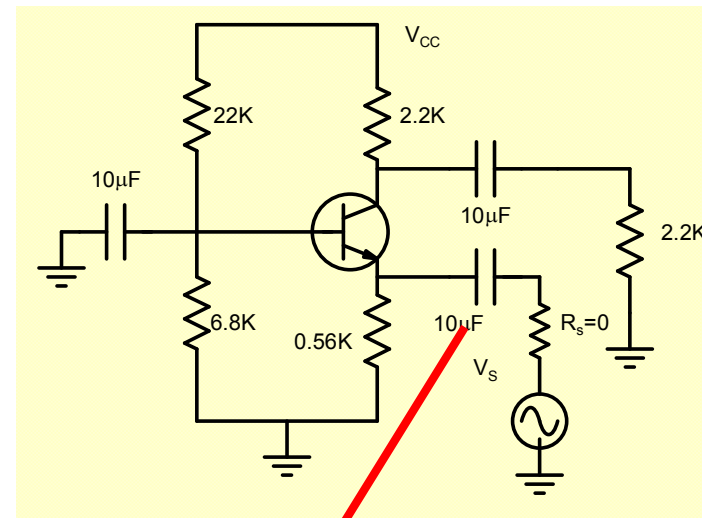
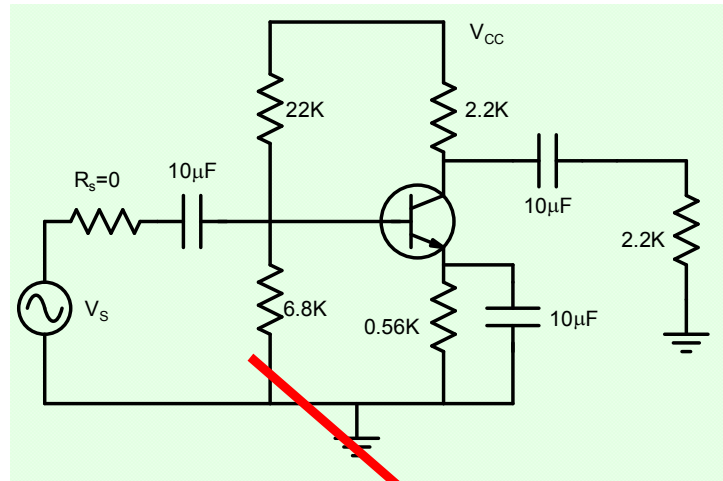
$$A_v = 110.7$$

$$R_{in} = 9.66\Omega; R_o = 2.2k\Omega$$

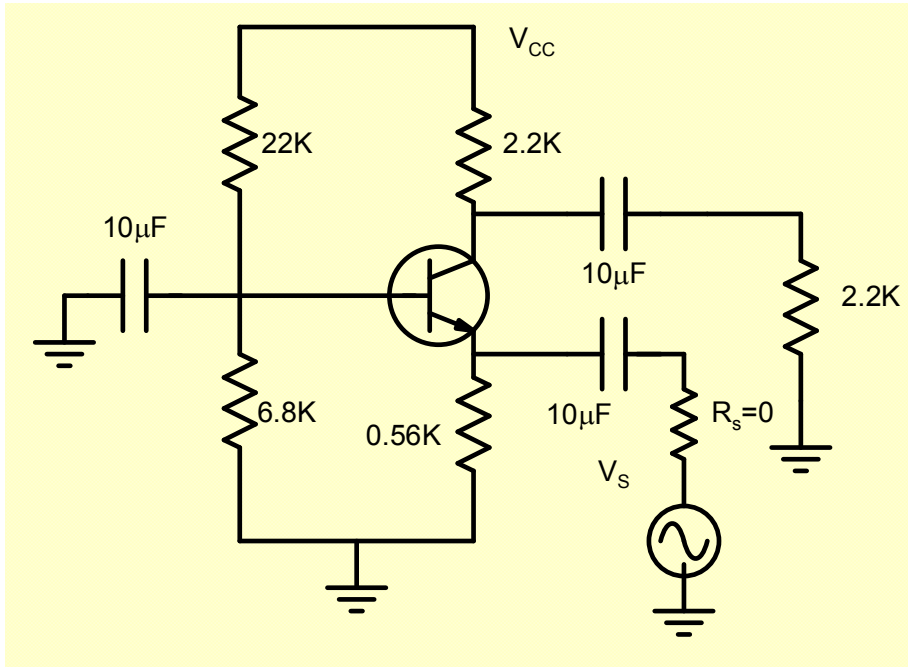
$$\frac{A_v \times R_{in}}{R_o} = 0.48$$

$$f_H = 5.8MHz$$

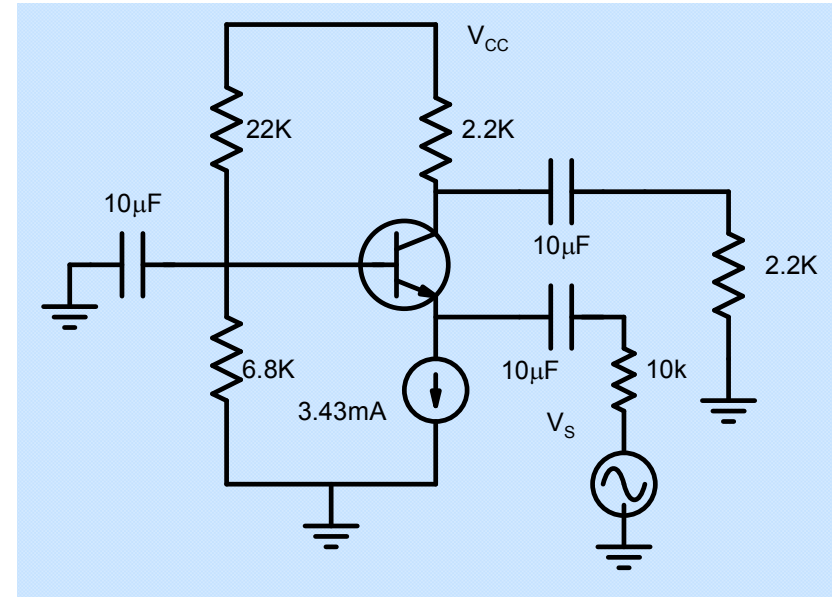
■ Critical analysis



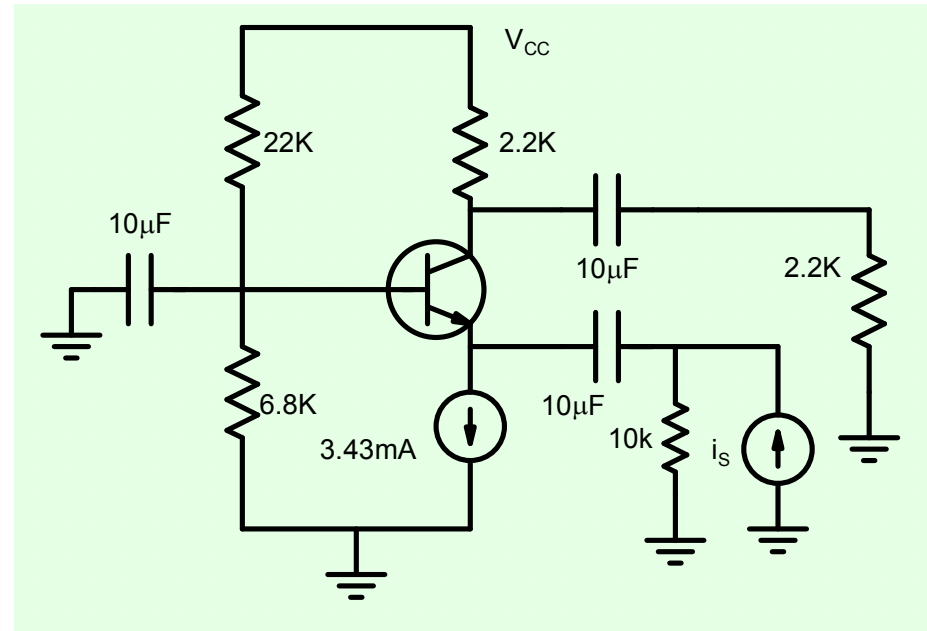
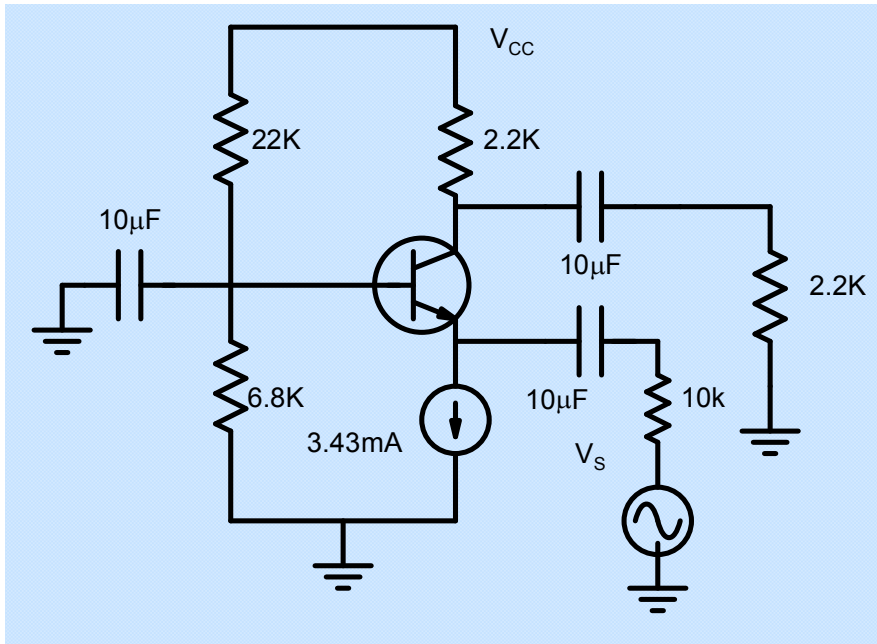
■ What – if ?



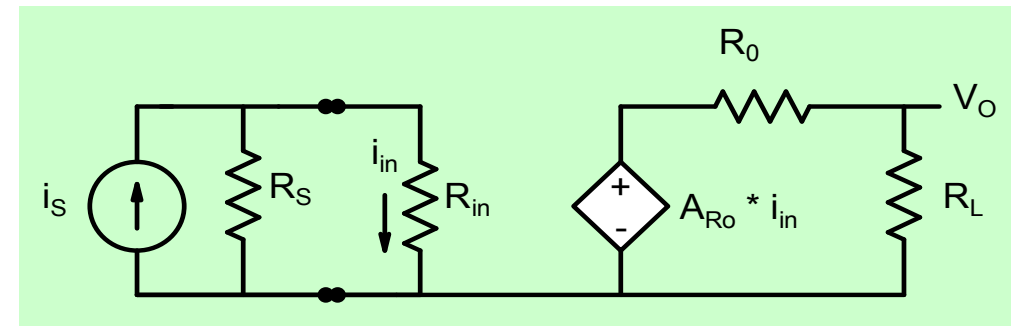
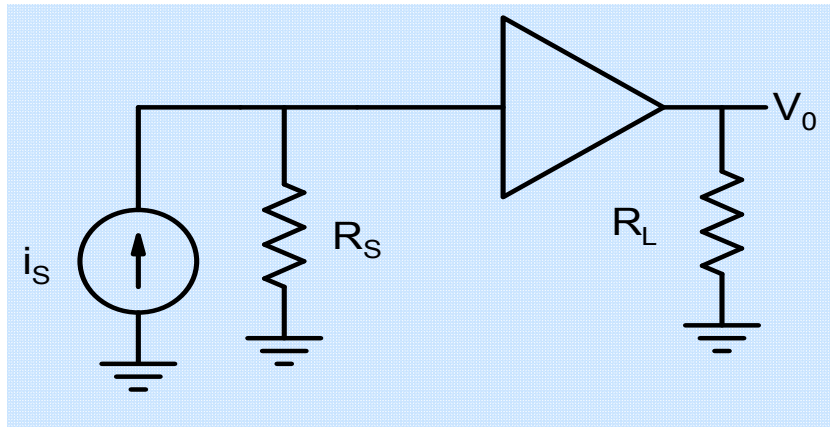
$$A_v = -110.7; f_H = 5.8MHz$$



$$CB : A_v = 0.1; f_H = 73.5MHz$$



Low input resistance is desirable for current input :
trans-impedance amplifier

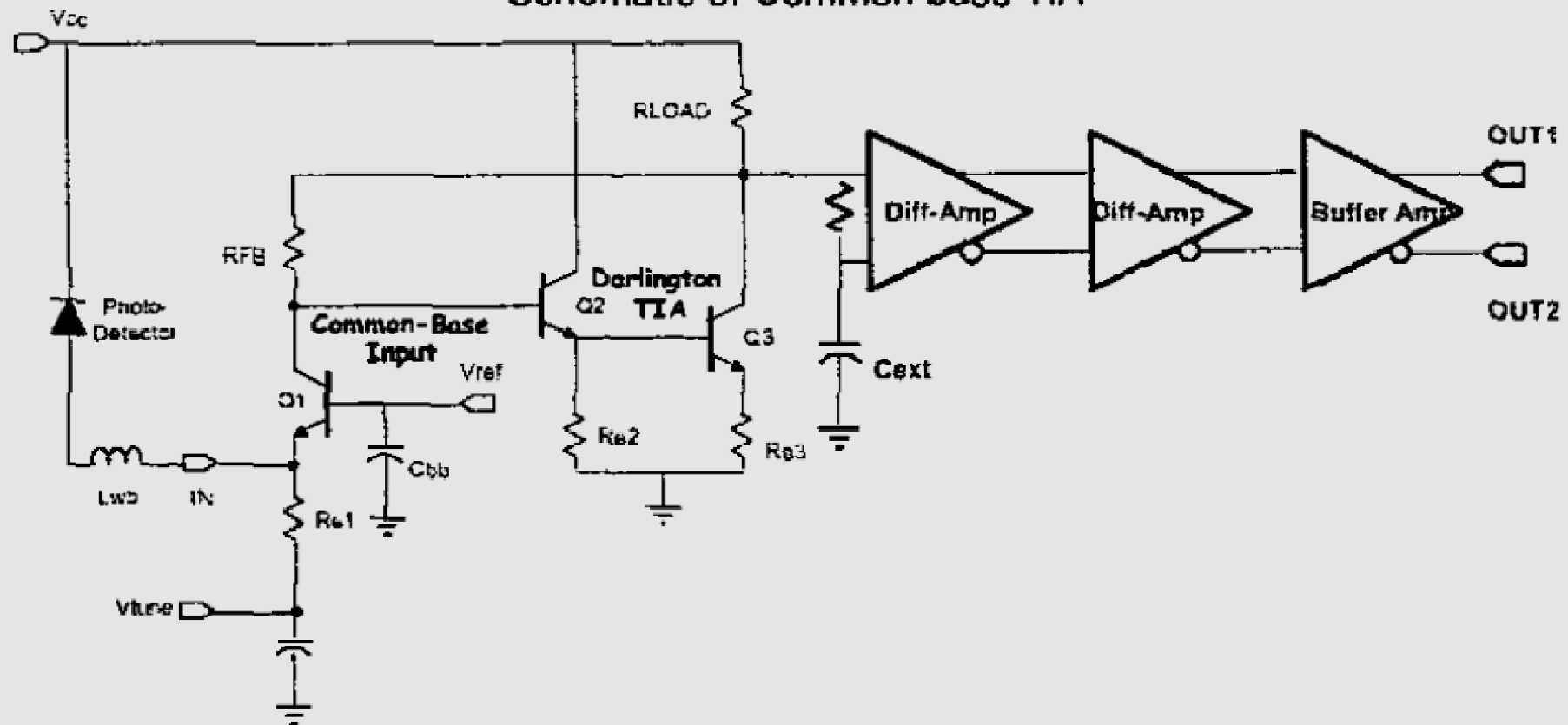


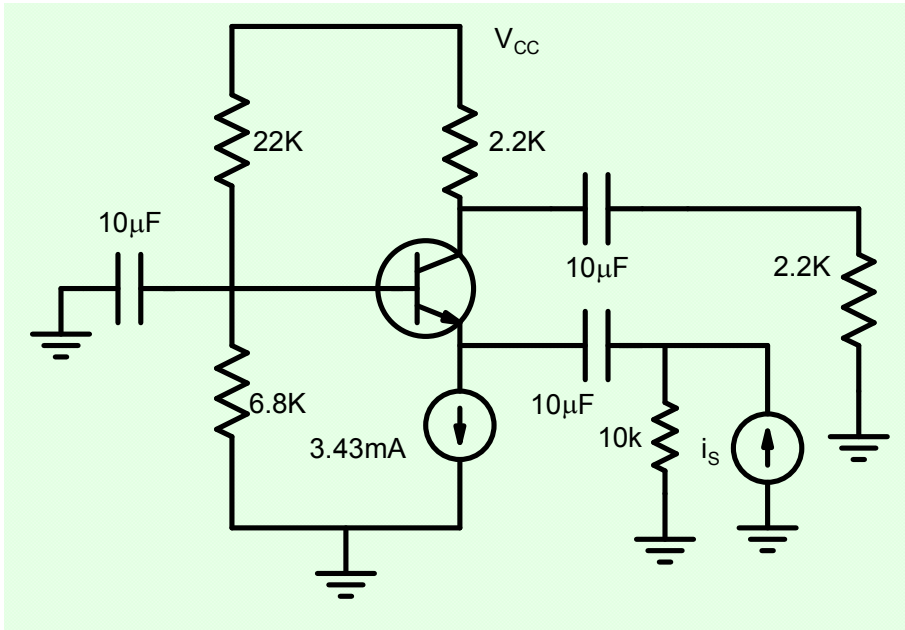
$$\frac{v_o}{i_s} = \left(\frac{1}{1 + \frac{R_{in}}{R_s}} \right) \times \left(\frac{1}{1 + \frac{R_o}{R_L}} \right) \times A_{RO}$$

AN InP HBT COMMON-BASE AMPLIFIER WITH TUNABLE TRANSIMPEDANCE FOR 40 GB/S APPLICATIONS

Kevin W. Kobayashi

Schematic of Common-base TIA





■ What – if ?

