

EE210: Microelectronics-I

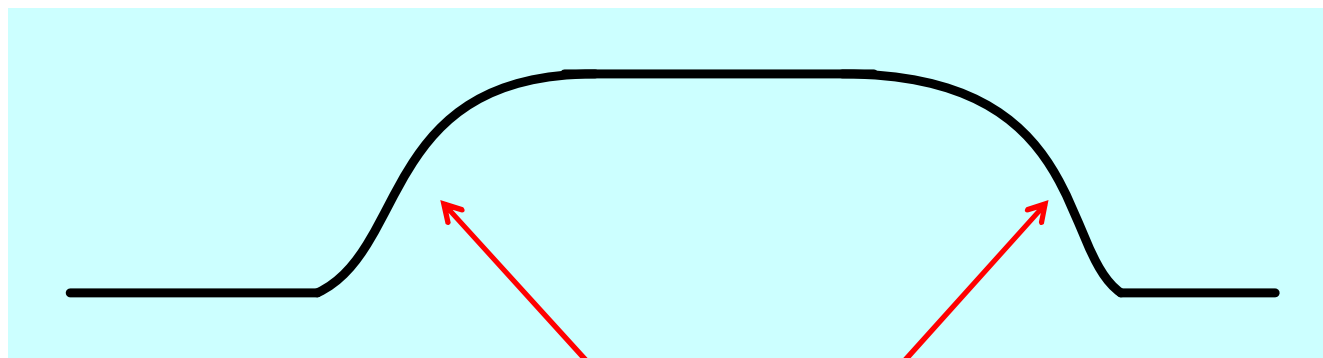
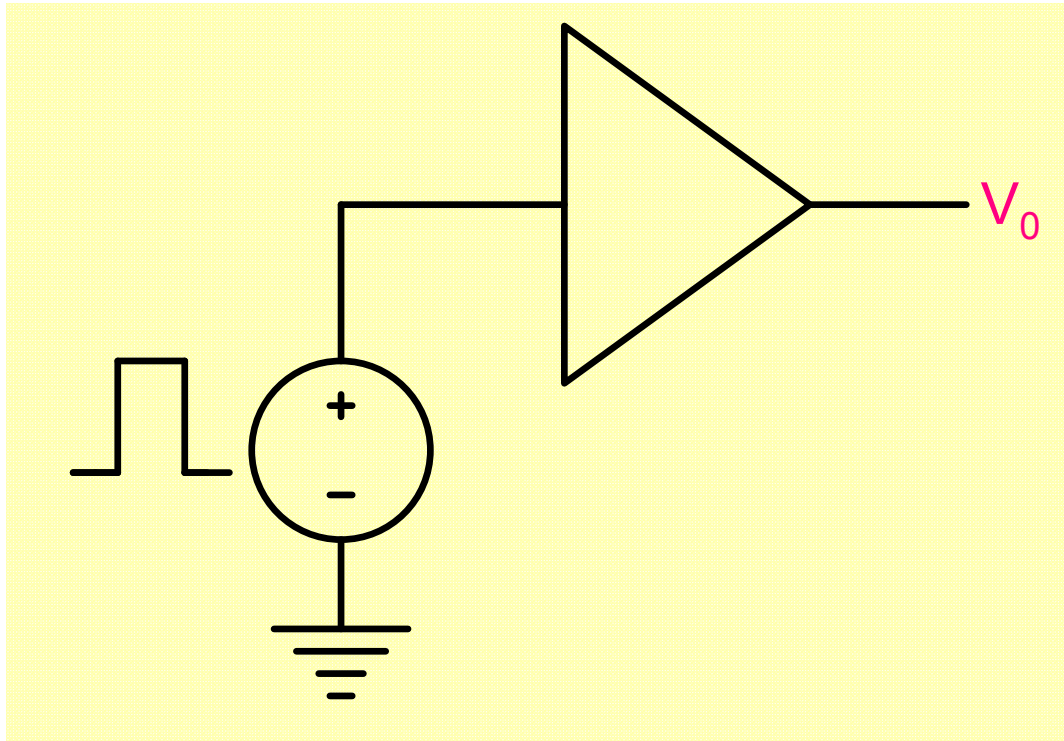
Lecture-2 Amplifier Characteristics-2

Instructor: Y. S. Chauhan

Slides from:

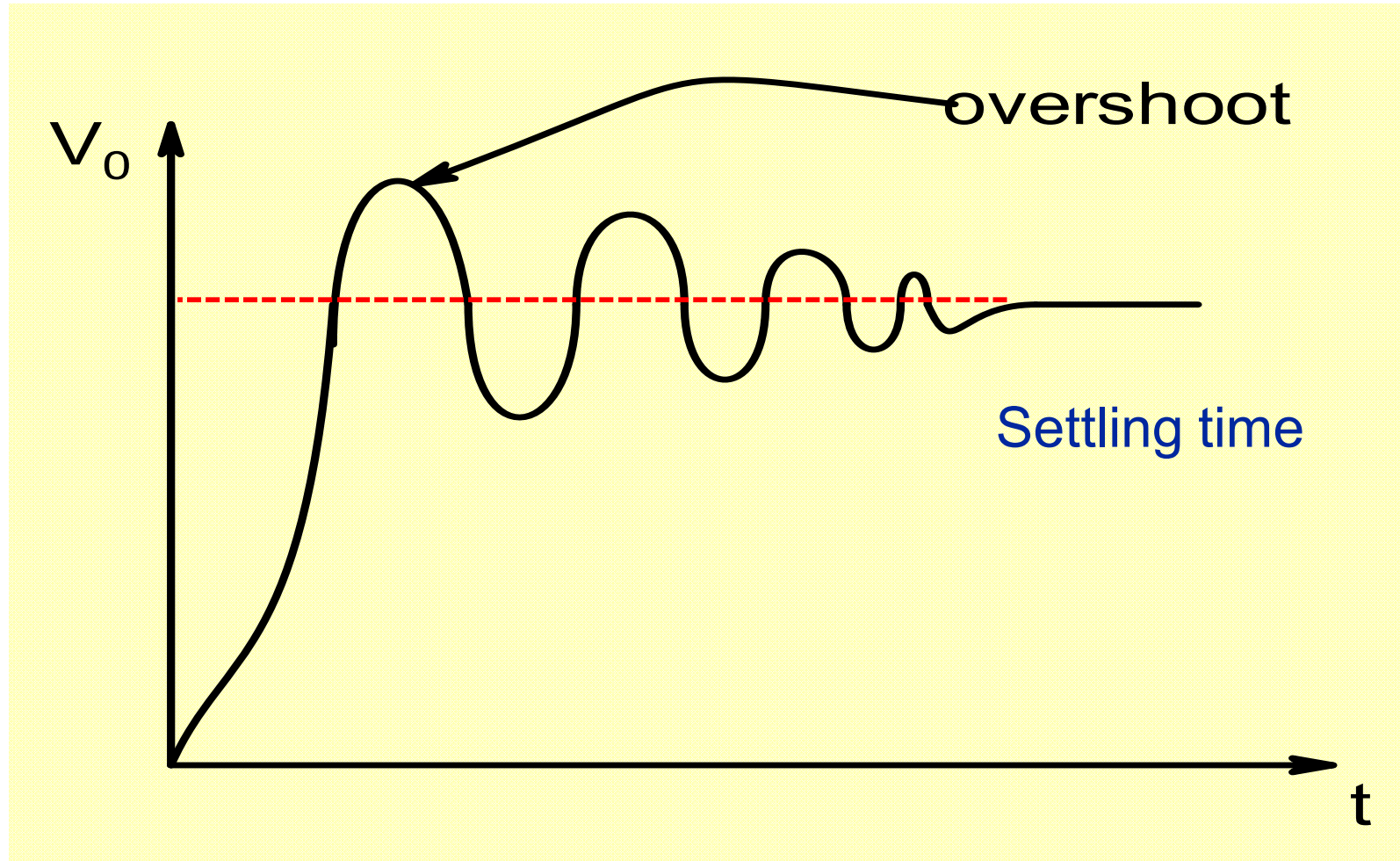
B. Mazhari
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Transient Response



Rise time and fall time

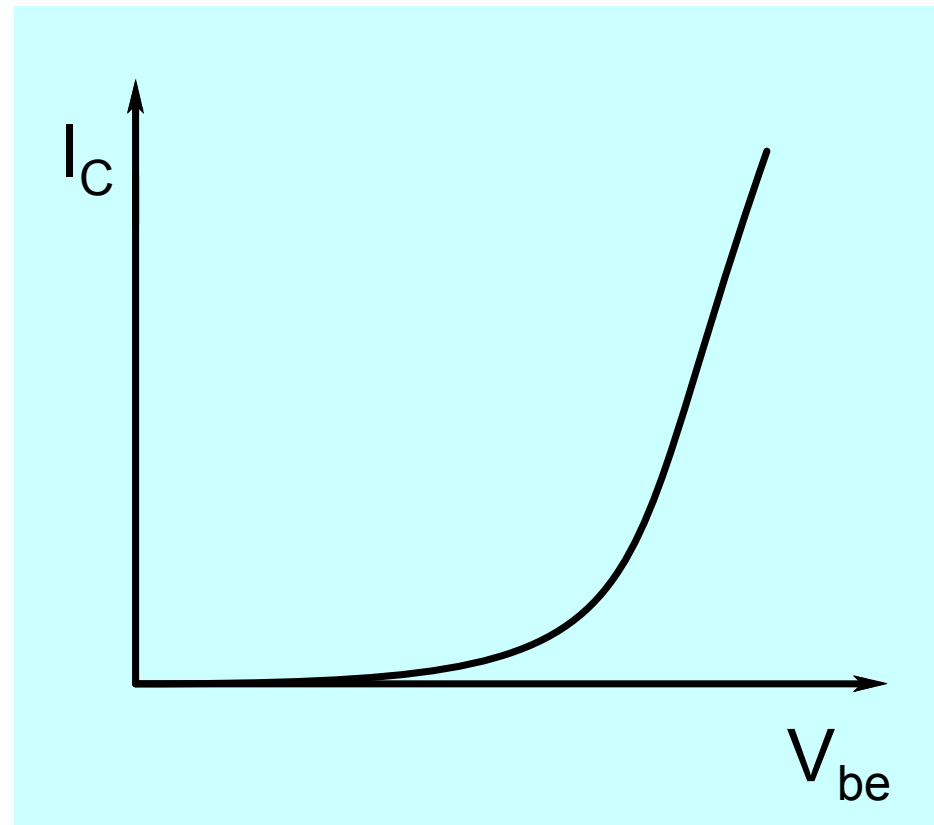
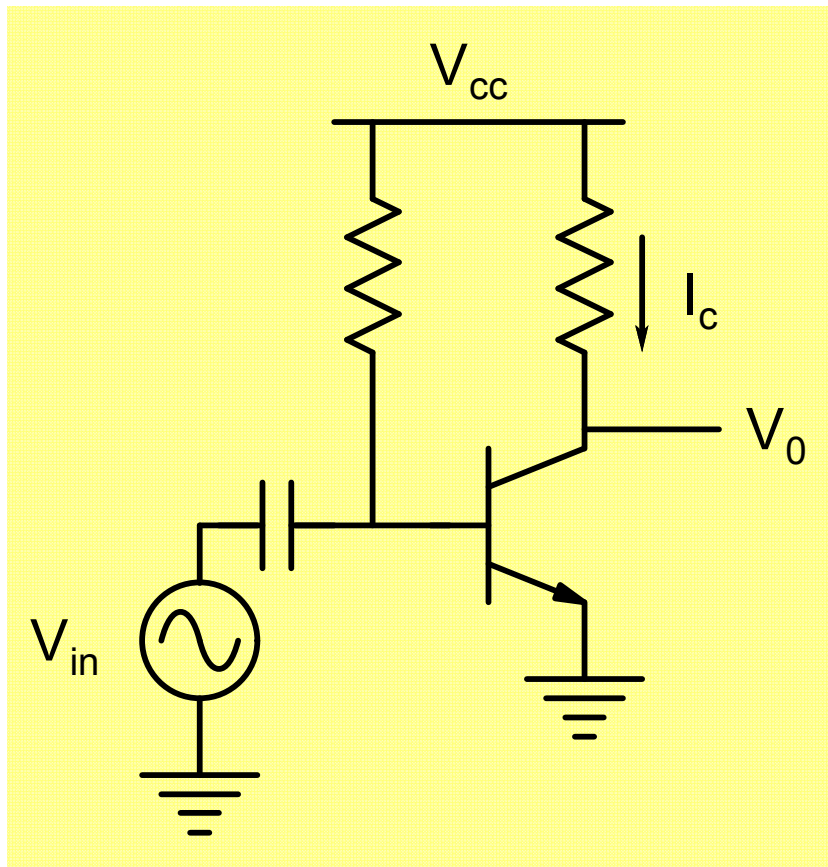
Overshoot and time Settling time

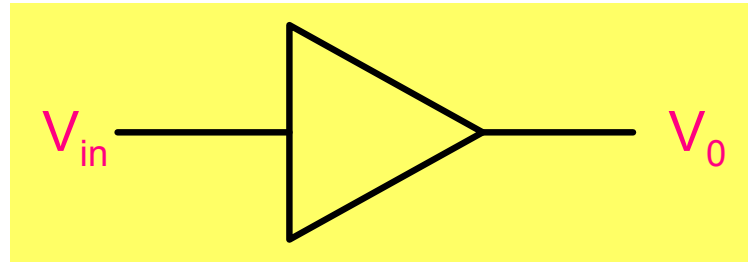


Distortion

$$V_o = A_v(f, \mathbf{V}_{in}, R_L, R_S, T) \times v_{in} + \tilde{e}_N$$

All amplifiers are nonlinear because transistors used for building amplifier are nonlinear elements.





$$V_{in} = a_0 \sin \omega t$$

$$V_0 = b_0 + b_1 \sin \omega t + b_2 \sin 2\omega t + b_3 \sin 3\omega t + \dots$$

$$HD_2 = \frac{b_2}{b_1} \times 100$$

$$HD_3 = \frac{b_3}{b_1} \times 100$$

$$THD = \frac{\sqrt{b_2^2 + b_3^2 + \dots}}{b_1} \times 100$$

Example

$$V_0 = kV_{in} + \frac{k}{10}V_{in}^2$$

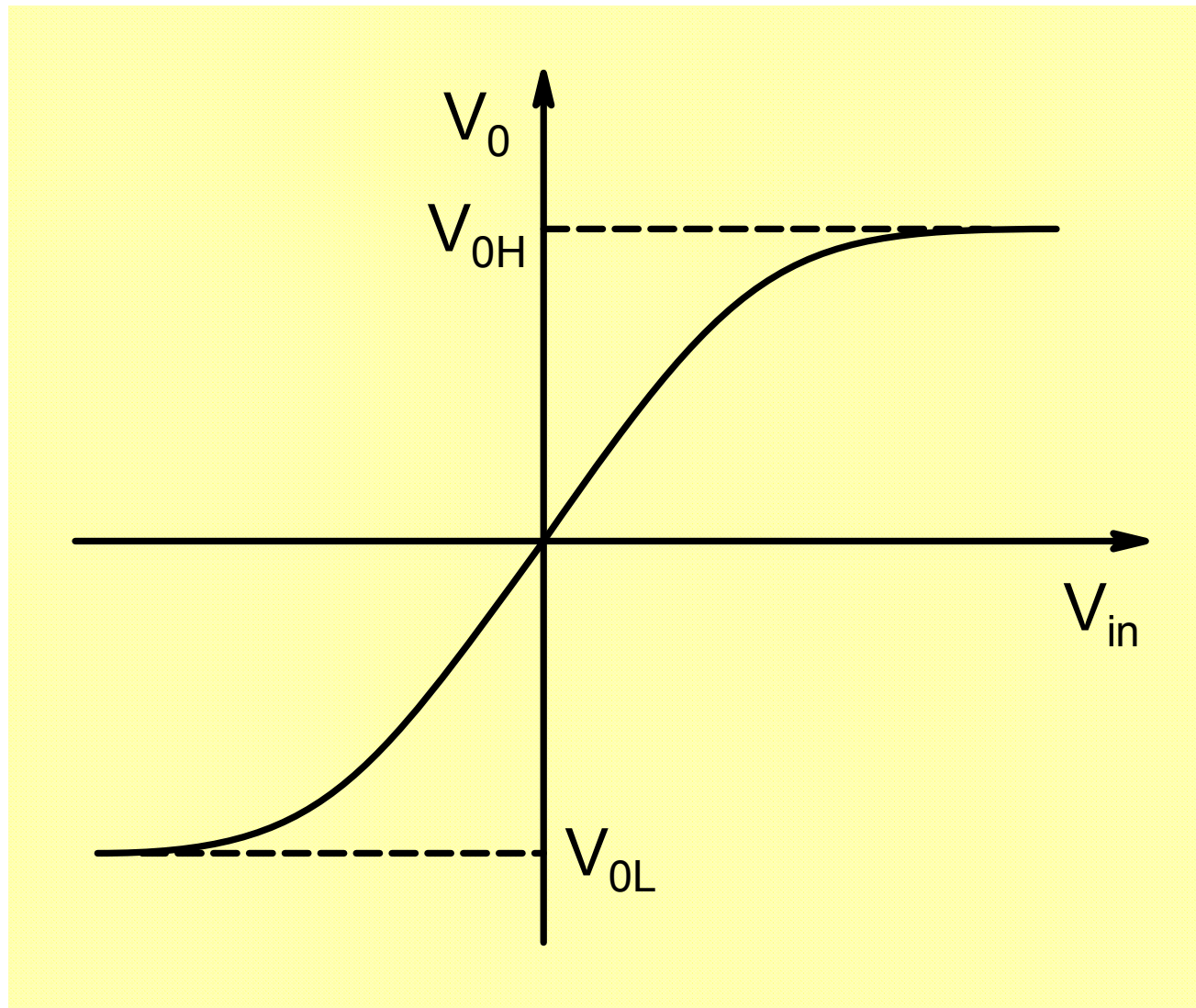
$$V_{in} = a_0 \sin \omega t$$

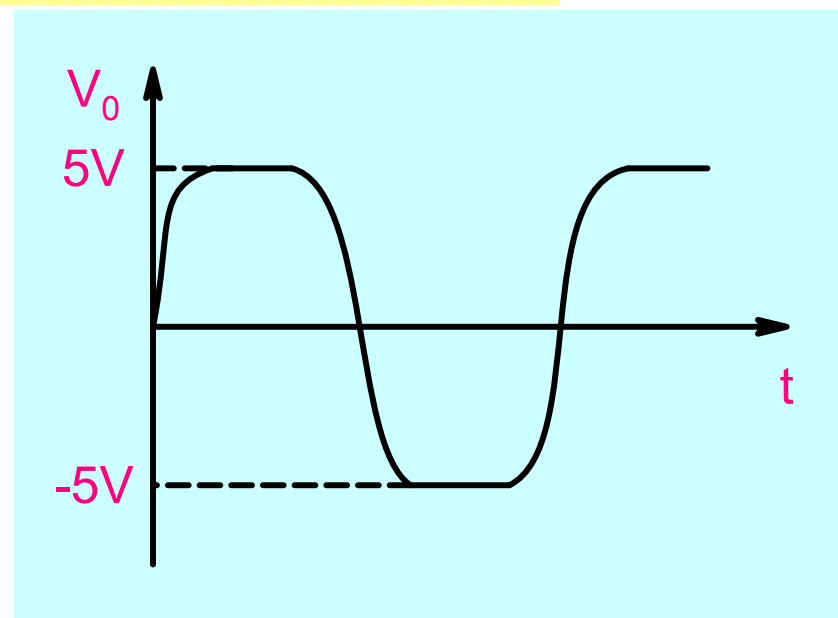
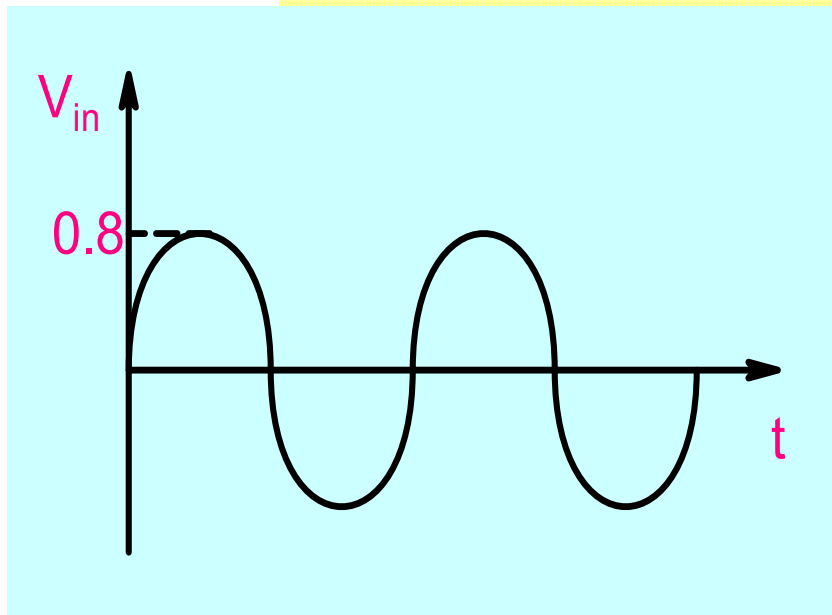
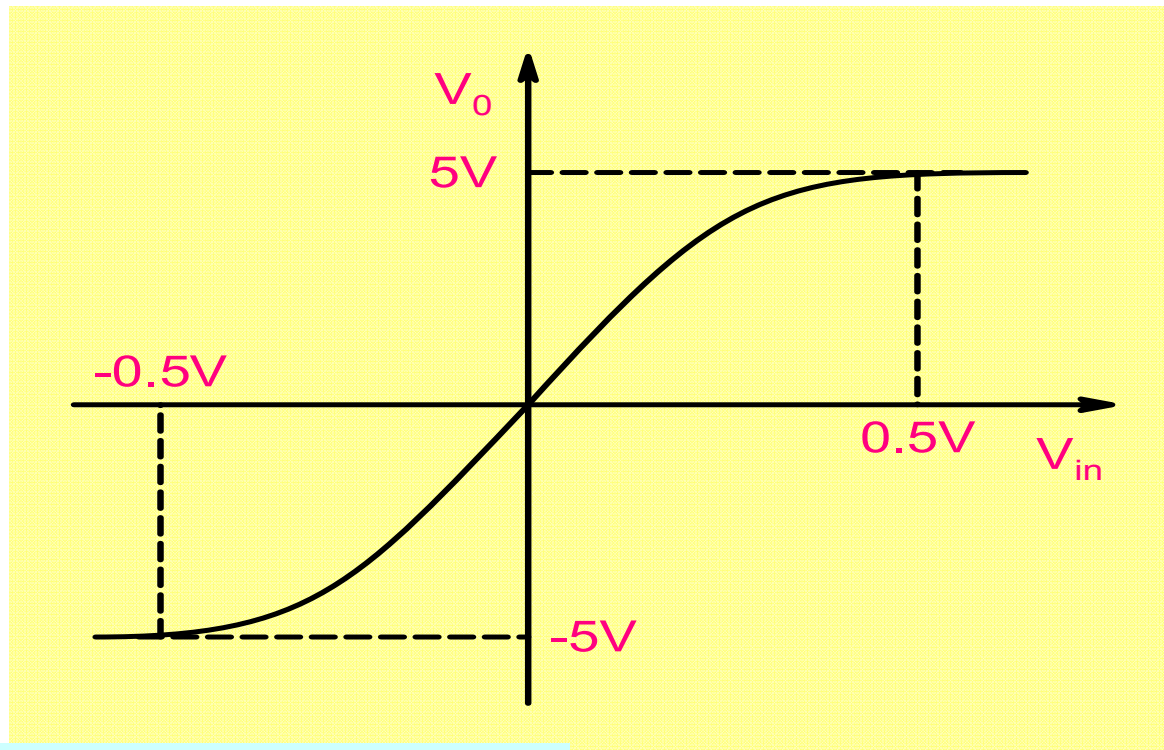
$$V_0 = \frac{ka_0^2}{20} + ka_0 \sin \omega t - \frac{ka_0^2}{20} \cos 2\omega t$$

$$THD = HD_2 = \frac{ka_0^2 / 20}{ka_0} \times 100 = 5a_0$$

Distortion increases with magnitude of input signal !

Maximum Voltage Swing

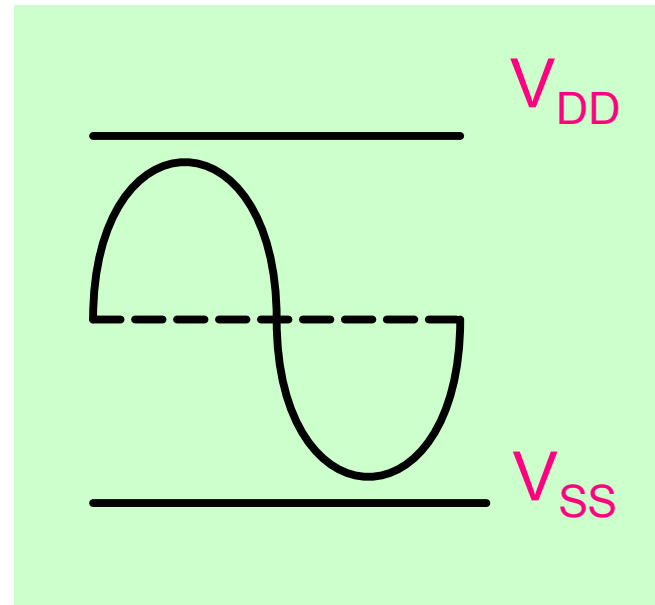
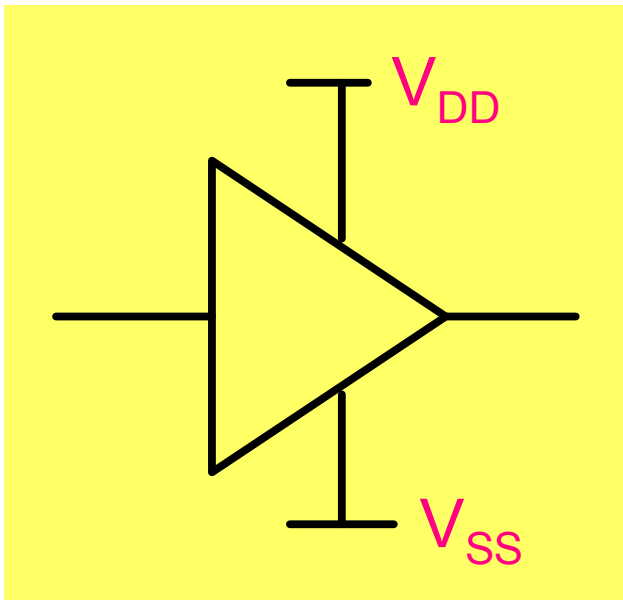




Rail-to-Rail output voltage swing

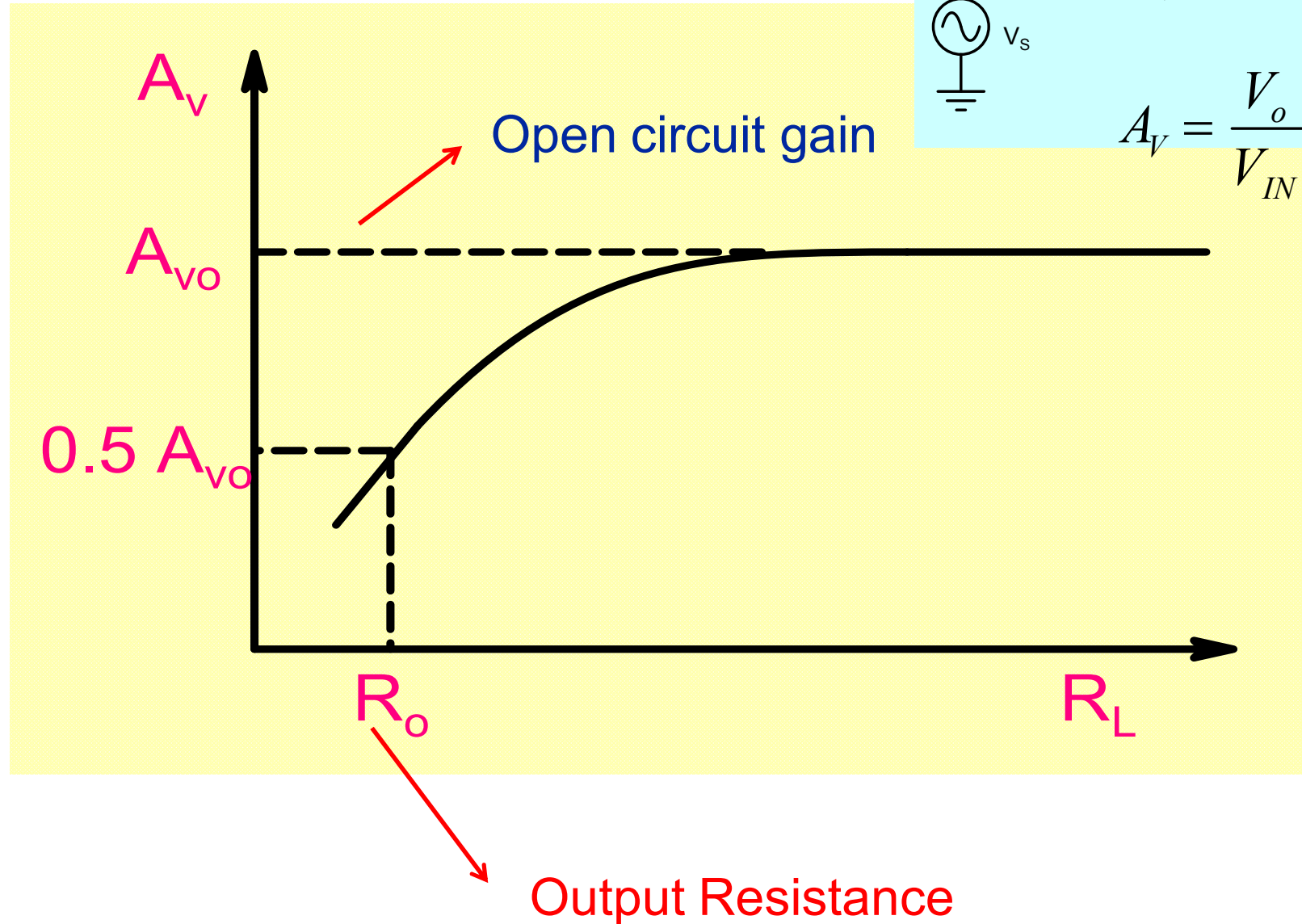
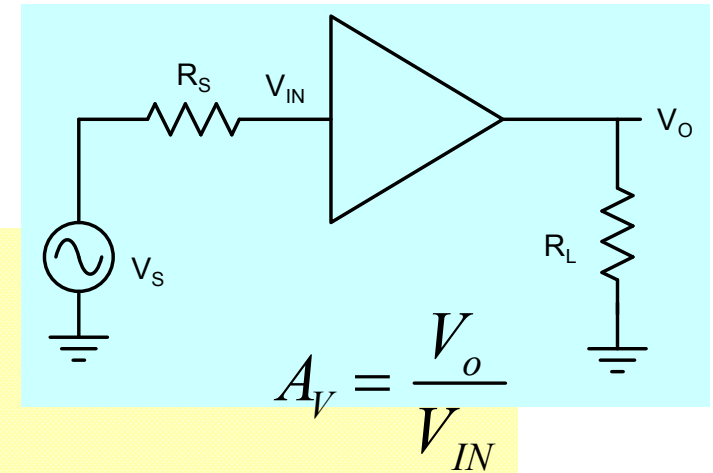
$$V_{OH} \leq V_{DD}$$

$$V_{OL} \geq V_{SS}$$

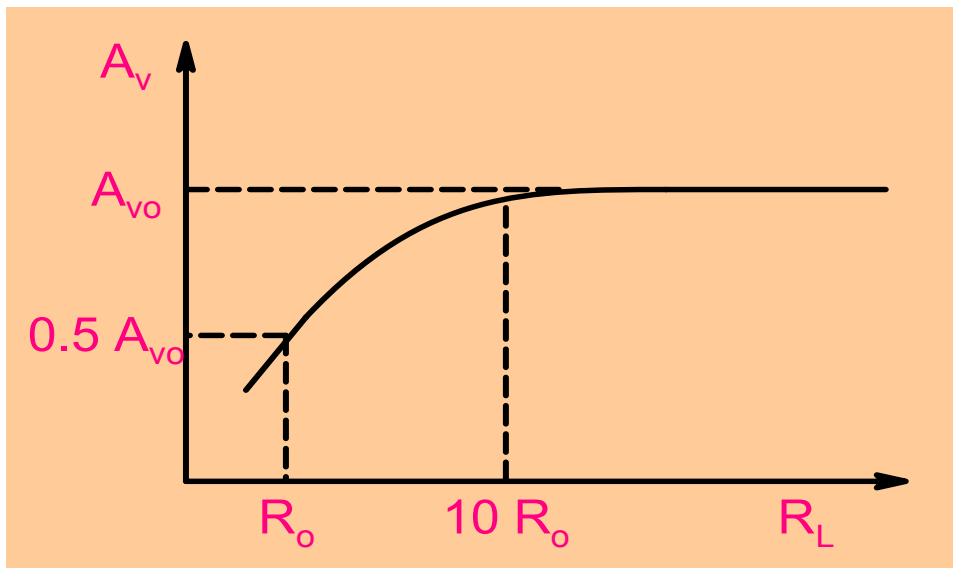
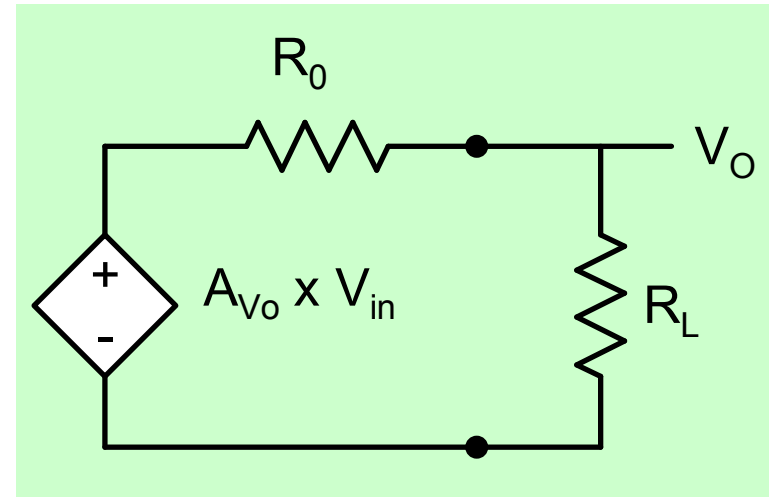
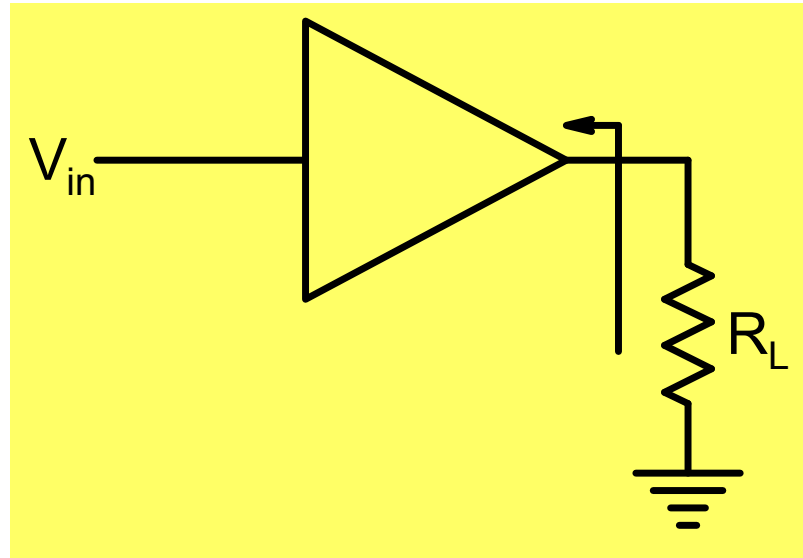


$$V_o = A_v(f, V_{in}, R_L, R_S, T) \times v_{in} + \tilde{e}_N$$

Effect of Load Resistance

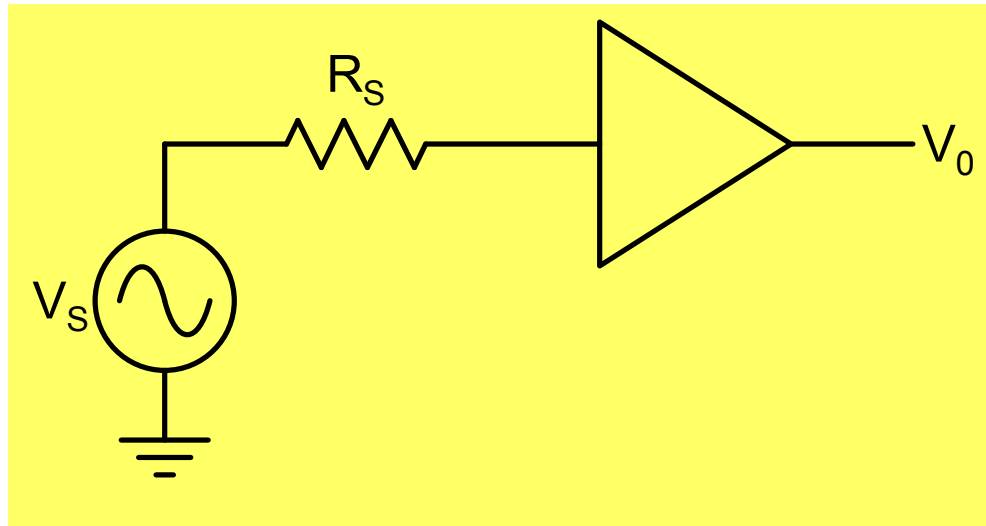


Output Resistance

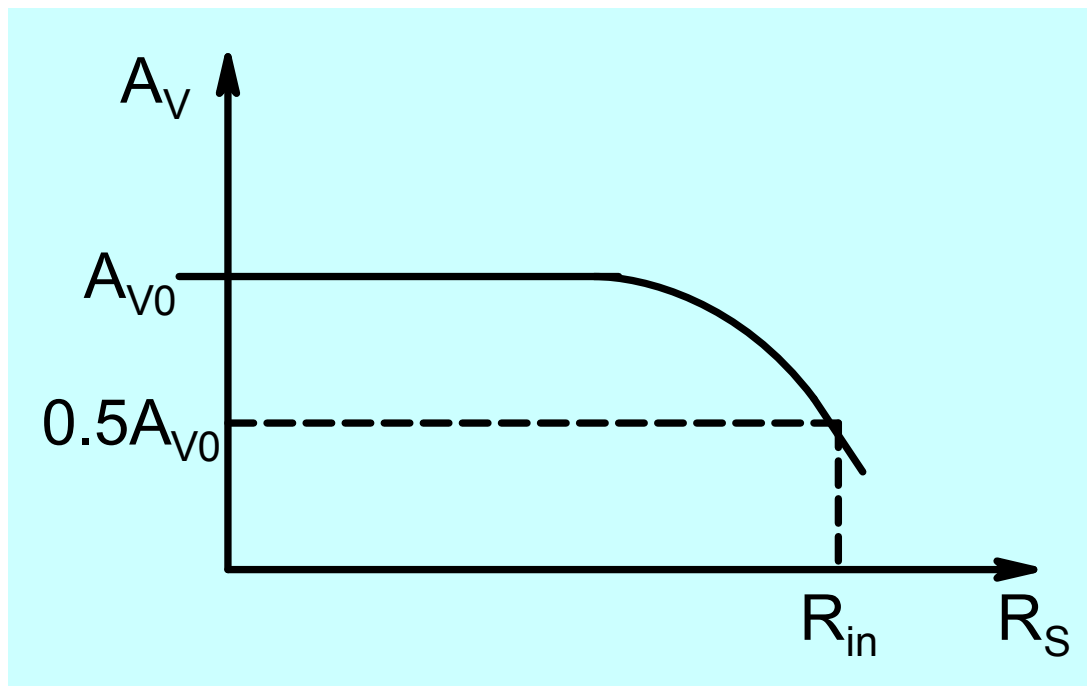


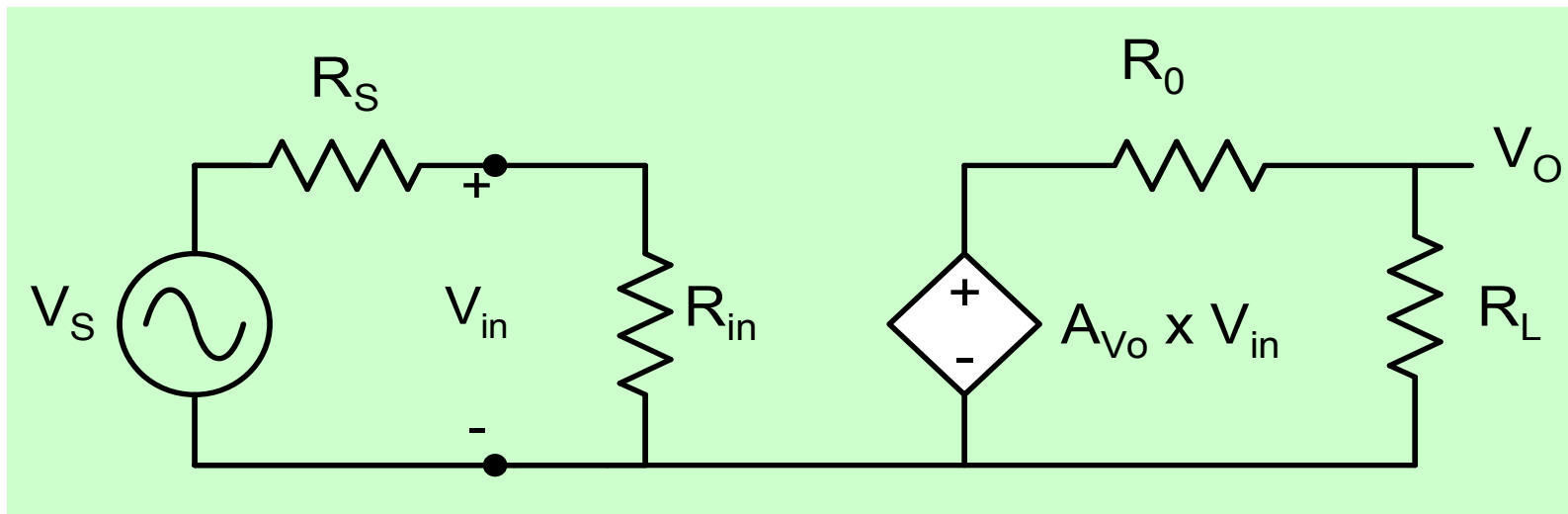
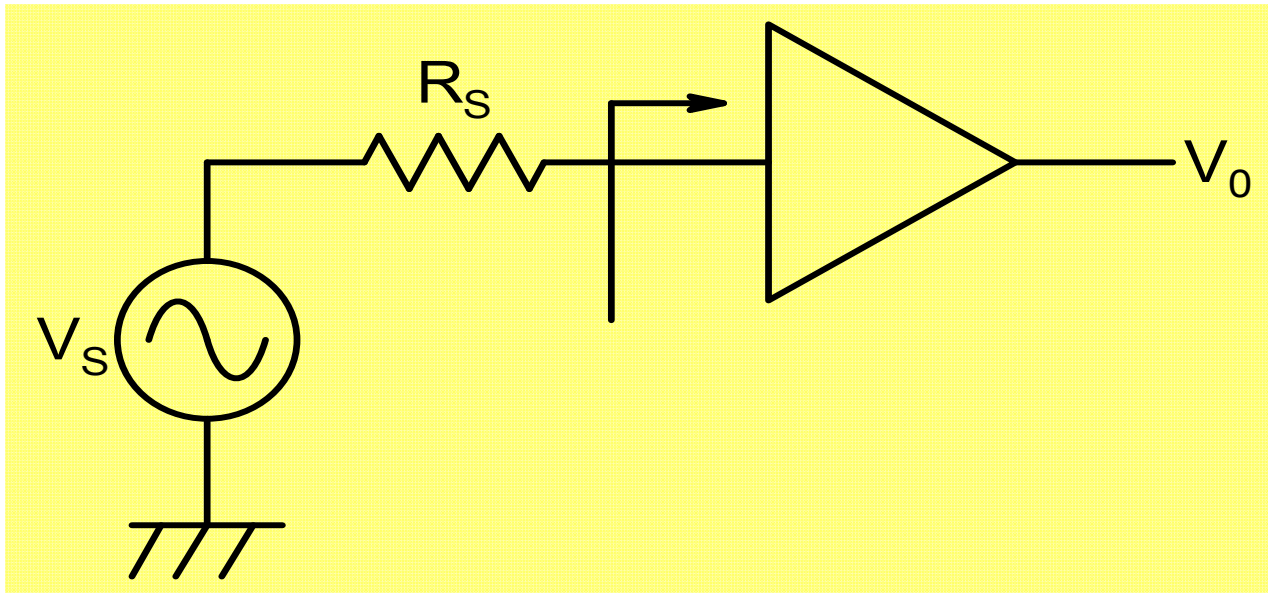
$$V_0 = A_{V_0} \times V_{in} \left(\frac{R_L}{R_0 + R_L} \right)$$

Input Resistance



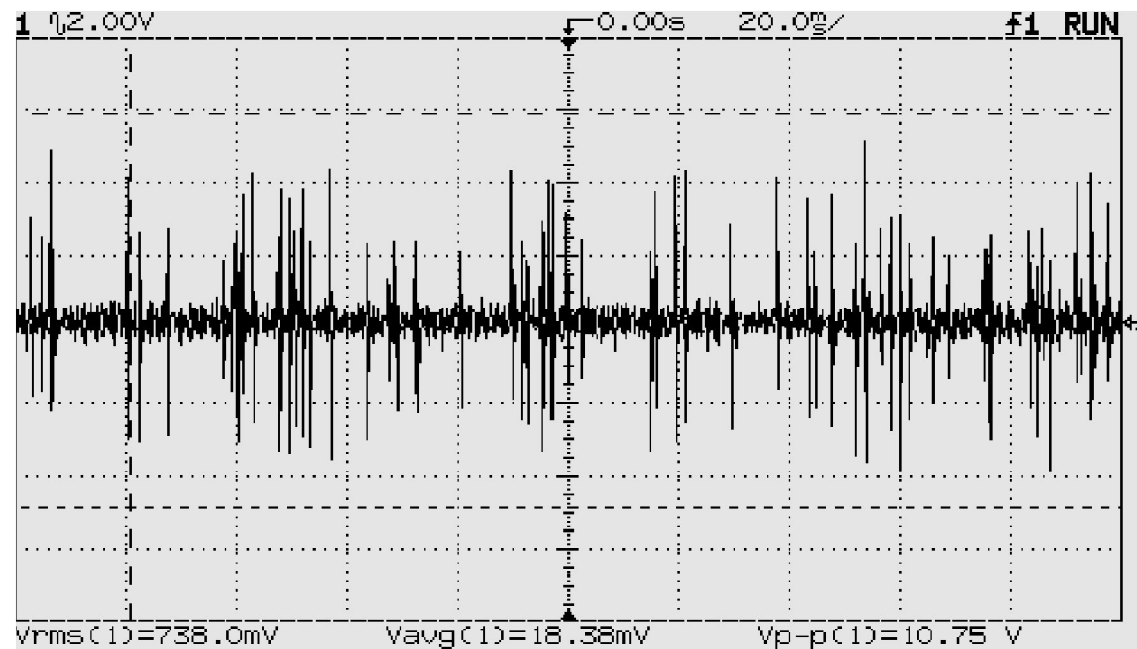
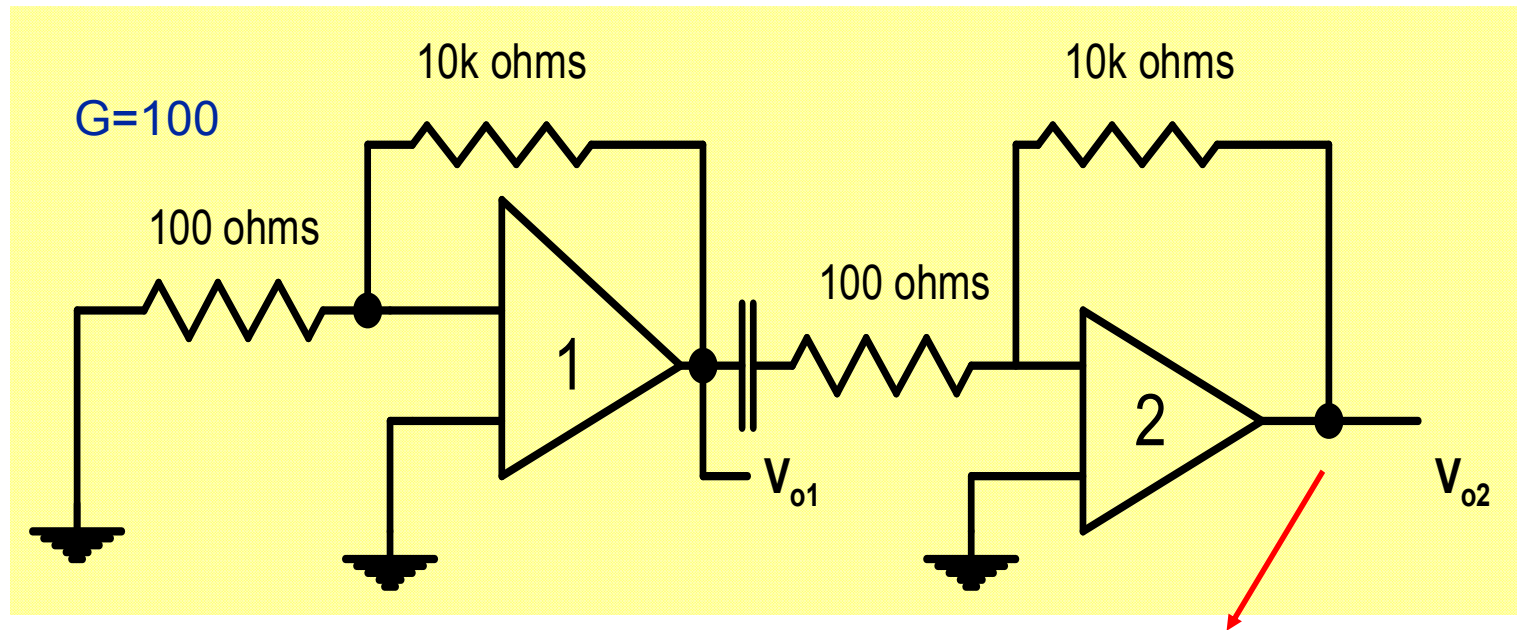
$$A_{VS} = \frac{V_0}{V_S}$$

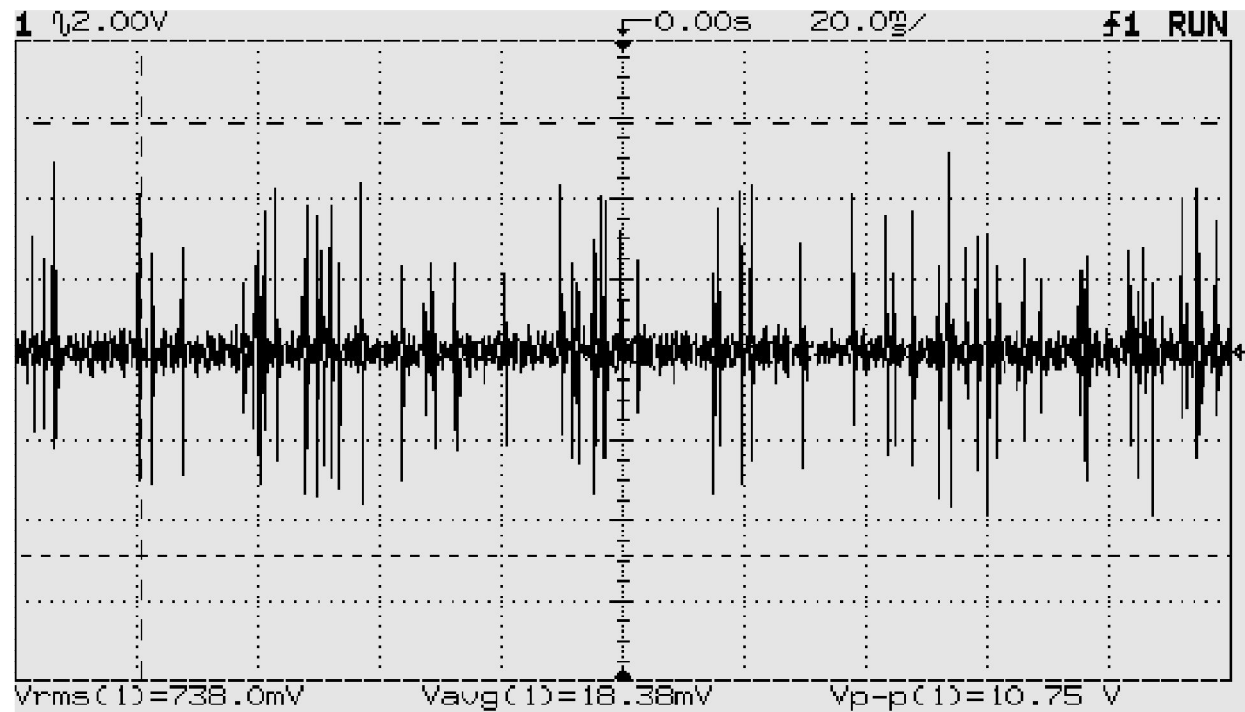




$$A_{VS} = A_{V0} \times \left(\frac{R_{in}}{R_S + R_{in}} \right) \times \left(\frac{R_L}{R_0 + R_L} \right)$$

NOISE





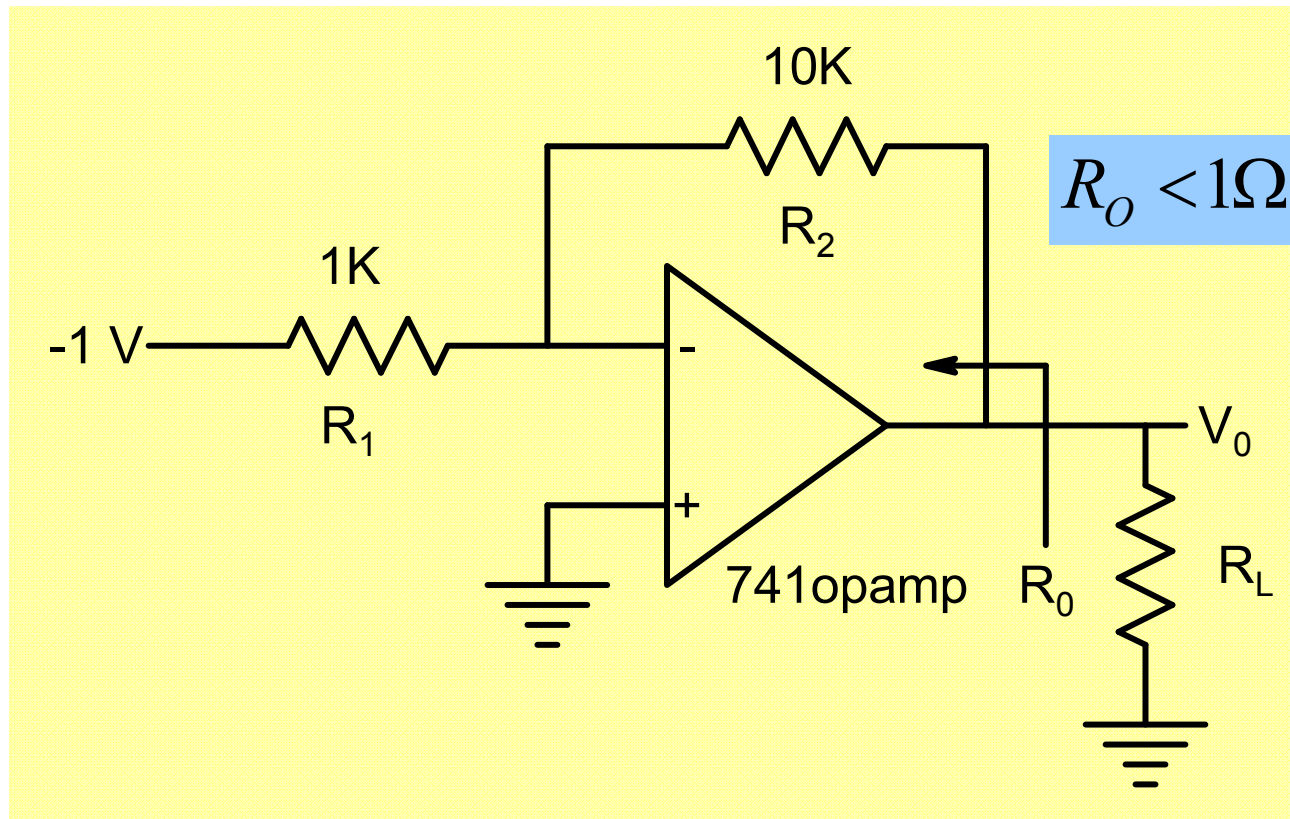
$$F = \frac{\text{Total Output Noise Power}}{\text{Output Noise due to Input Noise Only}}$$

$$F=1.26$$

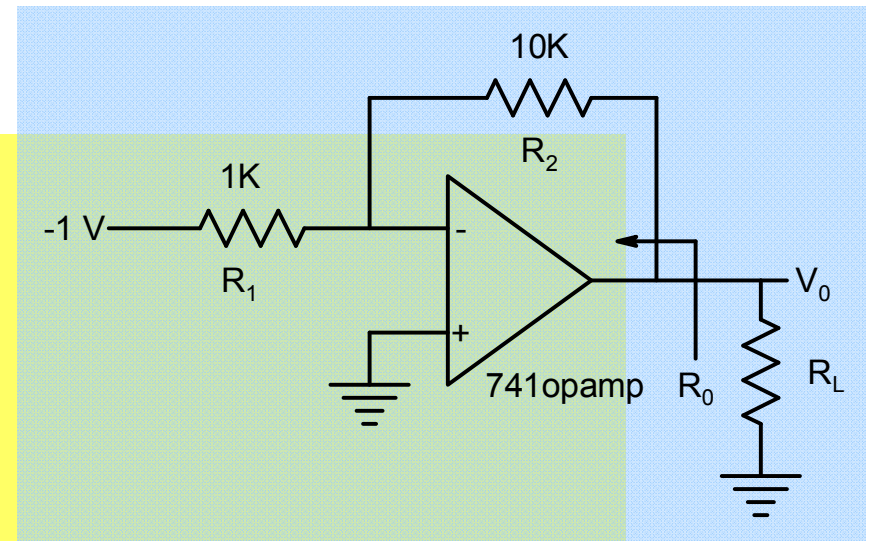
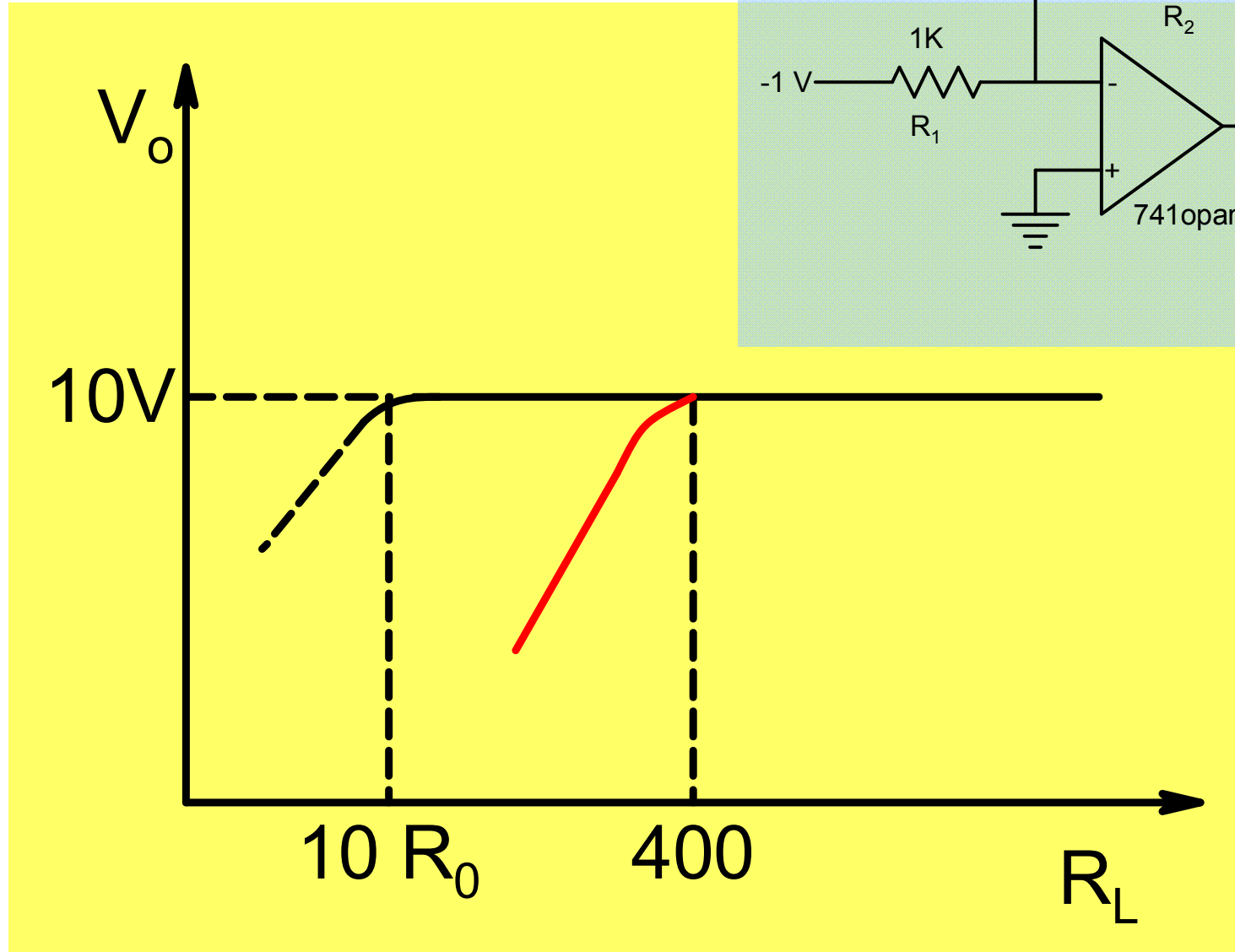
$$\text{Noise Figure : } NF=10 \times \text{Log}_{10}(F)$$

$$NF=1 \text{ dB}$$

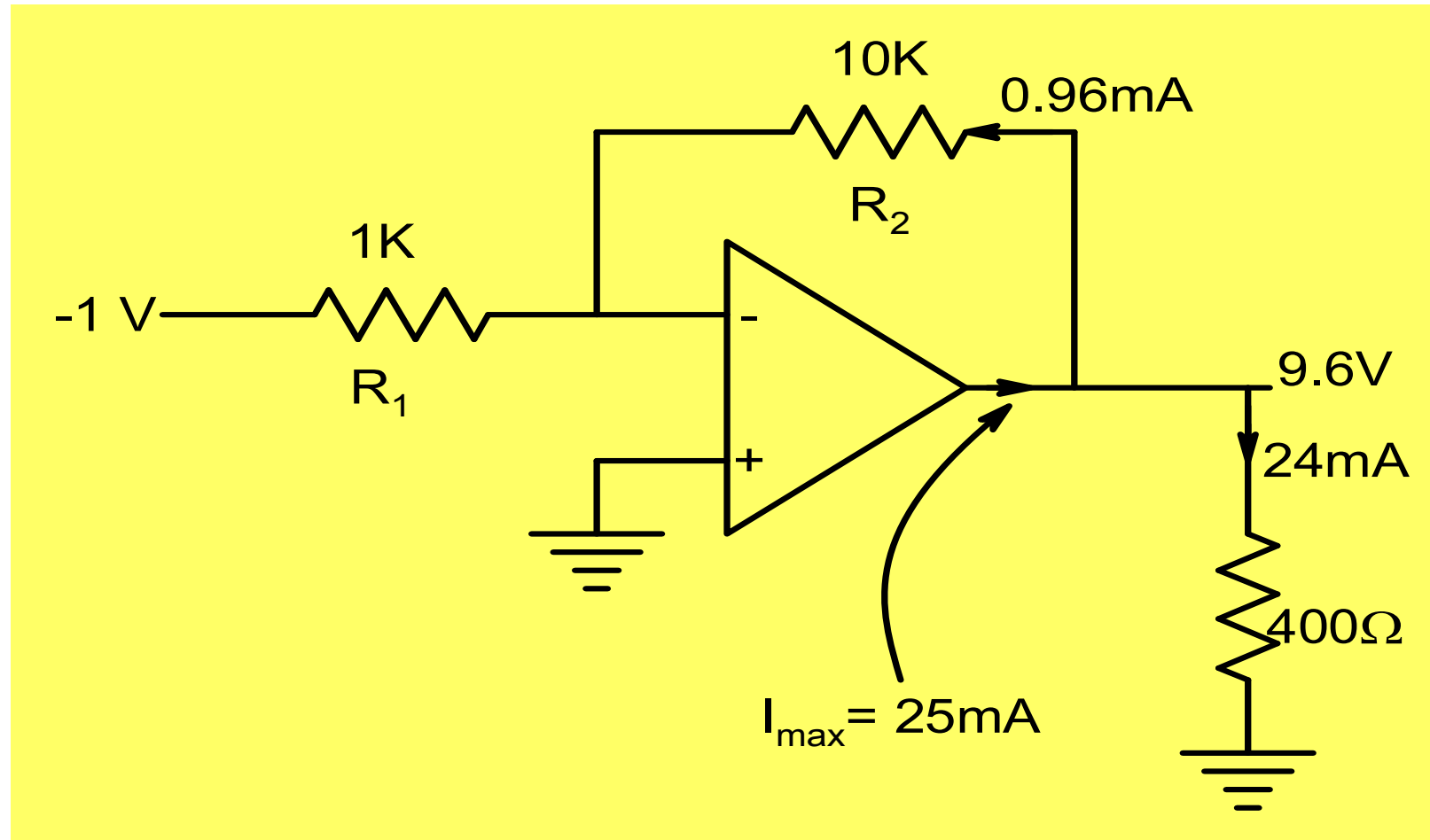
Maximum Current Driving Capability

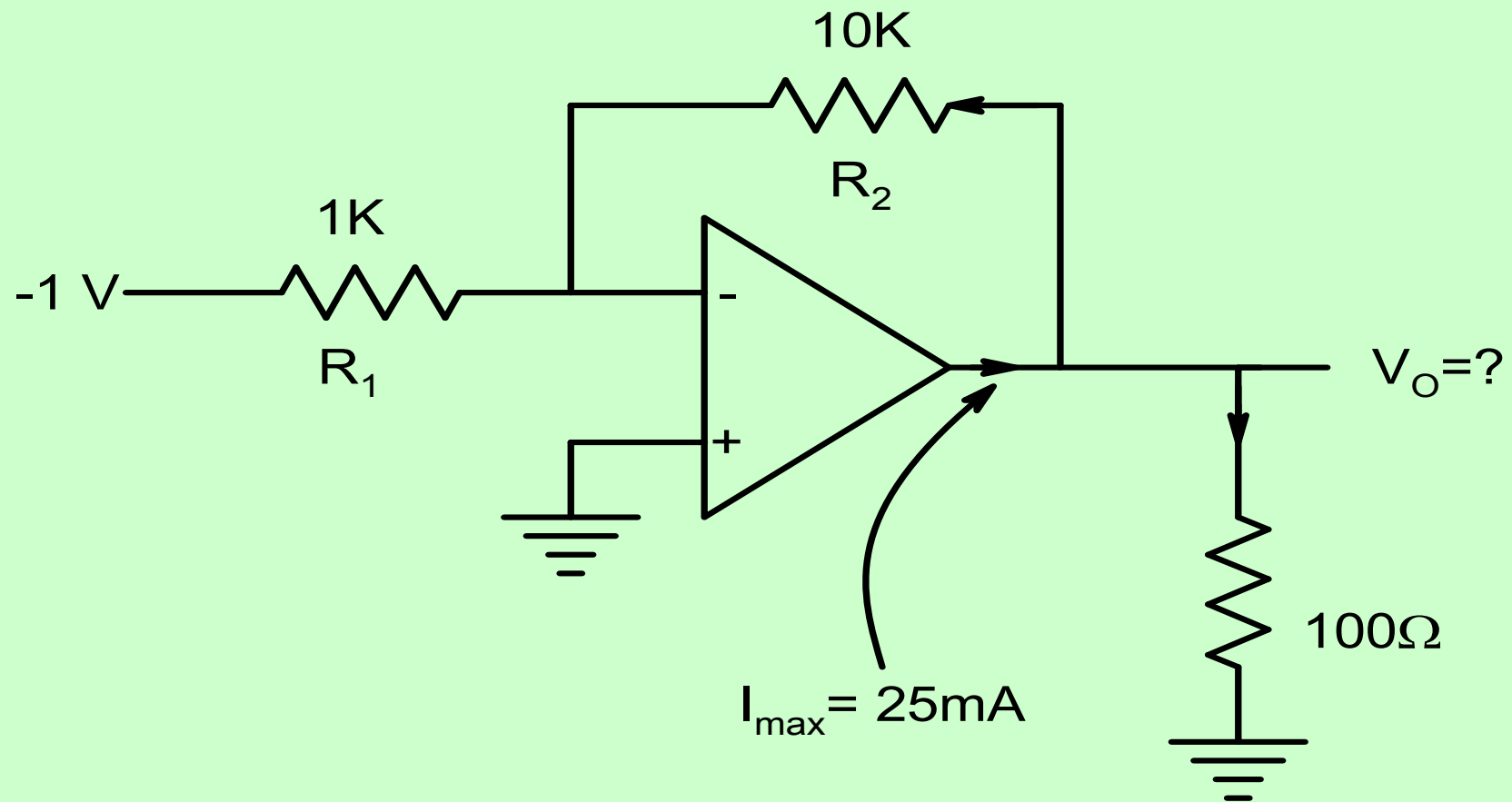


$$V_0 = -\frac{R_2}{R_1} V_{in} = 10V$$



Opamp has maximum current drive capability of 25mA





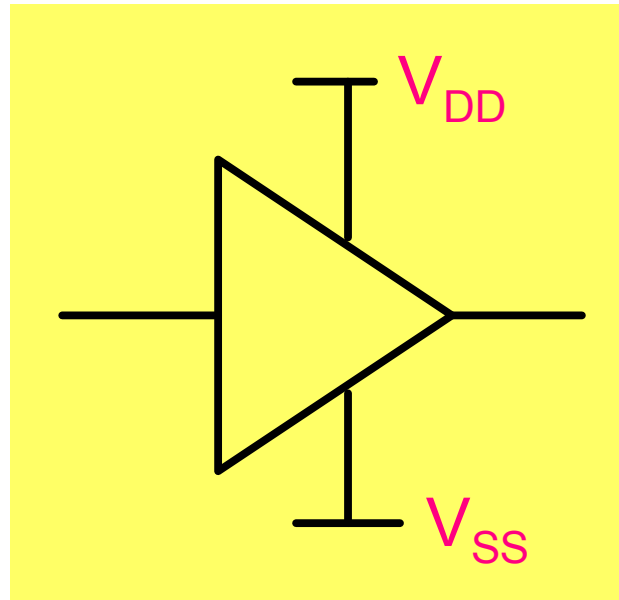
Summary

$$v_o = A_v(f, V_{in}, R_L, R_S, T) \times v_{in} + \tilde{e}_N$$

The diagram illustrates the relationship between the variables in the equation and specific circuit parameters. Red arrows point from each variable in the equation to a corresponding parameter in a blue box below:

- A_v points to A_{Vo}
- f points to BW
- V_{in} points to THD
- R_L points to R_O
- R_S points to R_{IN}
- T points to NF

Other specifications



$$\eta = \frac{P_L}{P_{\text{supply}}}$$

Power supply rejection ratio, common mode rejection ratio.....