

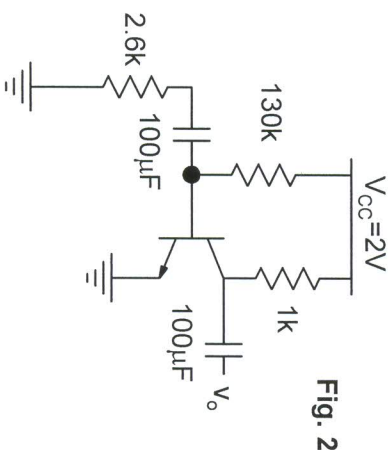
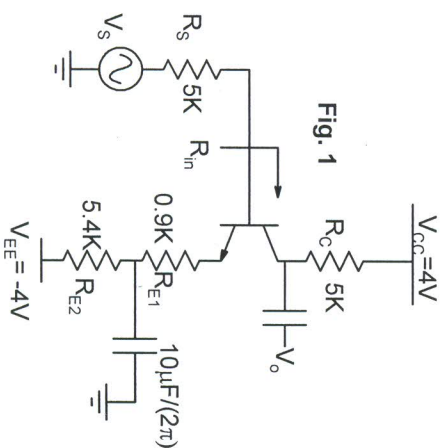
Date: 17.2.14

Max. Marks 30/35

In all the problems assume that BJT has the following characteristics:

$$V_{BE}(ON) = 0.7V; \beta = 100, C_{\pi} = \frac{2.5 \times 10^{-10}}{2\pi} F; C_{\mu} = \frac{2 \times 10^{-12}}{2\pi} F; r_o = \infty; r_{bb} = 0; V_T = 26mV$$

Q.1 Determine the bias point (I_{CQ}, V_{CEQ}), mid-frequency small-signal open circuit voltage gain (v_o/v_s), input resistance (R_{in}) and lower cutoff frequency for the amplifier circuit shown in Fig. 1. State three benefits that come from keeping a part of emitter resistance that is not bypassed by a capacitor. ----11



Ans. 1

$$I_C = \frac{0 - V_{BE} - V_{EE}}{R_{E1} + R_{E2} + \frac{R_S}{\beta}} = 0.52mA \quad (1)$$

$$V_C = V_{CC} - I_C R_C = 1.4 \quad V_E = V_{EE} + I_C (R_{E1} + R_{E2}) = -0.72$$

$$V_{CE} = 2.12V \quad (1)$$

$$r_{\pi} = \frac{V_T}{I_C} \cdot \beta = 5K \quad g_m = I_C / V_T = 0.02V$$

$$A_v = - \frac{g_m R_C}{1 + g_m R_{E1}} \cdot \left(\frac{R_{in}}{R_{in} + R_S} \right) \quad R_{in} = r_{\pi} + (1 + \beta) R_{E1} = 86K\Omega$$

$$= -5 \quad (2)$$

$$f_L = \frac{1}{2\pi C_E R_{eq}} \quad R_{eq} = R_{E2} \parallel (R_{E1} + \frac{r_{\pi} + R_S}{\beta}) = 843.7\Omega$$

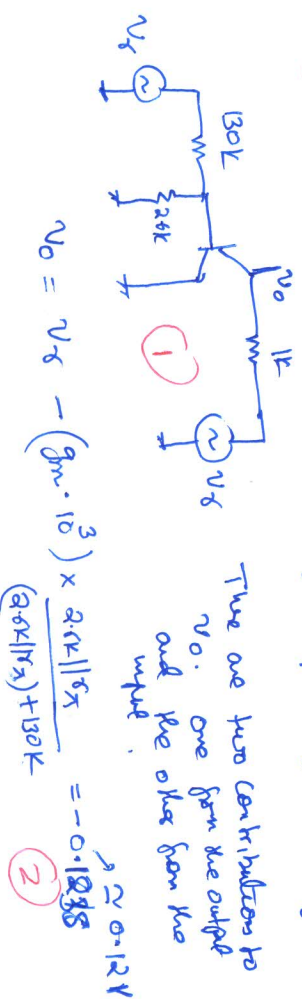
$$= 118.5Hz \quad (2)$$

Benefits: (1) High R_{in} (2) Small f_L (3) Reduced distortion (1)

Ans. 2

$$I_C = \frac{2 - 0.7 \times 100}{130K} = 1mA \Rightarrow r_{\pi} = 2.6K\Omega \quad g_m = 0.0385V$$

$$V_C = \text{ripple of } 0.2V \text{ magnitude}$$



Q.3 For the amplifier shown in Figure 3, determine a suitable value of R_{C2} such that output voltage swing at 10% second harmonic distortion is 0.8V and overall mid-frequency small-signal open circuit voltage gain V_o/V_s is maximized. Determine also the magnitude of this gain. ----5

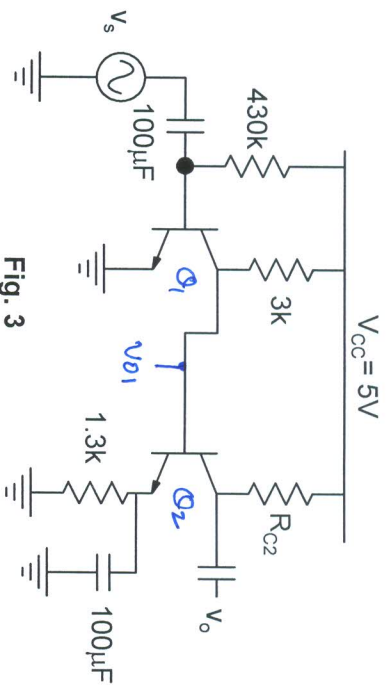


Fig. 3

Q.4 Determine the value of bias current I_o such that output resistance R_o in mid-frequency band is 20Ω . Assume that both transistors have identical characteristics and are biased in forward active mode. ----5

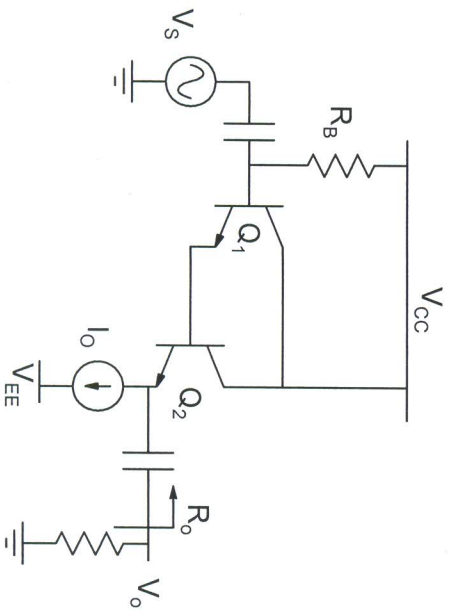


Fig. 4

Ans 3

$$I_{C1} = \frac{5 - 0.7 \times 100}{430k} = 1mA$$

$$V_{C1} = 5 - 3 = 2V$$

$$I_{C2} \approx \frac{2 - 0.7}{1.3k} = 1mA$$

$$V_{om} = 0.8V \Rightarrow V_{CE2} \geq 0.8 + 0.2 = 1V \quad \text{Choose minimum}$$

$$I_{C2} R_{C2} = 5 - 1 - I_{C2} R_{E2} = 2.7V$$

$$R_{C2} = 2.7k\Omega$$

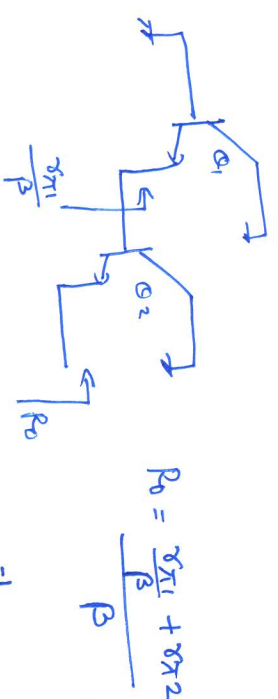
$$I_{C2} R_{C2} \cdot \frac{HD_2}{2\theta} = 1.08 > V_{om}$$

$$A_v = \frac{V_o}{V_s} = \frac{V_o}{V_{o1}} \times \frac{V_{o1}}{V_s} \quad \frac{V_o}{V_{o1}} = -\frac{I_{C2} R_{C2}}{V_T} = -103.84$$

$$\frac{V_{o1}}{V_s} = -g_{m1} \cdot 3k \parallel r_{\pi 2} \quad g_{m1} = 0.0385V \quad r_{\pi 2} = 2.6k\Omega$$

$$= 58.57 \quad A_v = 5.5 \times 10^3$$

Ans 4



$$R_o = \frac{r_{\pi 1}}{\beta} + r_{\pi 2}$$

$$\text{Since } I_{C2} = \beta I_{C1} \Rightarrow r_{\pi 2} = \beta^{-1} r_{\pi 1} \Rightarrow r_{\pi 1} = \beta r_{\pi 2}$$

$$R_o = \frac{2 r_{\pi 2}}{\beta} = \frac{2 V_T}{I_o} = 20\Omega$$

$$I_o = \frac{V_T}{10} = 2.6mA$$

Q.5 Determine the upper cutoff frequency of the two-stage amplifier shown below in Fig. 5. Assume that both transistors have identical characteristics and are biased in forward active mode -----5

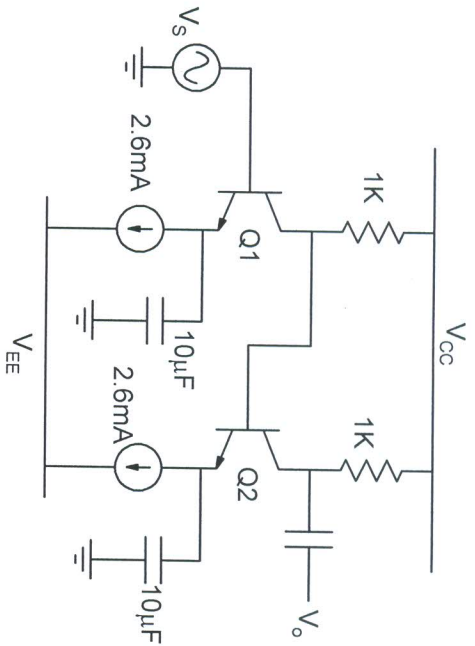


Fig. 5

Q.6 In the circuit shown in Fig. 6 determine collector current of transistor Q1. The PNP transistor Q2 has $V_{EB} = 0.7V$, identical current gain and other small signal parameters as NPN. Estimate the mid-frequency voltage gain V_o/V_s . -----4.5

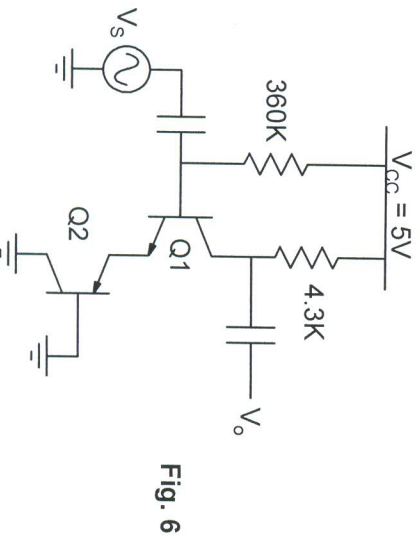
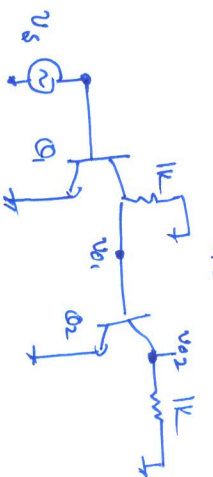


Fig. 6

Ans 5

$$r_{\pi 1} = r_{\pi 2} = \frac{V_T}{I_C} \cdot \beta = 1k\Omega \quad g_{m1} = g_{m2} = 0.1S$$



$$A_{v1} = -g_{m1} \parallel 1k \parallel r_{\pi 1} = -50$$

$$A_{v2} = -g_{m2} \cdot 1k = -100$$

There are 3 nodes (V_S , V_{O1} and V_{O2}) which contribute to 3-dB frequency.

Input node: $R = 0$ So it will not contribute

$$\text{Output node } R = 1k \parallel r_{\pi 2} = 0.5k \quad C = C_{\pi 1} + C_{\pi 2} + C_{\mu 2} \times (1 - A_{v2})$$

$$C = RC = \frac{0.25 \times 10^{-7} s}{2\pi} = \frac{4.5 \times 10^{-10} F}{2\pi}$$

$$\text{Output node } R = 1k \quad C \approx C_{\mu 2} = \frac{2 \times 10^{-12}}{2\pi} \quad f = \frac{1}{2\pi RC} \approx 4.4MHz$$

Ans 6.

$$V_{B1} = 0.7 + 0.7 = 1.4 \quad I_{B1} = \frac{5 - 1.4}{360k} = 10\mu A$$

Assuming Q_1 is forward-Active mode $I_C = \beta I_{B1} = 1mA$

$$V_{C1} = 5 - I_C R_C = 0.7 \quad \text{But } V_{E1} = 0.7$$

$$\Rightarrow V_{CE1} = 0 \Rightarrow Q_1 \text{ is in saturation region}$$

$$\Rightarrow V_C = 0.7 + 0.2 = 0.9V \quad I_C = \frac{5 - 0.9}{4.3k} = 0.95mA$$

Since Q_1 is in saturation, voltage gain will be negligible