KHWL_RV32I46F

Final version of milestone; RV32s project; its testbench draft.
R2: It's the non-cache version of RV32I47NF.R1 and the changes(Trap_Done, CSR_WE_OR, ED's f7)are for RV32I47NF.R2 too. Since no cache is supported, Zifencei is no longer supported. so RV32I46F.
Supports 46 Instructions including EBREAK, ECALL, mret.

22:29 **강현우**, KHWL2025.
2024.12.15(日) ~ 2025.05.11(日)