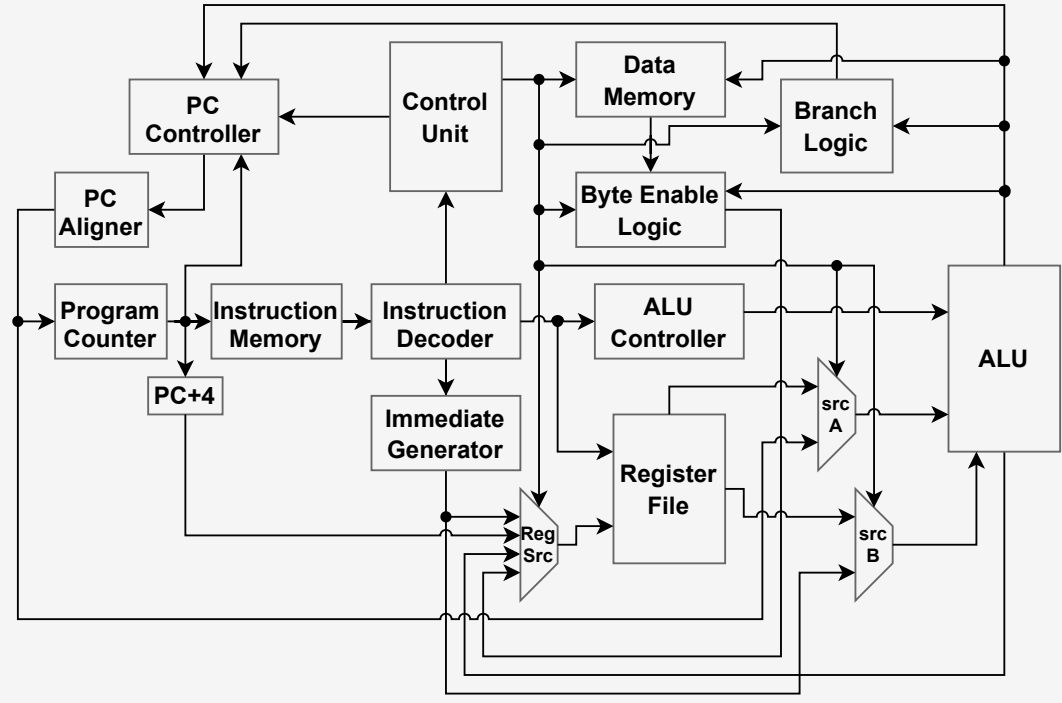
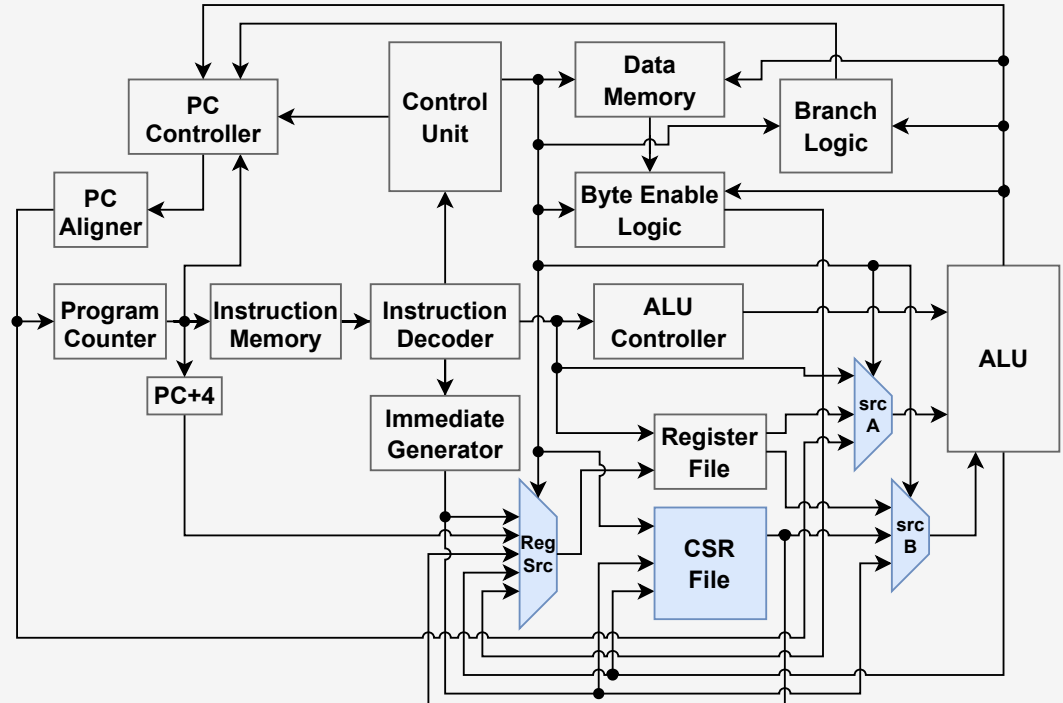


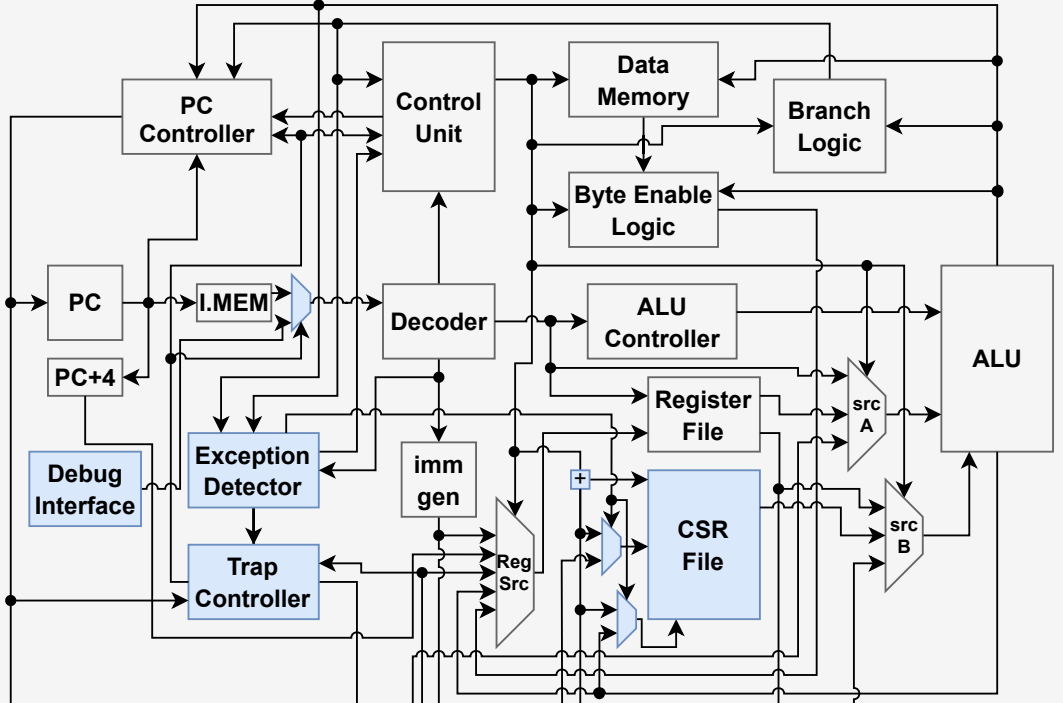
[RV32I37F]  
37F Architecture



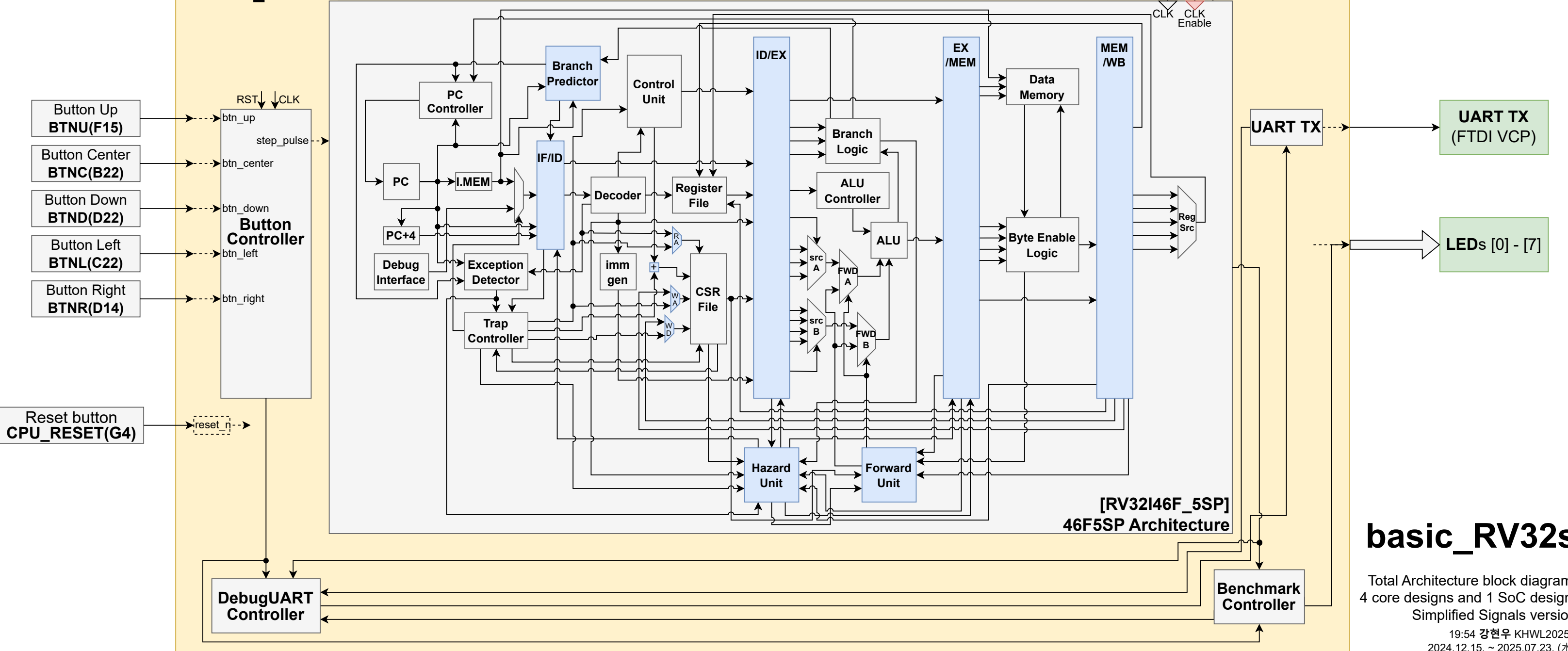
[RV32I43F]  
43F Architecture



[RV32I46F]  
46F Architecture



46F5SP\_SoC



basic\_RV32s

Total Architecture block diagram.  
4 core designs and 1 SoC design.  
Simplified Signals version  
19:54 강현우 KHWL2025.  
2024.12.15. ~ 2025.07.23. (水)