



RV32I46F_5SP.R1

23:28 강현우, KHWL2025.
2024.12.15(日) ~ 2025.05.22(金)

R1 : Pipelined RegWrite, CSR_write from Control Unit
RV32I46F's 5-Stage Pipeline Architecture.
Hazard unit, ForWard unit, Branch Predictor implemented.
2-bit BHT BTB Branch Predictor.