KHWL_RV32I46F.R4

Final version of milestone; RV32s project; its debugging draft
R4 : PCC stalls PC when it detects misalignment this solves
next_pc freeze issue. It was because PC works at posedge clk.
R3 : PCC_op signal for **next_pc race condition issue**
R2: Non-cache version of RV32I47NF.R1 revision of 46F is synced.
Supports 46 Instructions including EBREAK, ECALL, mret.

11:26 강현우, KHWL2025.
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