



# KHWL\_RV32I47F.R4

Instruction Decoder & Imm\_gen Logic modified.  
 Checked CLK, RST Signal's necessity for each modules.  
 Signals Optimized & Still working. Modules Verifying.  
 2024.12.15(日) ~ 2025.01.28(火) Now ALUSrcB has zero-extended 32-bit imm value from ID.