



KHWL_RV32I43F

One of Final version of milestone; RV32s project.
 Supports 43 Instructions with basic modules.
 Including Zicsr instructions. (csrww(i),csrrs(i),csrrc(i))
 Misaligned Address access is internally handled in PCC
 by making lower 2 bits 0. (2'b0)

20:55 강현우, KHWL2025.
 2024.12.15(日) ~ 2025.02.13(木)