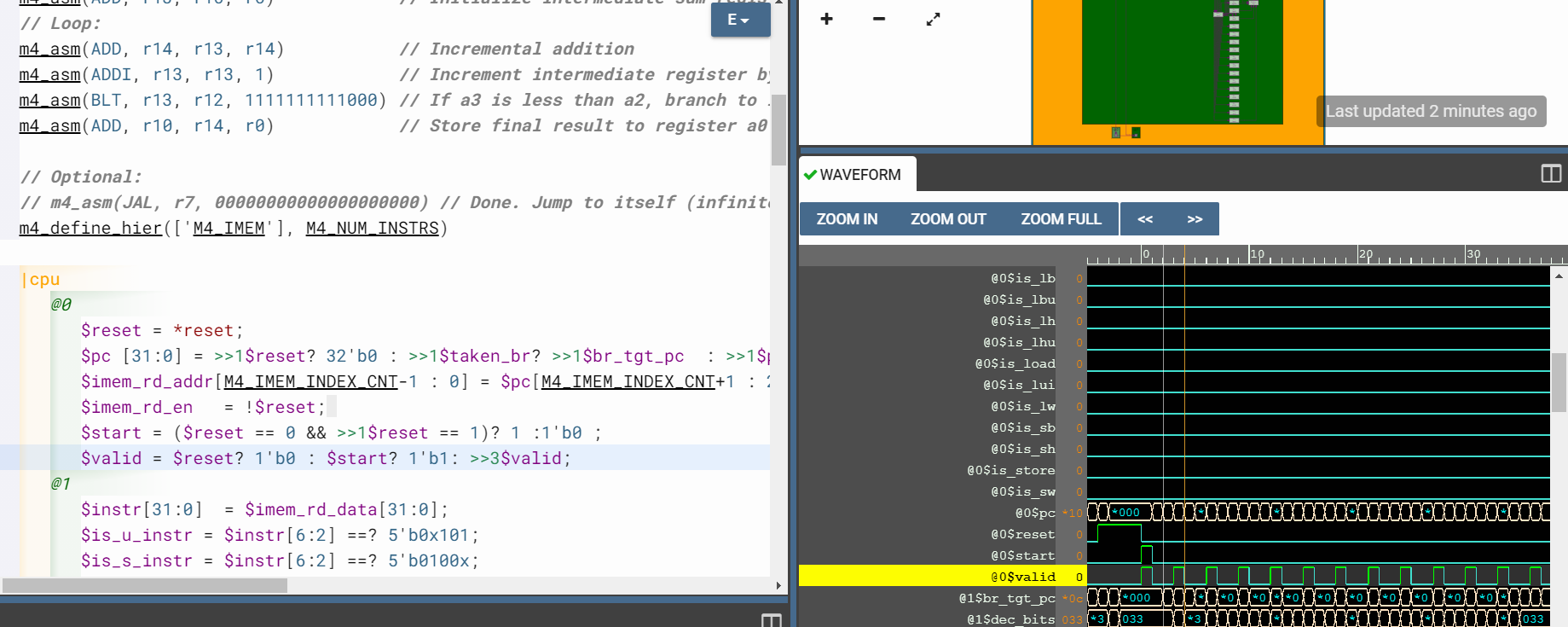
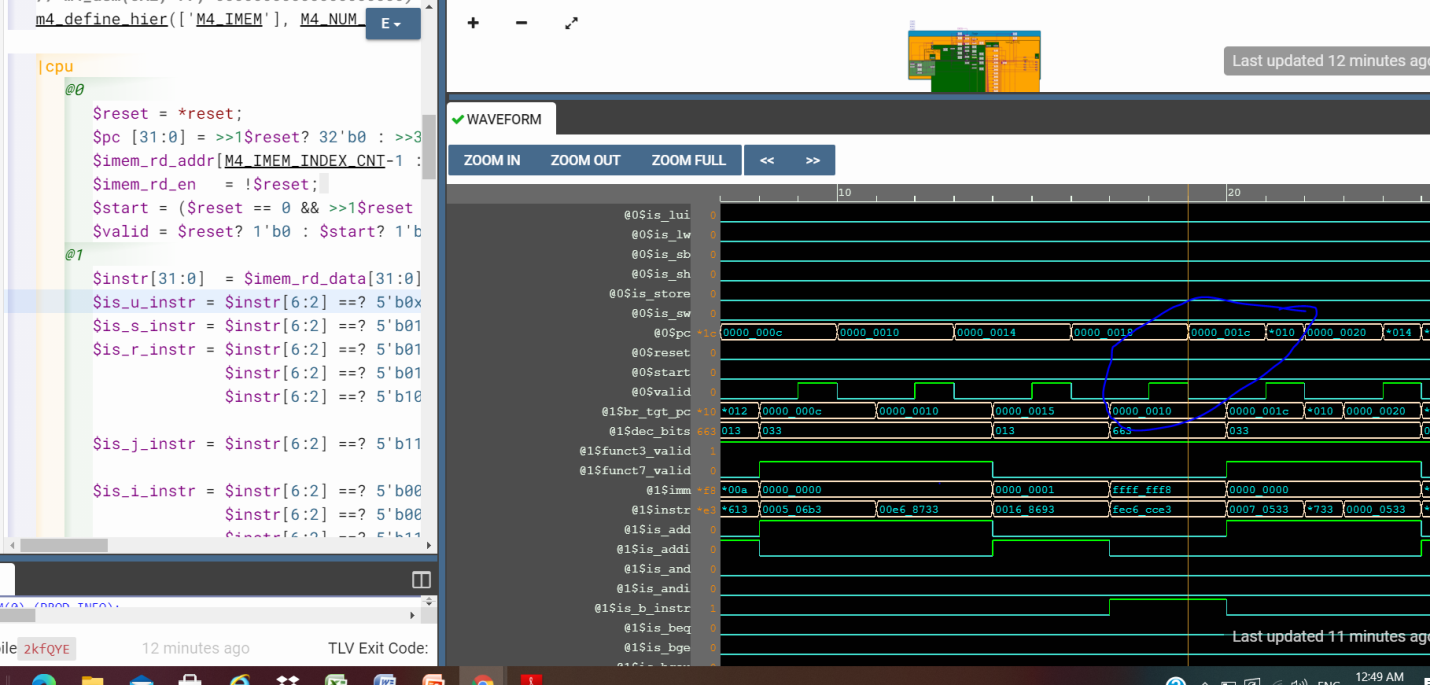
**DAY5**

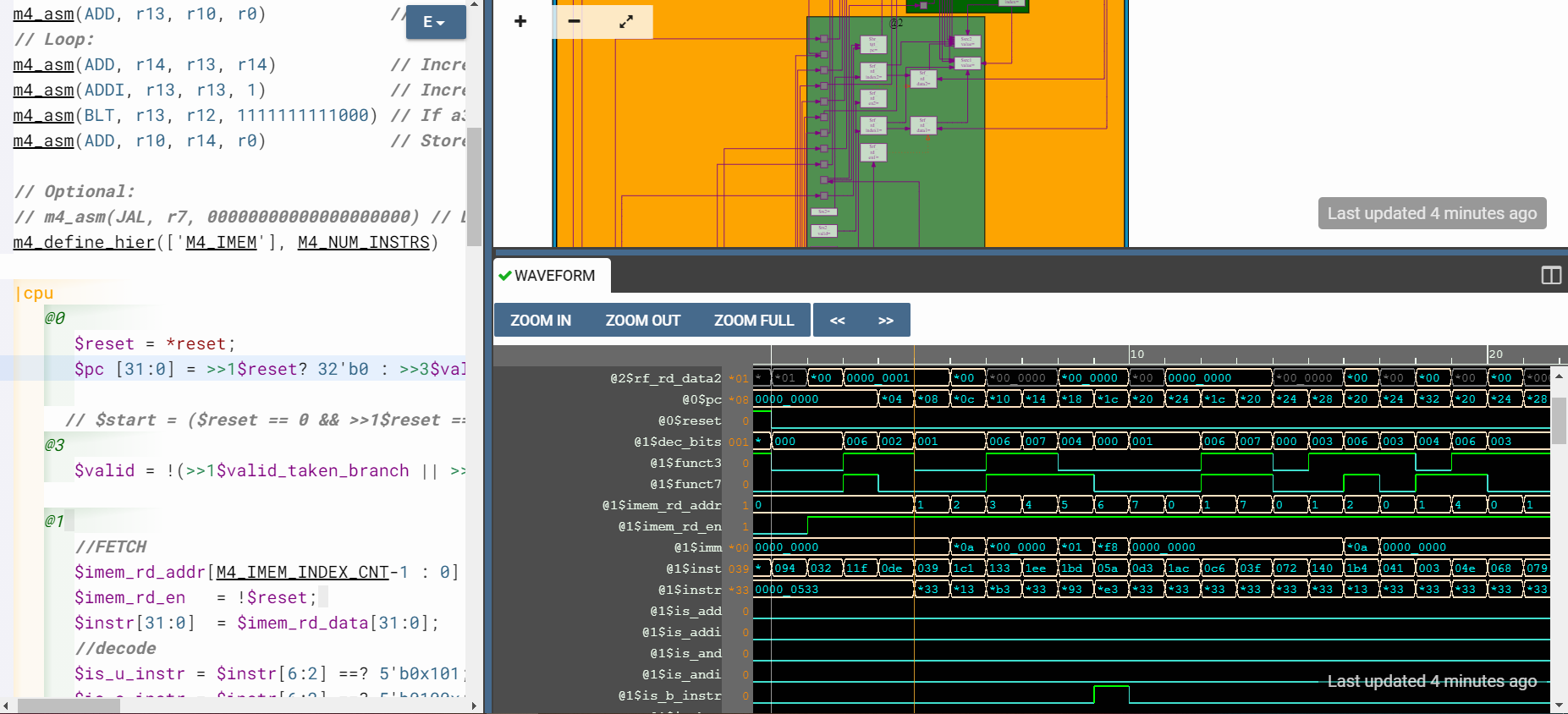
3-CYCLE $VALID:



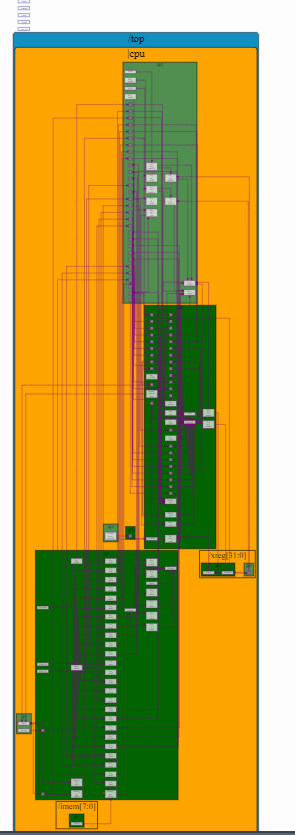
**Generation of pc from br\_tgt\_pc:**

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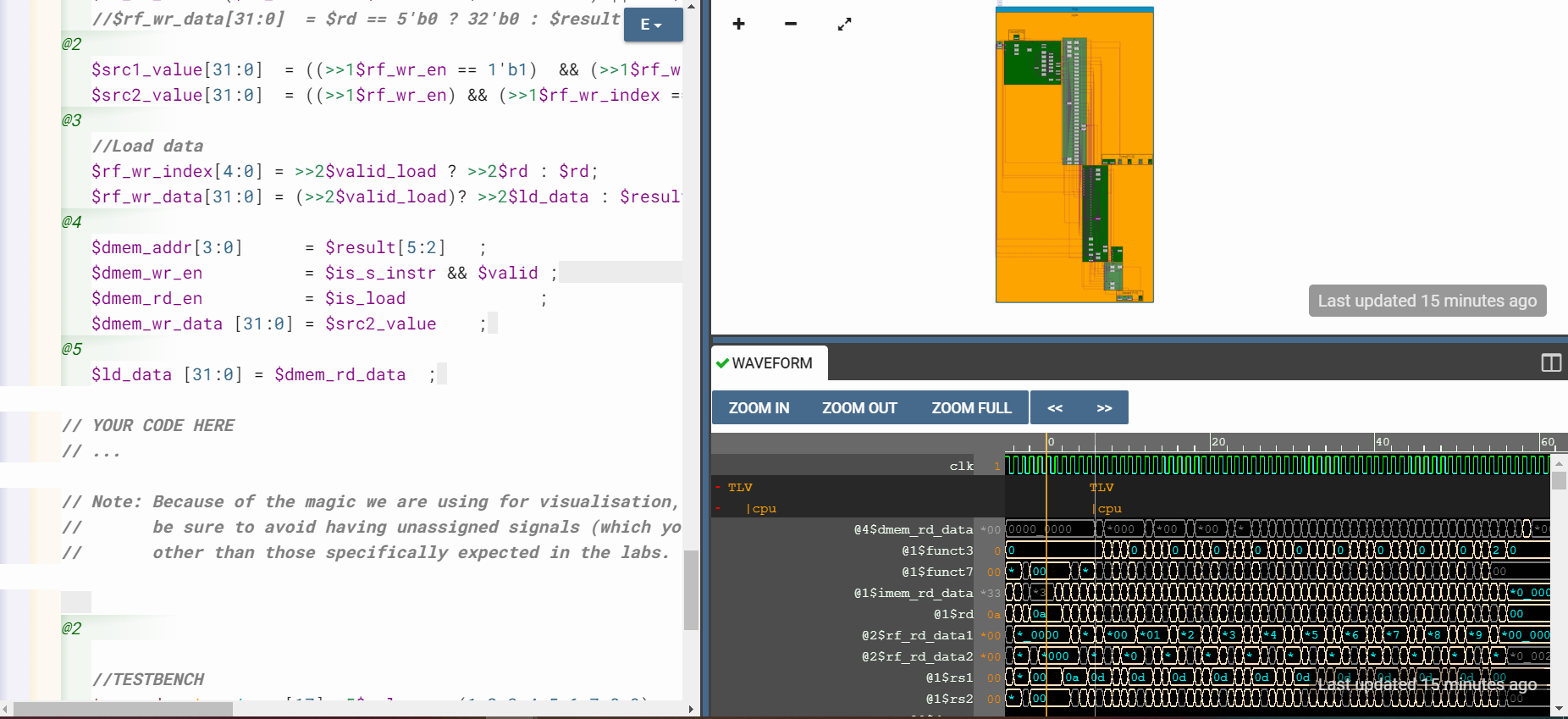
**PC IMPLEMENTED EVERY CYCLE:**

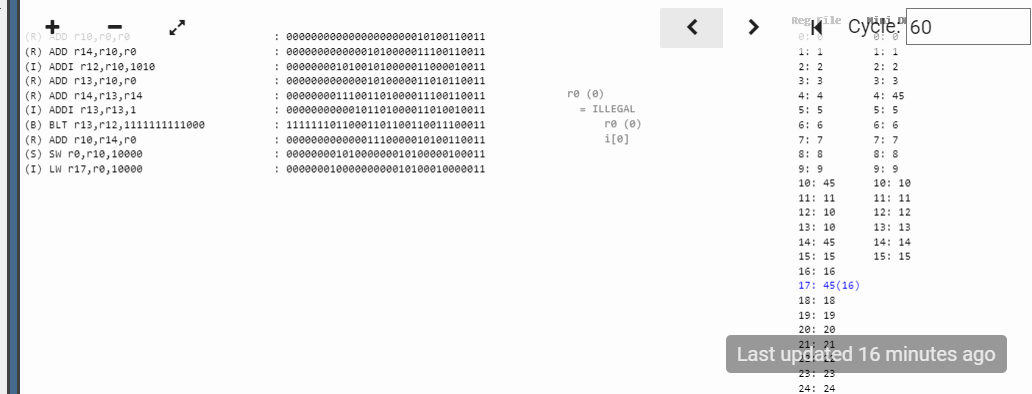
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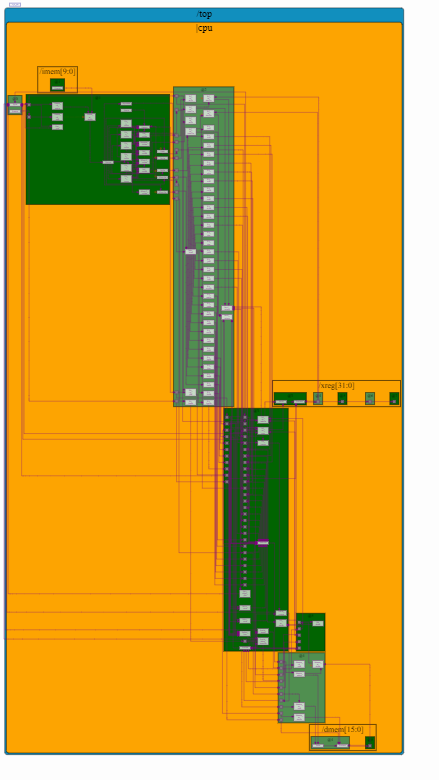
**Complete ALU without load and store:**

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**Final CPU:**

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