

Anubhab Tripathi



Interested in **Digital, Analog Circuit Design & Analysis, Machine Learning & Artificial Intelligence**. Majoring in **Electronics & Telecommunication Engineering** at **Jadavpur University**.

Education:

- **Jadavpur University, Kolkata**
July 2020 - Present
Bachelor's Degree in Electronics & Telecommunication Engineering. Completed 6th semester in May, 2023.
SGPA (Linear Average): 8.83
- **Ramakrishna Mission Vidyabhavan, Midnapore**
January 2012 - April 2020
WBCHSE 2020 (**Class 12**): **97.8%**
WBBSE 2018 (**Class 10**): **96.3%**

Skills:

- Digital Circuit Design & Analysis
- Analog Electronics
- SPICE Softwares: LTspice
Cadence, Multisim
- C, Python, Verilog
Programming
- MATLAB Programming
- COMSOL Software Modeling
- Microsoft and Google
Workspace

Internship Experience:

CMATER, JU [Jun 2022 – Dec 2022] (Project Guide: Prof. Ram Sarkar)

- ❖ Developed **ML model** using existing **BDDb approach** to extract letters from 10 different languages.

Projects:

- **Hardware Design for Signal Processing [Jul 2021 – Nov 2021]**
Designed circuits for Transistor Mixer, PAM, IF Tuner Amplifier, Modulation, Demodulation, PLL, ASK, PSK, FSK, DPSK, QPSK and many more.
- **Rain-Sensor Hardware Design [March 2023 – July 2023]**
Designed a Comparator circuit interfaced with LED for visual output along with a VCO interfaced with Speaker for auditory output.

Additional:

- Qualified GATE ECE 2023
- WBJEE (2020) [GMR **314**]; Qualified JEE Main & Advanced
- JBNSTS Scholar (2018)
- Participated in Circuit Design, Data Analysis Hackathon

Contact:

Cell- 9832910091

Email- tripathianubhab@gmail.com

LinkedIn- <https://www.linkedin.com/in/anubhabtripathi66>

Languages:

English, Hindi, Bengali, Spanish.