

# Specification document of AD22100S

Component manufacturer Analog Devices
Model number AD22100S

Datasheets AD22100 (REV. D) (analog.com)

Specification Ver 01.00.00 Oct 03,2022 New release

01.00.01 Oct 18,2022 Corrected license content

Application item add

Documentation provided Rui Long Lab Inc. https://rui-long-lab.com/

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## 1. Component datasheet

Temperature accuracy  $\pm 3.0^{\circ}$  C ( $-50^{\circ}$  C to  $+150^{\circ}$  C)

Range of power supply voltage (Vdd) 4.0 to 6.5[V]

Output voltage (Vout) Linear 22.5×Vdd/5 [mV/° C] Typ.

Vdd = 5.0 [V]

-50 [° C] 0.250[V] Typ. 150 [° C] 4.750 [V] Typ.

Calculation  $Vout = (Vdd/5 V) \times (1.375 V + 22.5 mV)^{\circ} C \times Ta)$ 

 $Ta = (Vout / (Vdd/5V)) - 1.375V) / 22.5 mV/^{\circ} C$ 

Applications IoT etc

· HVAC systems

· System temperature compensation

· Board level temperature sensing

• Electronic thermostats

Automotive



## 2. Component Software IF specification

The software interface specifications based on the AD22100S component specifications are as follows. The voltage value-to-physical value conversion equation is a linear conversion equation as shown in the equation below.

ADC value to voltage value conversion formula

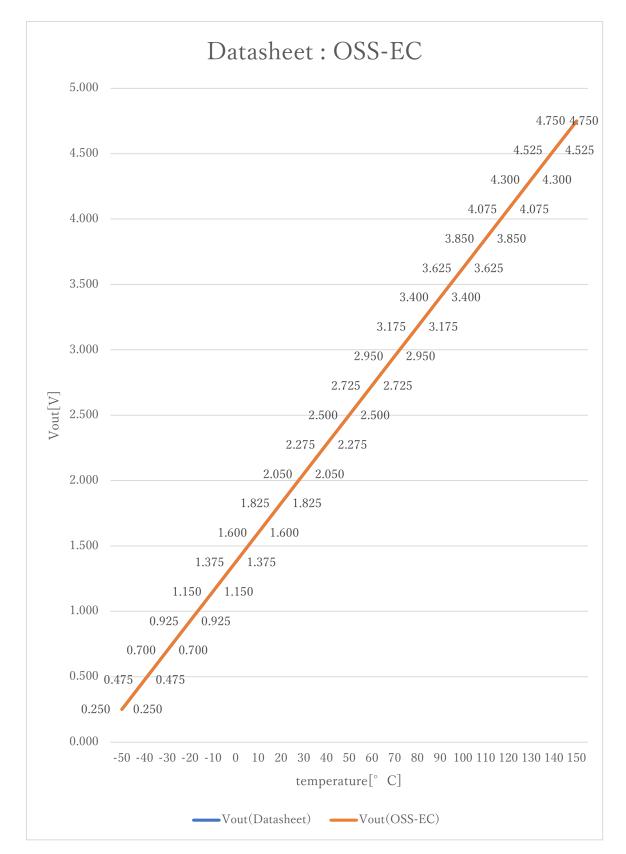
```
vi = (ai \times iADC_vdd) / 2^{iADC_bit} [V]
```

Voltage value to physical value conversion formula

```
y = (vi - iAD22100S\_xoff) / iAD22100S\_gain + iAD22100S\_yoff [°C] iAD22100S\_min \le y \le iAD22100S\_max
```

```
A/D conversion value
ai
                 Sensor output voltage value [V]
٧i
i ADC_vdd
                 Sensor supply voltage value [V]
iADC bit
                 A/D conversion bit length
                 Temperature value [°C]
#define iAD22100S_xoff
                           (1.375F*(iADC_vdd/5.0)) // X offset [V]
#define iAD22100S_yoff
                                                     // Y offset [°C]
                          0. OF
#define iAD22100S_gain
                           (<u>0.0225F*(iADC_vdd/5.0))</u> // Gain [V/°C]
                                                     // Temperature Max [°C]
#define iAD22100S_max
                          150. OF
#define iAD22100S_min
                          <u>-50. 0F</u>
                                                     // Temperature Min [°C]
```







#### 3. File Structure and Definitions

#### AD22100S.h

```
#include "user_define.h"
// Components number
#define iAD22100S
                               108U
                                                        // Analog devices AD22100S
// AD22100S System Parts definitions
                            (1.375F*(iADC_vdd/5.0)) // X offset [V]
#define iAD22100S_xoff
#define iAD22100S_yoff
                            0. OF
                                                        // Y offset [°C]
                            (0.0225F*(iADC_vdd/5.0)) // Gain [V/°C]
#define iAD22100S_gain
#define iAD22100S_max
                            150. OF
                                                        // Temperature Max [°C]
#define iAD22100S_min
                            <u>-50. 0F</u>
                                                        // Temperature Min [°C]
extern const tbl_adc_t tbl_AD22100S;
```



## AD22100S.cpp

```
#include
                "AD22100S. h"
#if
        iAD22100S_ma == iSMA
                                                         // Simple moving average filter
static float32 AD22100S_sma_buf[iAD22100S_SMA_num];
static const sma_f32_t AD22100S_Phy_SMA =
        iInitial ,
                                                         // Initial state
        iAD22100S_SMA_num ,
                                                       // Simple moving average number & buf size
        OU ,
                                                         // buffer position
        0.0F,
                                                         // sum
        &AD22100S_sma_buf[0]
                                                         // buffer
};
#elif iAD22100S_ma == iEMA
                                                         // Exponential moving average filter
static const ema_f32_t AD22100S_Phy_EMA =
{
        iInitial ,
                                                         // Initial state
        0.0F,
                                                         // Xn-1
        iAD22100S_EMA_K
                                                         // Exponential smoothing factor
};
#elif
        iAD22100S_ma == iWMA
                                                         // Weighted moving average filter
static float32 AD22100S_wma_buf[iAD22100S_WMA_num];
static const wma_f32_t AD22100S_Phy_WMA =
{
        iInitial ,
                                                         // Initial state
        iAD22100S_WMA_num ,
                                                     // Weighted moving average number & buf size
        OU ,
                                                         // buffer poition
        iAD22100S\_WMA\_num * (iAD22100S\_WMA\_num + 1)/2 , // kn sum
        &AD22100S_wma_buf[0]
                                                         // Xn buffer
};
#else
                                                         // Non-moving average filter
#endif
#define iDummy_adr
                         0xffffffff
                                                         // Dummy address
```



```
const tbl_adc_t tbl_AD22100S =
        i AD22100S
        iAD22100S_pin
        iAD22100S\_xoff
        iAD22100S_yoff
        iAD22100S_gain
        iAD22100S_max
        iAD22100S_min
        i AD22100S_ma
#if
        iAD22100S_ma == iSMA
                                                          // Simple moving average filter
        &AD22100S_Phy_SMA
         (ema_f32_t*) iDummy_adr
        (wma_f32_t*) iDummy_adr
#elif
        iAD22100S_ma == iEMA
                                                          // Exponential moving average filter
        (sma_f32_t*) iDummy_adr ,
        &AD22100S_Phy_EMA
        (wma_f32_t*) iDummy_adr
#elif
        iAD22100S_ma == iWMA
                                                          // Weighted moving average filter
         (sma_f32_t*) iDummy_adr
         (ema_f32_t*) iDummy_adr,
        &AD22100S_Phy_WMA
#else
                                                          // Non-moving average filter
         (sma_f32_t*) iDummy_adr ,
         (ema_f32_t*) iDummy_adr
         (wma_f32_t*) iDummy_adr
#endif
};
```