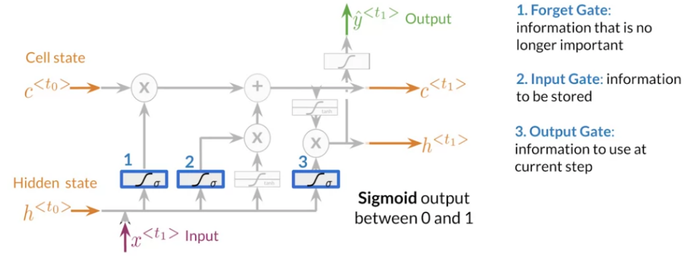
LSTM Architecture

The LSTM architecture could get complicated and don't worry about it if you do not understand it. I personally prefer looking at the equation, but I will try to give you a visualization for now and later this week we will take a look at the equations.

Gates in LSTM



Note the forget gate (1), input gate (2) and output gate (3) marked in blue. In contrast with vanilla RNNs, there is the cell state in addition to the hidden state. The idea of the **forget gate** to drop the information that is no longer important. It makes use of the previous hidden state $h^{<t0>}$ and the input $x^{<t1>}$. The **input gate** makes sure to keep the relevant information that needs to be stored. Finally the **output gate** creates an output that is used at the current step.

LSTM equations (optional):
For better understanding, take a look at the LSTM equations and relate them to the figure above.

The forget gate: $f = \sigma\left(Wf\left[ht-1; xt\right] + bf\right)$ (marked with a blue 1)

The input gate: $i = \sigma\left(Wi\left[ht-1; xt\right] + bi\right)$ (marked with a blue 2)

The gate gate (candidate memory cell): $g = \tanh\left(Wg\left[ht-1; xt\right] + bg\right)$

The cell state: $ct = f \odot c^{t-1} + i \odot g$

The output gate: $o = \sigma\left(Wo\left[ht-1; xt\right] + bo\right)$ (marked with a blue 3)

The output of LSTM unit: $ht = ot \odot tanh(ct)$

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