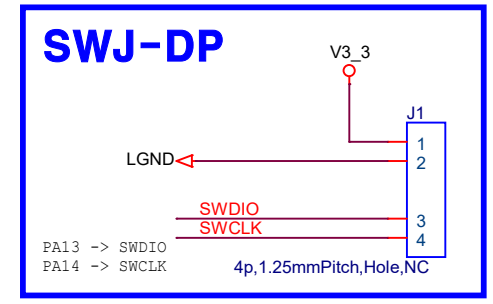
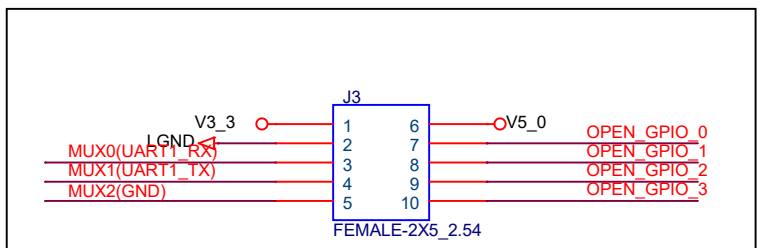


MAIN PROCESSOR



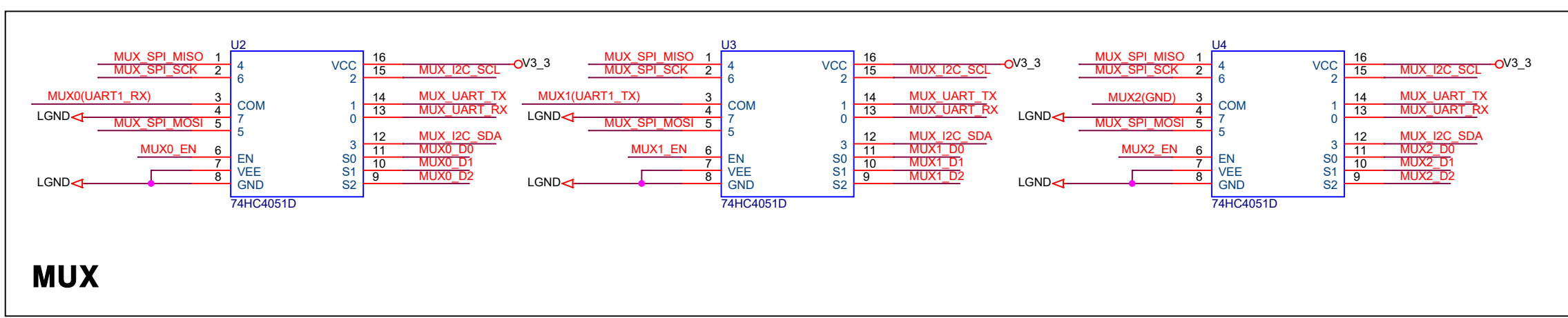
Bootloader activation pattern : BOOT0(1) , BOOT1(0)
MUX_()_() 모두 TP 추가



Connector

- MUX:
- EN : Low activate(Pull Down)
 - 0 : UART RX
 - 1 : UART TX
 - 2 : I2C SCL
 - 3 : I2C SDA
 - 4 : SPI MISO
 - 5 : SPI MOSI
 - 6 : SPI SCK
 - 7 : GND

DEFAULT
MUX0 : UART RX
MUX1 : UART TX
MUX2 : GND



MUX