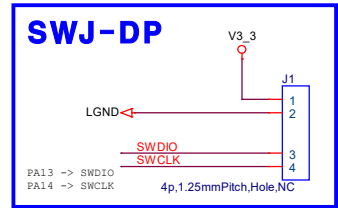
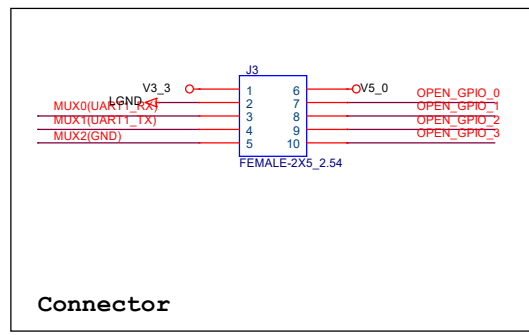


MAIN PROCESSOR



Bootloader activation pattern : BOOT0(1) , BOOT1(0)

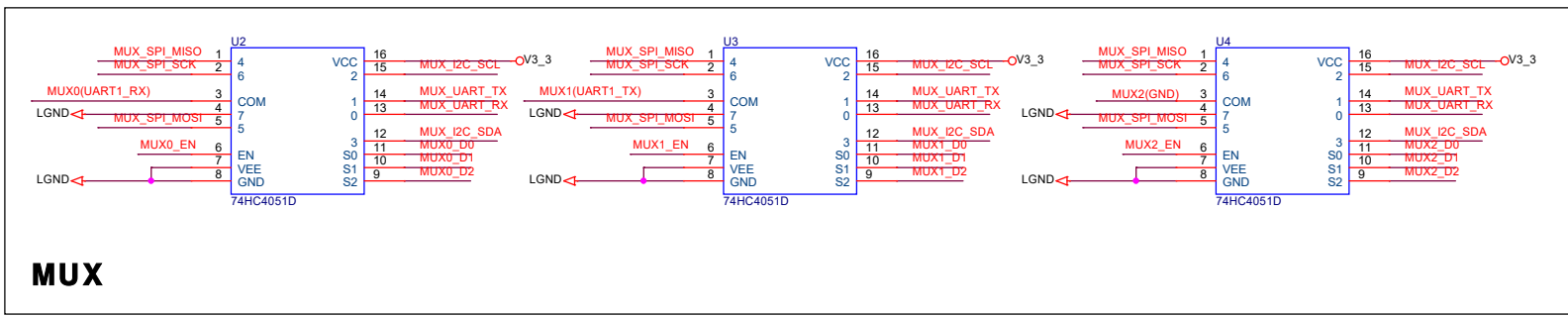
MUX_()_() 무 T



Connector

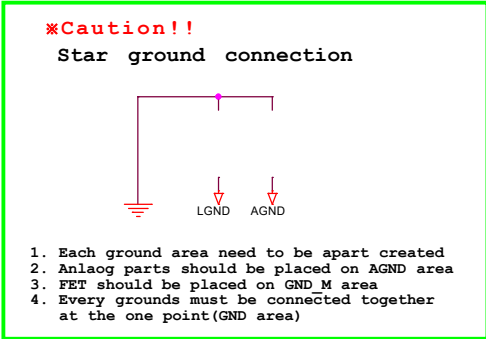
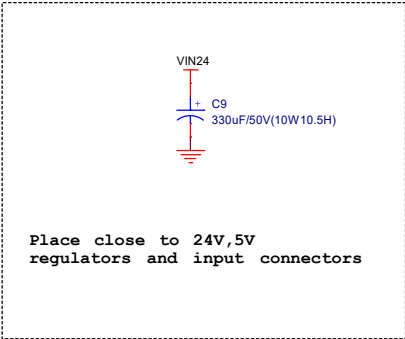
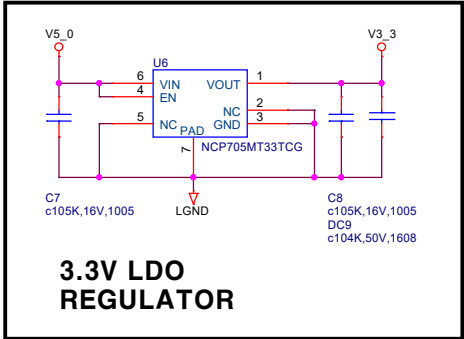
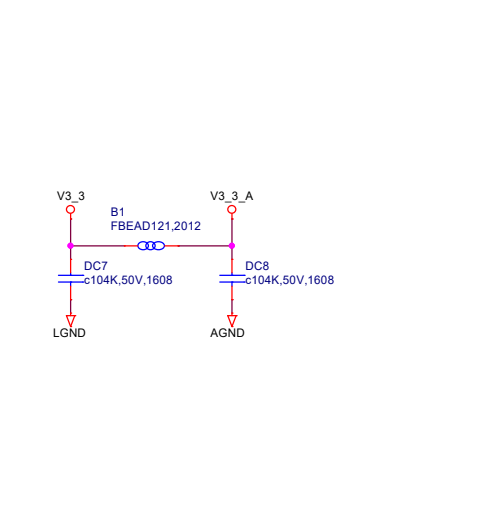
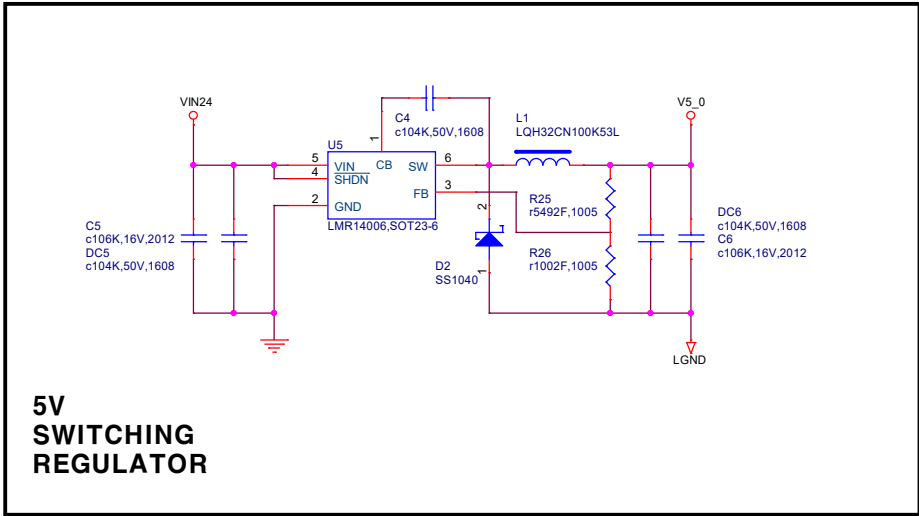
- MUX:
- EN : Low activate(Pull Down)
 - 0 : UART RX
 - 1 : UART TX
 - 2 : I2C_SCL
 - 3 : I2C_SDA
 - 4 : SPI_MISO
 - 5 : SPI_MOSI
 - 6 : SPI_SCK
 - 7 : GND

- DEFAULT
- MUX0 : UART RX
 - MUX1 : UART TX
 - MUX2 : GND



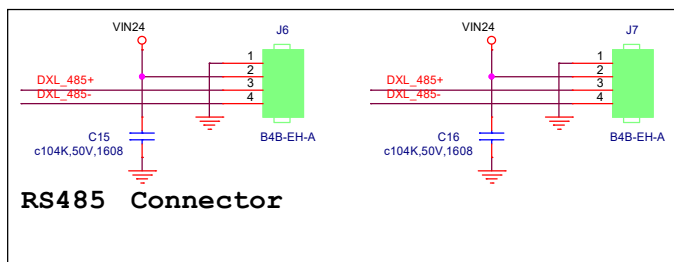
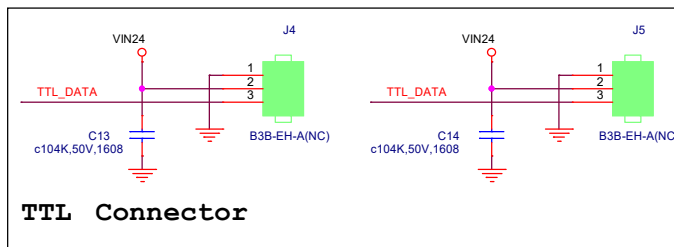
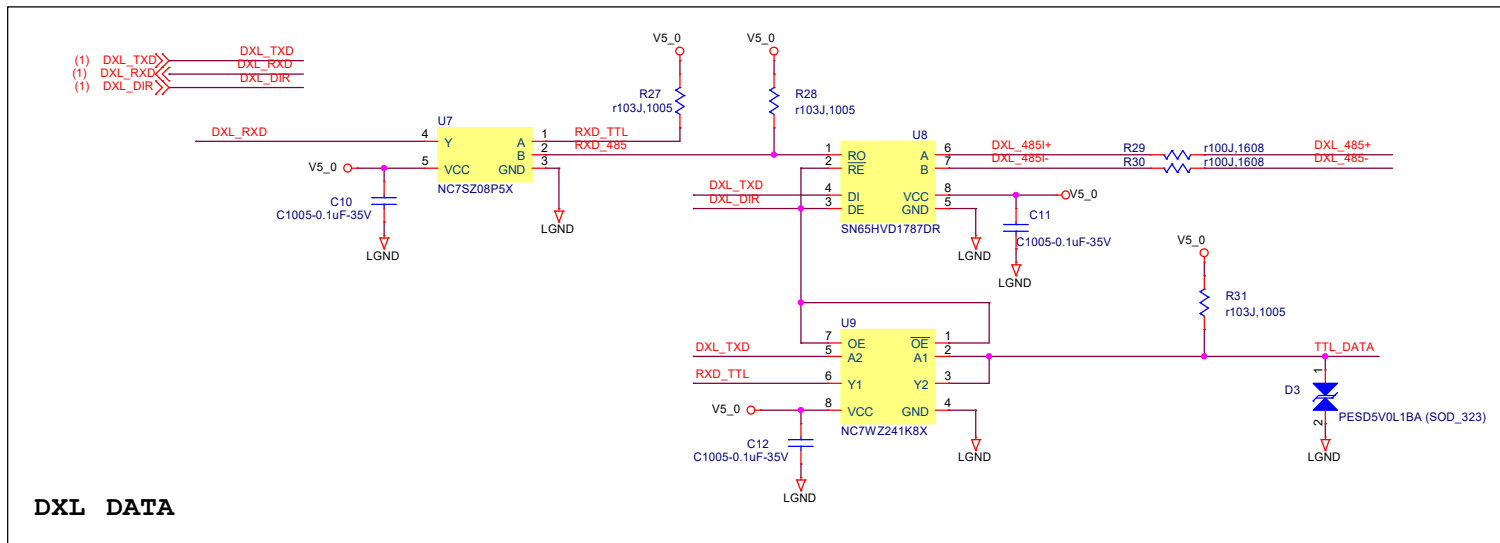
MUX

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Date:	Friday, February 01, 2019		Sheet 1 of 3	



***Caution!!**
Star ground connection

-
1. Each ground area need to be apart created
 2. Anlaog parts should be placed on AGND area
 3. FET should be placed on GND_M area
 4. Every grounds must be connected together at the one point(GND area)



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