

GPUFORT

A source-to-source translator for Fortran accelerator dialects



Motivation

- Porting to new accelerator architectures costs time and money
 - Especially if a new programming language needs to be adopted
- Developers that can do such porting are difficult to find
- However, more often than not it is the only way to achieve good performance on novel architectures
- GPUFORT is a tool that aims to reduce the costs of porting Fortran applications from one accelerator language to the other

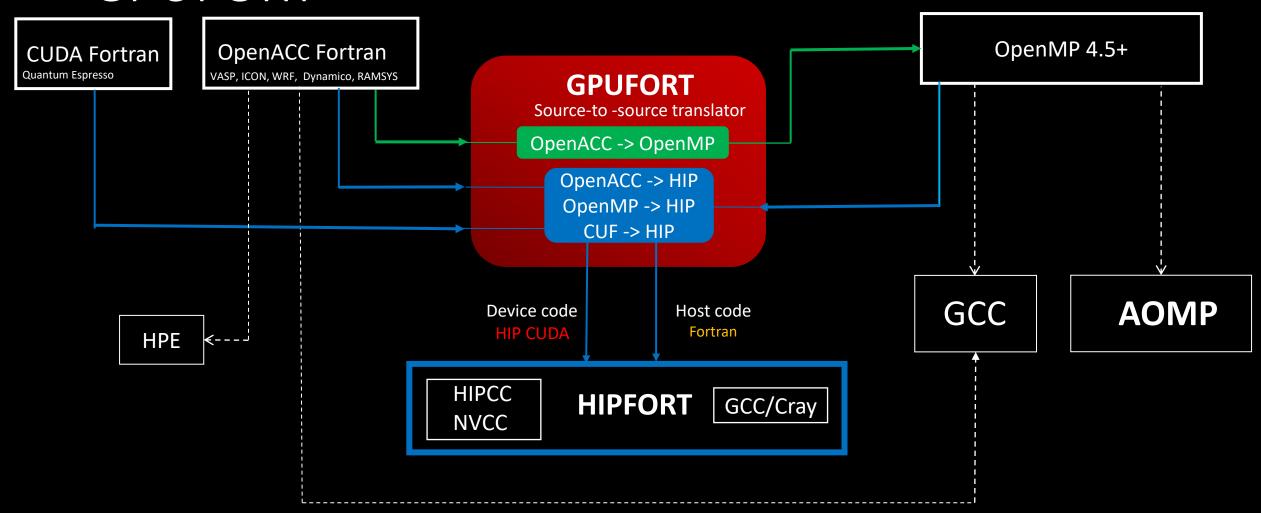


Outline

- GPUFORT Goals and Design
- CUDA Fortran & OpenACC Examples
- Components
- Current Limitations
- Success Story
- Summary & Next Steps



GPUFORT





Remarks

- All translation paths we support/aim to support result in code that compiles (at least for) AMD and NVIDIA devices
- Many of the applications we classify as OpenACC applications use "vendor-specific standard extensions" such as acc_get_cuda_stream; e.g. to use CUDA Fortran math modules
 - A "standard OpenACC compiler without vendor-specific extensions" cannot compile these applications



Goals of the project

- Develop a translator / productivity tool that aids HPC developers in translating a whole codebase from one Fortran accelerator dialect to the other
- Make the tool's actions transparent and allow manual intervention, customization, & code generation output optimization
- Anticipate + support debugging & benchmarking needs of HPC developers
- Make the tool as lightweight as possible:
 - no complicated build process/long list of dependencies/heavy UI



Design decisions

- Specifically for Fortran (might extend it to C)
 - Many popular HPC codes are written in CUDA Fortran and/or OpenACC Fortran
- Written in Python 3
 - Very expressive, cross platform & no compilation-latency
 - Not (that) slow and will be 2x-5x faster [G.v.Rossum]
 - Third-party packages for fast IO, code generation, parsing

- Allow customizations and output styling
 - It is tuneable and scriptable (many options, config files are in Python 3)
 - And again: It is all Python 3
- Ease usage, debugging, and developing:
 - Generate domain-specific debug code
 - Lots of logging with levels and filters
 - Allow inspection of intermediate output (symbol tables, kernel code)
 - Allow adoption/extension of toolchain



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OpenACC Example (1/2)

```
program main
 integer, parameter :: N = 1000
 integer :: i
 integer(4) :: x(N), y(N),
y_exact(N)
 do i = 1, N
  y_exact(i) = 3
 end do
 !$acc data create(x(1:N),y(1:N))
 !$acc parallel loop
 do i = 1, N
  x(i) = 1
 y(i) = 2
                             <1/2>
 end do
```

```
!$acc parallel loop
do i = 1, N
  y(i) = x(i) + y(i)
end do

!$acc end data

do i = 1, N
  if ( y_exact(i) .ne.&
      y(i) ) ERROR STOP "GPU and CPU result do not match"
end do
print *, "PASSED"
end program

<2/2>
```



OpenACC Example (2/2)

```
program main
 integer, parameter :: N = 1000
 integer :: i
 integer(4) :: x(N), y(N), y exact(N)
 do i = 1, N
  y exact(i) = 3
 end do
#ifdef GPUFORT
 !$omp target data map(alloc:x(1:N),y(1:N))
 !$omp target teams distribute parallel do
 do i = 1, N
  x(i) = 1
  y(i) = 2
 end do
 !$omp target teams distribute parallel do
 do i = 1, N
  y(i) = x(i) + y(i)
 end do
 !$omp end target data
                                      <1/2>
```

```
#else
 !$acc data create(x(1:N),y(1:N))
 !$acc parallel loop
 do i = 1, N
  x(i) = 1
  y(i) = 2
 end do
 !$acc parallel loop
 do i = 1, N
  y(i) = x(i) + y(i)
 end do
 !$acc end data
#endif
 do i = 1. N
  if ( y exact(i) .ne.&
      y(i) ERROR STOP "GPU and CPU result do not match"
 end do
 print *, "PASSED"
                                                        <2/2>
end program
```

```
    gpufort –w <file> –E omp
```

```
-w: #ifdef ___GPUFORT
```

- OpenMP generated
- OpenACC original



CUDA Fortran Example (1/3): Input File

```
program main
use cudafor
implicit none
integer, parameter :: N = 40000
real :: x(N), v(N), a
real, device, allocatable :: x d(:)
real, allocatable :: y d(:)
 type(dim3) :: grid, tBlock
integer :: I
attributes(device) :: y_d
tBlock = dim3(256,1,1)
grid = dim3(ceiling(&
  real(N)/tBlock%x),1,1)
                  <1/2>
```

```
allocate(x_d(N),y_d(N))
 x = 1.0; y = 2.0; a = 2.0
x d = x
y d = y
 do i=1,size(y d,1)
  y d(i) = y d(i) + a*xi
 end do
y = y_d
deallocate(x d,y d)
 write(*,*) 'Max error: ', maxval(abs(y-4.0))
end program main
                             <2/2>
```

```
    CUDA Fortran module
```

- device variables
- device malloc/free
- host<->device copies
- offloaded loop

[similar to: nvidia.com/blog/easy-introduction-cuda-fortran/]

AMDA

CUDA Fortran Example (2/3): S2S Translation

```
#ifdef GPUFORT
#include "vector-add.f90-fort2hip.f08"
#endif
program main
#ifdef GPUFORT
 use main fort2hip
 use hipfort
 use hipfort check
#else
 use cudafor
#endif
 implicit none
 integer, parameter :: N = 40000
 real :: x(N), y(N), a
#ifdef GPUFORT
 real,pointer,dimension(:)::x d
 real,pointer,dimension(:) :: y d
#else
 real, device, allocatable :: x d(:)
 real, allocatable :: y d(:)
#endif
 type(dim3) :: grid, tBlock
 integer :: i
#ifndef GPUFORT
 attributes(device) :: y d
#endif
                           <1/3>
```

```
tBlock = dim3(256,1,1)
 grid = dim3(ceiling(real(N)/tBlock%x),1,1)
#ifdef GPUFORT
 call hipCheck(hipMalloc(x d, N))
 call hipCheck(hipMalloc(y d, N))
#else
 allocate(x_d(N),y_d(N))
#endif
x = 1.0; y = 2.0; a = 2.0
#ifdef GPUFORT
 call hipCheck(hipMemcpy(x d, x, hipMemcpyHostToDevice))
 call hipCheck(hipMemcpy(y_d, y,hipMemcpyHostToDevice))
#else
 x d = x
 y d = y
#endif
#define xi x_d(i)
                                                    <2/3>
```

```
#ifdef GPUFORT
  extracted to HIP C++ file
 call launch_main_26_e28c45(grid,tBlock,0,c_null_ptr,c_loc(y_d),&
   size(y d,1),lbound(y d,1),a,c loc(x d),size(x d,1),lbound(x d,1))
call hipCheck(hipMemcpy(y, y_d, hipMemcpyDeviceToHost))
 call hipCheck(hipFree(x_d))
call hipCheck(hipFree(y d))
#else
 !$cuf kernel do(1) <<<grid, tBlock>>>
 do i=1,size(y d,1)
 y_d(i) = y_d(i) + a*xi
 end do
y = y_d
 deallocate(x_d,y_d)
#endif
write(*,*) 'Max error: ', maxval(abs(y-4.0))
end program main
                                                                 <3/3>
```

- gpufort -w -E hip vector-add.f90 (#ifdef's via -w are optional)
- All Fortran files can be compiled hipfc (hipFORT), gfortran, or cray •
- HIP C++ kernel implementation must be compiled via *hipcc*

- **CUDA Fortran module**
- device variables
- device malloc/free
- host<->device copies
- offloaded loop



CUDA Fortran Example (3/3): HIP C++ kernel

```
#include "gpufort.h
//...
// BEGIN main 26 e28c45
 HIP C++ implementation of the function/loop body of:
   !$cuf kernel do(1) <<<grid, tBlock>>>
   do i=1,size(y d,1)
   y_d(i) = y_d(i) + a*xi
   end do
  global void main 26 e28c45(
  float * _restrict__ y_d,
  const int y d n1,
  const int y d lb1,
  float a.
  float * restrict x d,
  const int x d n1,
  const int x_d_lb1) {
 #undef idx y d
 #define idx y d(a)((a-(y d lb1)))
 #undef idx x d
 #define idx x d(a) ((a-(x d lb1)))
 int i = 1 + (1)*(threadIdx.x + blockIdx.x * blockDim.x);
 if (loop_cond(i,y_d_n1,1)) {
  y_d[_idx_y_d(i)]=(y_d[_idx_y_d(i)]+a*x_d[_idx_x_d(i)]);
```

- Generates per loop nest: kernel & variety of kernel launchers
- Kernel name consists of name of parent module/program/procedure, line number and a hash code
 - Hash code encodes non-whitespace characters in Fortran original;
 can be used to detect significant code changes



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Requirements/tasks for a translator for ...

CUDA Fortran

✓ CUDA-like runtime and math libraries (https://github.com/ROCmSoftwarePlatform/hipfort)

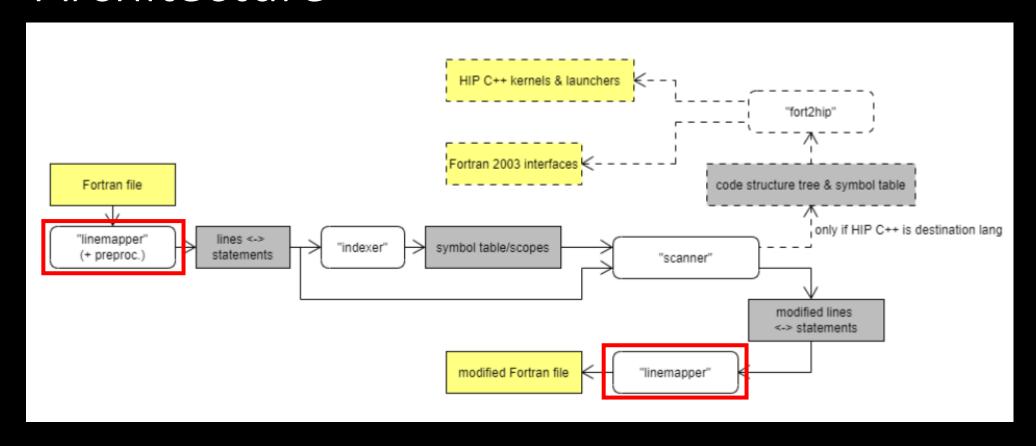
- Detect & translate CUDA Fortran intrinsics & lang extensions to hipFORT routines and standard Fortran types
- Detect, analyze & translate:
 - CUDA Fortran accelerator routines
 - CUDA Fortran !\$cuf directives

OpenACC Fortran

- √ (only Fortran + HIP C++)
 A runtime for managing array/ derived type mappings, async queues and exposing OpenACC runtime API (GNU LIBGOMP)
- Detect & translate OpenACC directives that result in runtime calls (!\$acc update, !\$acc data, ...)
- Detect, analyze & translate:
 - OpenACC accelerator routines
 - OpenACC !\$acc parallel and !\$acc kernels construct



Architecture





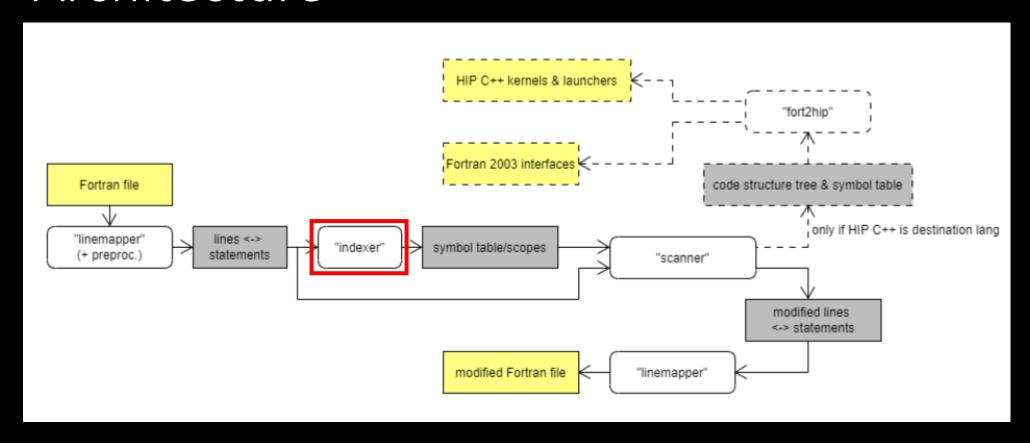
Components: "linemapper"

- Single lines can contain multiple statements in Fortran (green and blue) or spread over multiple lines (orange)
- "linemapper" maps code lines <-> statements
 - Maps also allow to append/prepend epilog/prolog
- Has C-style preprocessor to filter lines & evaluate macros
- Other toolchain components use/modify linemaps
 - Code generation works with the preprocessed statements
 - S2S translation modifies the maps and flags them
- "linemapper" then modifies file according to flagged maps

```
a(i) = 5; b(i) = 2 c(i) = 5 * & (a(i) + b(i))
```



Architecture



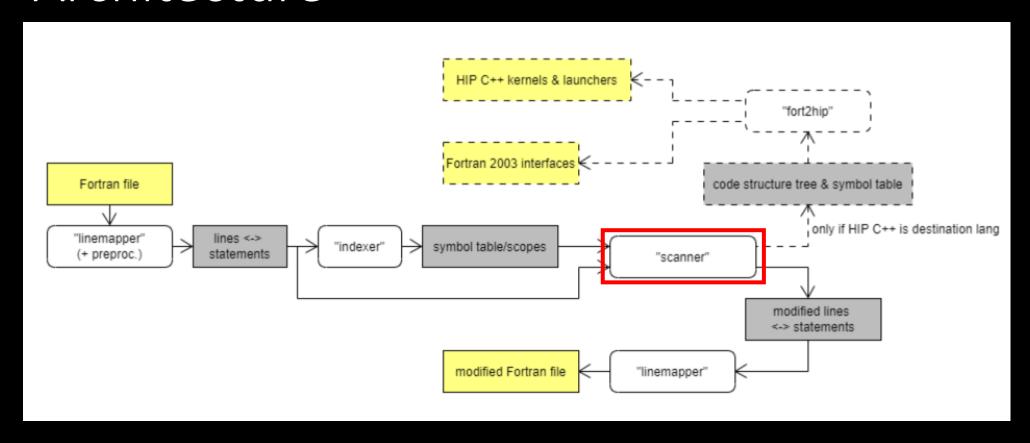


Components: "indexer"

- Creates GPUFORT mod file (json) per module/program/subprogram:
 - Collects info on modules, programs, procedures, derived types & variables
 - No dependency on particular compiler and their mod file format
- Can create scopes for modules, programs, procedures
 - Resolves module and parameter dependencies
- Variable records contain info on type & kind, rank, name, ... plus
 - CUDA Fortran qualifiers such as device, managed, pinned, ...
 - If variables are subject to an OpenACC declare directive
- Subprogram records contain arguments, result name, ... plus
 - CUDA Fortran procedure modifiers such as device/host/host,device/global
 - If subprogram is subject to an OpenACC routine directive



Architecture



AMD

Components: "scanner"

```
integer parameter :: N = 10
integer :: i
integer(4) :: x(N), y(N), y exact(N)
```

```
y_exact(:) = 3
!$acc data copy(x(1:N),y(1:N))
!$acc parallel loop
do i = 1, N
    x(i) = 1
    y(i) = 2
end do
```

```
!$acc parallel

!$acc loop

do i = 1, N

y(i) = x(i) + y(i)

end do

!$acc end parallel

!$acc end data
```

- Root
 - STProgram
 - STDeclaration
 - STDeclaration
 - STDeclaration
 - STAccDirective
 - STLoopKernel
 - STLoopKernel
 - STAccDirective
- Iterates statements and puts interesting blocks into a tree
- Avoids full parses of statements <u>during tree creation</u> (cheap) and records only statements meaningful for offloading / compute (reduced complexity)
- Calls "translator" backend on identified blocks in source modification / code generation phase for in-depth analysis and transformation of marked code blocks



Parsing

- Strategy is to use fast standard string comparison and re module; e.g. for marking/filtering in "scanner" and "indexer" ...
 - re.split((<tokens>),text,flags) for tokenizing
 - re.compile/re.sub for simple pattern matching
- ... and more sophisticated parsing tools for complicated expressions such as arithmetic & logical expression, assignments, function calls, ...
 - Tool of choice is pyparsing
- Remark: The frontend is not the hard part, the backend is!



AMD

On pyparsing (1): Syntax trees

```
# !/usr/bin/env python3
import pyparsing as pp
# grammar
rvalue = pp.pyparsing_common.identifier
op = pp.Literal("+")
expr = rvalue + op + rvalue
# test
print(expr.parseString("a + b"))
# output: ['a','+','b']
```

- pyparsing is a quick way to construct and execute grammars directly in python [github.com/pyparsing/pyparsing]; e.g. used by Red Hat Ansible
- Usage of *parse actions* allows you to generate an abstract syntax tree from a string expression
 - AST structure is as you define in ___init___
- pyparsing comes with some great helpers ...



On pyparsing (1): Syntax trees

```
# !/usr/bin/env python3
import pyparsing as pp
# grammar
rvalue = pp.pyparsing_common.identifier
op = pp.Literal("+")
expr = rvalue + op + rvalue
class Rvalue():
  def init (self, tokens):
    self. value = tokens
class Op():
  def __init__(self,tokens):
    self. op = tokens
rvalue.setParseAction(Rvalue)
op.setParseAction(Op)
print(expr.parseString("a + b"))
# output: [ main__.RValue object ..., __main__.Op object ...,
```

main .RValue object ...]

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On pyparsing (2): Shortcuts

pyparsing.InfixNotation(...)

```
import pyparsing as pyp
number = pyp.pyparsing_common.number
identifier = pyp.pyparsing common.identifier
         = identifier | number
rval
expr = pyp.infixNotation(rval, [\
  (pyp_oneOf('* /'), 2, pyp_opAssoc_LEFT),\
  (pyp_oneOf('+ -'), 2, pyp_opAssoc_LEFT),\
  (pyp_oneOf('-'), 1, pyp_opAssoc_LEFT),\
]) + pyp.stringEnd()
print(expr parseString("-5 + 3*(a+1.0)"))
```

pyparsing.Forward()

```
import pyparsing as pyp

identifier = pyp.pyparsing_common.identifier
derived_type_rvalue = pyp.Forward()

derived_type_elem = identifier + pyp.Literal("%") +\
    derived_type_rvalue )
derived_type_rvalue <<= derived_type_elem | identifier

print(derived_type_elem parseString("a%b"))
print(derived_type_elem parseString("a%bb"))
print(derived_type_elem parseString("a%bb%c"))</pre>
```

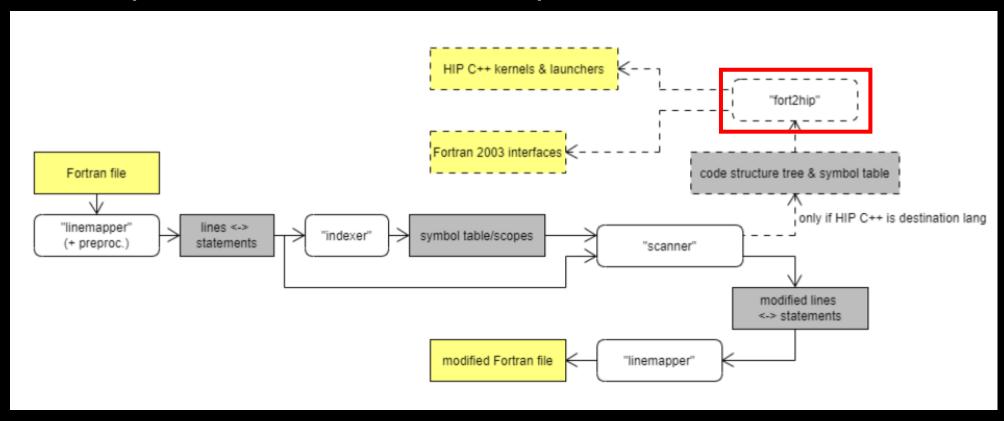


On pyparsing (3): Lessons learned

- Don't overuse pyparsing!
 - String/list comparisons and regular expressions can be much faster!
 - pyparsing error messages are difficult to understand:
 - parse parts of flow statements manually
- Don't express details via grammar!
 - Check details with string comparisons or regular expressions after the initial parsing
 - Bad: expr = pyp.Literal("parameter") | pyp.Literal("device") | ...
 - Faster:
 - grammar: expr = pyp.Regex(r''[a-z]+'')
 - check in parse action __init__: if tokens[0] in ["parameter","device",...]
- Run pyparsing.ParserElement.enablePackrat() at the top of your grammar to enable caching of partial results when dealing with nested expressions
 - Makes parsing often much faster!



Components: "fort2hip"





Components: "fort2hip"

- Given symbol table and "scanner tree", "fort2hip" generates HIP C++ kernels, launchers and interfaces to the Fortran code
- Code generation is simplified thanks to Jinja
 - "Jinja is a fast, expressive, extensible templating engine. Special placeholders in the template allow writing code similar to Python syntax." [palletsprojects.com/p/jinja/]
 - Widely used by web frameworks
 - Takes Python JSON objects as input
- "fort2hip" relies on HIP C++ header file providing overloaded device math functions macros to facilitate type conversions, complex variable construction, max/min with arbitrary argument list, ... (also generated with Jinja)



AMD

On Jinja2: Example from hipFORT

```
{% for tuple in datatypes %}{% for dims in dimensions %}{% if dims > 0 %}
{% set size = 'size(dest)*' %}
{% set rank = ',dimension(' + ':,'*(dims-1) + ':)' %}
{% else %}
{% set size = " %}
{% set rank = " %}
{% endif %}
{% for size t in sizeTypes %}
{% set name = 'hipMemcpyAsync ' + tuple[0] + ' ' + dims | string + ' ' + size t -%}
  function {{name}}(dest,src,length,myKind,stream)
   use iso c binding
   use hipfort enums
   use hipfort types
   implicit none
   {{tuple[2]}},target{{ rank }},intent(inout) :: dest
   {{tuple[2]}},target{{ rank }},intent(in) :: src
   integer({{size t}}),intent(in) :: length
   integer(kind(hipMemcpyHostToHost)) :: myKind
   type(c ptr)::stream
   integer(kind(hipSuccess)) :: {{name}}
  {{name}} = hipMemcpyAsync b(c loc(dest),c loc(src),length*{{tuple[1]}} 8,myKind,stream)
  end function
{% endfor %}
```

- Jinja is also great for generating overloaded interfaces for C libraries (such as the ROCm)
- Example on the left is a (simplified) snippet that generates interfaces for hipMemcpyAsync in hipFORT
- Input is a python dict, e.g.:

```
context = {
    "datatypes": [
        ["r4","4","real(4)"],
        ["r8","8","real(8)"],
        ...],
    "dimensions": range(0,7+1)
}
```



Additional features

- GPUFORT can be configured via CLI (see: gpufort –help) or Python 3 config file (see: gpufort –print-config-defaults)
- Selected CLI options (also accessible via config file):
 - Extract original loop nests and put them into separate "CPU kernel launcher" routine:
 - --emit-cpu-impl
 - Downloads GPU data and then runs original loop on CPU
 - Can be plugged into original application and then gradually replaced by manually tested GPU implementation
 - Great for creating unit tests (in Fortran or C++)
 - Generate debug code into kernel launcher routines (GPU and CPU):
 - --emit-debug-code
 - Output kernel (launcher) argument values
 - Download and print GPU array elements, array norms, min and max
 - Great to gather (application specific) input data for unit tests