

CSO ASSIGNMENT 2

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Q3-Info about my Inspiron 15 3000 Laptop system

Operating System(OS):

Kernel	: Linux 5.14.0-1038-oem (x86_64)
C Library	: GNU C Library / (Ubuntu GLIBC 2.31-0ubuntu9.9) 2.31
Distribution	: Ubuntu 20.04.4 LTS(GNOME version: 3.36.8)
Computer Name	: rohit-Inspiron-15-3511
Language	: en_GB.UTF-8 (en_IN:en)(OS-Type: 64-bit)
Windowing System	: X11

USB Devices

Linux Foundation 3.0 root hub
Microdia Integrated Webcam HD
Realtek Semiconductor Corp. Bluetooth Radio
Linux Foundation 2.0 root hub

Battery

Battery	: LI-Ion,3-cell, 41 Wh, lithium-polymer type battery
Capacity	: 32 / Normal (SMP manufactured)
Model Number	: DELL MGCMS16(Serial Number = 5115)
Battery voltage	: 11.25 VDC (4 hrs charging time)
Battery Height x Width x depth	: 206.4 mm x 82 mm x 5.75 mm
Battery Temperature Range(Operating):	0°C to 35°C

PCI Devices

Host bridge	: Intel Corporation Device 9a14 (rev 01)
VGA compatible controller:	Intel Corporation Device 9a49(rev 01)(prog-if 00 [VGA controller])
Signal processing controller	: Intel Corporation Device 9a03,9a0d (rev 01)
RAID bus controller	: Intel Corporation Volume Management Device NVMe RAID Controller
USB controller	: Intel Corporation Device a0ed (rev 20) (prog-if 30 [XHCI])
(RAM memory,PCI bridge)	: Intel Corporation Device (a0ef,(a0b1,a0bc)) (rev 20)
Serial bus controller [0c80]	: Intel Corporation Device a0e8,a0e9,a0a4 (rev 20)
(Communication,SATA) controller	: Intel Corporation Device (a0e0,a0d3) (rev 20)
System peripheral	: Intel Corporation Device 09ab
(ISA bridge,Audio device,SMBus)	: Intel Corporation Device (a082,a0c8,a0a3) (rev 20)
Network controller:	Realtek Semiconductor RTL8821CE 802.11ac PCIe Network Adapter
Non-Volatile memory controller	: Sandisk Corp Device 5007 (rev 01)

Storage

Model	: ATA ST1000LM035-1RK1(SEAGATE) Disk (Revision--1002)
SCSI Controller	: scsi0
HDD,SSD Storage	: 1 TB,256 GB

DMI

Product	: Name--Inspiron 15 3511 Family--Inspiron Vendor--Dell Inc.
BIOS	: Vendor--Dell Inc. Version--1.11.0
Board	: Name--0042CN Vendor--Dell Inc Version--A00
Chassis	: Vendor--Dell Inc. Type--[10] Notebook

Sensors

../../../../BAT0/in0	Voltage 10.99V	Battery
../../../../nvme0/temp1	Temperature 33.85°C	HDD
coretemp/temp1	Temperature 42.00°C	CPU
coretemp/temp2	Temperature 39.00°C	CPU
coretemp/temp3	Temperature 40.00°C	CPU
coretemp/temp4	Temperature 41.00°C	CPU
coretemp/temp5	Temperature 38.00°C	CPU
thermal/thermal_zone2	Temperature 39.05°C	CPU
thermal/thermal_zone0	Temperature 39.05°C	CPU
thermal/thermal_zone3	Temperature 41.05°C	CPU
thermal/thermal_zone1	Temperature 20.00°C	CPU
thermal/thermal_zone4	Temperature 41.00°C	CPU

Processor

Name	: 11th Gen Intel Core i5-1135G7 @ 2.40GHz
Logical CPU Configuration	: 8x4200.00MHz (Device ID--0x9A49)(ISA-64 bit)
Processor wattage	: 15 W
(Cores,Threads )	: (4,8)
Max Turbo Frequency,Clock Speed	: 4.20 GHz, 2.40 GHz
Cache,Max Memory Size	: 8 MB Intel® Smart Cache, 64 GB
Bus Speed,Clocks	: 4 GT/s,400.00-4200.00 MHz 8x
Lithography	: 10 nm SuperFin
Configurable TDP-(up,down)	Base Frequency:(2.40 GHz(28 W),900 MHz(12 W))
Processor Graphics	: Intel® Iris® Xe Graphics

Graphics Max Dynamic Frequency : 1.30 GHz  
Graphics Output : eDP 1.4b, MIPI-DSI 2.0, DP 1.4, HDMI 2.0b  
Execution Units : 80 (TJUNCTION: 100°C)  
OpenCL\* Support : 3.0 (Sockets Supported: FCBGA1449)  
# of Displays Supported,Max # of Memory Channels : 4,2  
Multi-Format Codec Engines,Max CPU Configuration : 1,2  
(Microprocessor,Chipset) PCIe Revision: (Gen 4,Gen 3)  
Package Size : 45.5x25

Memory

Memory : 7.5GiB (DDR4)  
Memory slots,speed : Two SODIMM slots,2666 MHz  
(Max,Min) memory configuration :(16,4) GB  
Memory size per slot : 4 GB, 8 GB, 16 GB  
RAM : 8 GB

File Systems

NAME	FSTYPE	LABEL	MOUNTPPOINT
sda			
└sda1			
└sda2	BitLocker		
└sda3	ext4		/home
nvme0n1			
└nvme0n1p1	vfat	ESP	/boot/efi
└nvme0n1p2			
└nvme0n1p3	BitLocker		
└nvme0n1p4	ntfs	WINRETOOLS	
└nvme0n1p5	ntfs	Image	
└nvme0n1p6	ntfs	DELLSUPPORT	
└nvme0n1p7	ext4		/

Benchmark scores

BenchMarks	Score
CPU Fibonacci	0.57
CPU BlowFish	1.67
CPU Cryptohash	712.79
GPU Drawing	8658.25
FPU FFT	0.84

Kernel Modules

rfcomm : Bluetooth RFCOMM ver 1.11  
libcrc32c : CRC32c (Castagnoli) calculations  
br\_netfilter : Linux ethernet netfilter firewall bridge  
llc : LLC IEEE 802.2 core support  
cmac : CMAC keyed hash algorithm  
ecdh\_generic : ECDH generic algorithm  
mc : Device node registration for media drivers  
soundwire\_intel : Intel SoundWire Link Driver  
soundwire\_generic\_allocation : SoundWire Generic Bandwidth Allocation  
snd\_soc\_acpi\_intel\_match : Intel Common ACPI Match module  
snd\_hda\_intel : Intel HDA driver  
snd\_intel\_dspcfg : Intel DSP config driver  
snd\_intel\_sdw\_acpi : Intel SoundWire ACPI helpers  
snd\_hwdep : Hardware dependent layer  
intel\_tcc\_cooling : TCC offset cooling device Driver  
x86\_pkg\_temp\_thermal: X86 PKG TEMP Thermal Driver  
snd\_seq\_midi : Advanced Linux Sound Architecture sequencer MIDI synth.  
dell\_laptop : Dell laptop driver  
intel\_powerclamp : Package Level C-state Idle Injection for Intel CPUs  
intel\_rapl\_msr : Driver for Intel RAPL (Running Average Power Limit) control via MSR interface  
snd\_seq\_midi\_event : MIDI byte &lt;-&gt; sequencer event coder  
coretemp : Intel Core temperature monitor  
i915 : Intel Graphics  
crct10dif\_pclmul : T10 DIF CRC calculation accelerated with PCLMULQDQ.  
snd\_rawmidi : Midlevel RawMidi code for ALSA.  
ghash\_clmulni\_intel : GHASH hash function, accelerated by PCLMULQDQ-NI  
rtw88\_pci : Realtek 802.11ac wireless PCI driver  
aesni\_intel : Rijndael (AES) Cipher Algorithm, Intel AES-NI instructions optimized  
rtw88\_core : Realtek 802.11ac wireless core module  
mac80211 : IEEE 802.11 subsystem  
snd\_seq : Advanced Linux Sound Architecture sequencer.  
dell\_wmi : Dell laptop WMI hotkeys driver  
snd : Advanced Linux Sound Architecture driver for soundcards.  
dell\_smbios : Common functions for kernel modules using Dell SMBIOS  
input\_leds : Input -&gt; LEDs Bridge  
dcdbas : Dell Systems Management Base Driver (version 5.6.0-3.4)  
mei\_me : Intel(R) Management Engine Interface  
dell\_wmi\_descriptor : Dell WMI descriptor driver  
intel\_pmt\_telemetry : Intel PMT Telemetry driver  
intel\_pmt\_class : Intel PMT Class driver  
i2c\_algo\_bit : I2C-Bus bit-banging algorithm  
processor\_thermal\_device\_pci\_legacy : Processor Thermal Reporting Device Driver  
mei : Intel(R) Management Engine Interface  
intel\_rapl\_common : Intel Runtime Average Power Limit (RAPL) common code  
sysimgblt : 1-bit/8-bit to 1-32 bit color expansion (sys-to-sys)  
igen6\_edac : MC Driver for Intel client SoC using In-Band ECC  
int3403\_thermal : ACPI INT3403 thermal driver  
int340x\_thermal\_zone: Intel INT340x common thermal zone handler  
int3400\_thermal : INT3400 Thermal driver  
acpi\_thermal\_rel : Intel acpi thermal rel misc dev driver  
i2c\_i801 : I801 SMBus driver  
intel\_lpss\_pci : Intel LPSS PCI driver  
intel\_lpss : Intel LPSS core driver  
intel\_pmt : Intel Platform Monitoring Technology PMT driver  
pinctrl\_tigerlake : Intel Tiger Lake PCH pinctrl/GPIO driver

Q4 Given that assembly code (a,b) implies  
 push b  
 push a  
 call assemblycode → Process-①

In lines <+18> and <+38>, it is assumed that "assembly code" should be present in place of "asm2"

In Q4, assemblycode (0xc, 0x15) is called

push 0x15  
 push 0xc  
 call assemblycode

Stack-Status (12)<sub>10</sub>

0x15	→ ebp + 0xc
0xc	→ ebp + 0x8
ret	→ ebp + 0x4
prev ebp	→ ebp

<+0>: push ebp # push ebp onto stack

<+1>: mov ebp, esp # copy esp to ebp

<+3>: sub esp, 0x10 → (16)<sub>10</sub> # reserving 16 bytes on stack

# esp register is an indicator for the top of the stack, it changes/grows as stack grows/shrinks

# ebp is helping register, we push content of ebp on stack, then we copy esp to ebp, that is why when we refer to other items on stack, we use constant value of ebp (not changing value of esp), (y<sub>1</sub>, y<sub>2</sub>, y<sub>3</sub>, y<sub>4</sub> are local variables)

0x15	→ ebp + 0xc
0xc	→ ebp + 0x8
ret	→ ebp + 0x4
prev ebp	→ ebp
	→ ebp - 0x4 (y <sub>1</sub> )
	→ ebp - 0x8 (y <sub>2</sub> )
	→ ebp - 0xc (y <sub>3</sub> )
	→ ebp - 0x10 (y <sub>4</sub> )

<+6>: mov eax, DWORD PTR [ebp+0xc]  
 # eax = 0x15

<+9>: mov DWORD PTR [ebp-0x4], eax  
 # y<sub>1</sub> = 0x15

<+12>: mov eax, DWORD PTR [ebp+0x8]  
 # eax = 0xc

<+15>: mov DWORD PTR [ebp-0x8], eax  
 # y<sub>2</sub> = 0xc

<+18>: jmp 0x50c <assemblycode+31>  
 # jumps to <+31> else below

0x15	→ ebp + 0xc
0xc	→ ebp + 0x8
ret	→ ebp + 0x4
prev ebp	→ ebp
0x15	→ ebp - 0x4 (y <sub>1</sub> )
0xc	→ ebp - 0x8 (y <sub>2</sub> )
	→ ebp - 0xc (y <sub>3</sub> )
	→ ebp - 0x10 (y <sub>4</sub> )

<+20>: add DWORD PTR [ebp-0x4], 0x1  
 # y<sub>1</sub> = 0x15 + 0x1 = 0x16

<+24>: add DWORD PTR [ebp-0x8], 0xaf  
 # y<sub>2</sub> = 0xc + 0xaf = 0xbb

<+31>: cmp DWORD PTR [ebp-0x8], 0xa3d3  
 # compares y<sub>2</sub> and 0xa3d3

<+38>: jle 0x501 <assemblycode+20>  
 # jumps if y<sub>2</sub> ≤ 0xa3d3

0x15	→ ebp + 0xc
0xc	→ ebp + 0x8
ret	→ ebp + 0x4
prev ebp	→ ebp
0x15	→ ebp - 0x4 (y <sub>1</sub> )
0xbb	→ ebp - 0x8 (y <sub>2</sub> )
	→ ebp - 0xc (y <sub>3</sub> )
	→ ebp - 0x10 (y <sub>4</sub> )

# The loop in the above block of code runs until y<sub>2</sub> > 0xa3d3, which would take  $\frac{0xa3d3 - af}{175} = \frac{41739}{175} \approx 240$  iterations

↳ when we enter the loop

\* It implies that we add 1 to 0x15, 240 times  $\Rightarrow y_1 = 0x15 + 0xf0 = 0x105$

Stack-status at the end of loop

Stack-status

0x15	$\rightarrow$ ebp+0xc
0xc	$\rightarrow$ ebp+0x8
ret	$\rightarrow$ ebp+0x4
prev ebp	$\rightarrow$ ebp
0x105	$\rightarrow$ ebp-0x4(y <sub>1</sub> )
0xa41c	$\rightarrow$ ebp-0x8(y <sub>2</sub> )
	$\rightarrow$ ebp-0xc(y <sub>3</sub> )
	$\rightarrow$ ebp-0x10(y <sub>4</sub> )

<+40>: mov eax, DWORD PTR [ebp-0x4]

# eax = 0x105

# The 4 bytes at [ebp-0x4] are moved to eax register. Arithmetic opcodes cannot operate with two memory-operands. Data needs to be moved to a register first before the arithmetic is performed, eax now holds the value of y<sub>1</sub> variable

<+43>: leave

# Cleanup stack (reverse the "mov ebp, esp" from above. Destroys the stack frame, Essentially maps to pop ebp and restores the old stack-base pointer

<+44>: ret

# Pops the top of stack treating the value as a return address and sets the program pointer (eip) to this value. The return address typically holds the address of the instruction directly after the call instruction that brought execution to this function

# assemblycode(0xc, 0x15) returns value of 0x105

assemblycode(int x<sub>1</sub>, int x<sub>2</sub>)

{  
  int c = x<sub>2</sub>; //ebp-0x4  
  int d = x<sub>1</sub>; //ebp-0x8

while(d <= 0xa3d3)

{  
  c = c + (0x1);  
  d = d + (0xaf);

}

return c;

}

equivalent  
C-code



Q5 a) `gcc q5.c → ./a.out` → prints string  
`./q5.out` → No such file/directory

Interpreter  
①

Interpreter for a.out = `./libc6-arm64-2.27-3ubuntu1-i386.ld`

Interpreter for q5.out = `/lib64/ld-linux-x86-64.so.2`

After interpreter for q5.out is changed to Interpreter-① and executed it prints string. Dynamic binaries need external components to run correctly. Different Interpreter need different external components for corresponding dynamic binary (DYN-shared object file). Interpreters translates program one-statement at a time. Often external components are normal libraries → (common functions)

Q5

(b) The binary file uses 64-bit architecture. It follows little-endian byte ordering. It is a shared object file (DYN). The expected machine type for this file is AMD X-86-64. (UNIX-System V) ABI is used (to understand overlap in common functions)