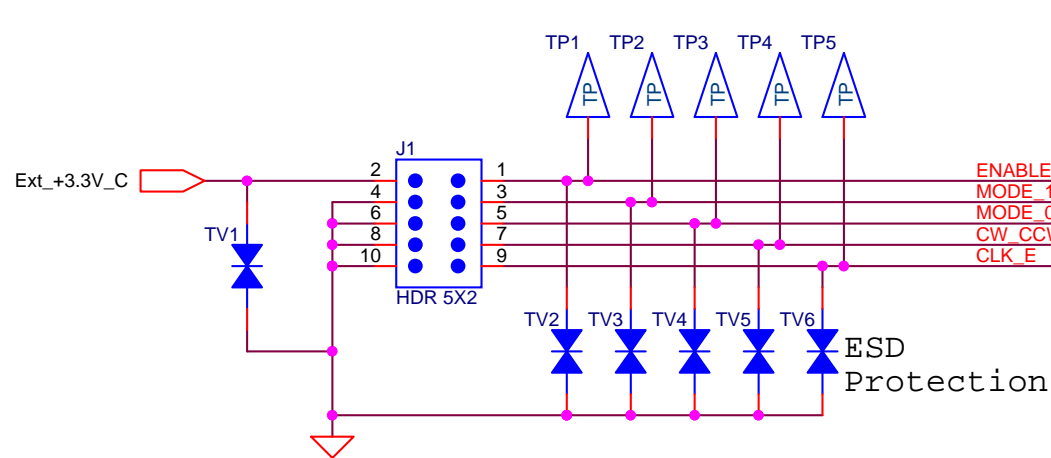
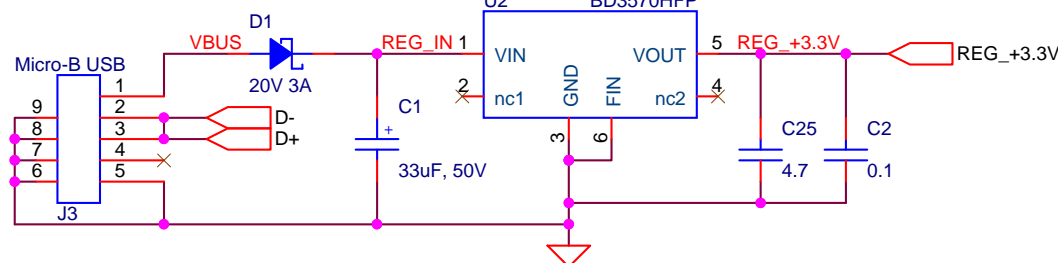


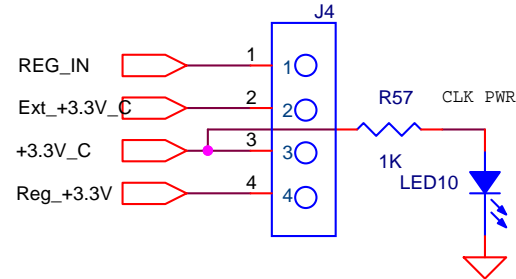
## CLOCK-IN EXTERNAL INPUTS



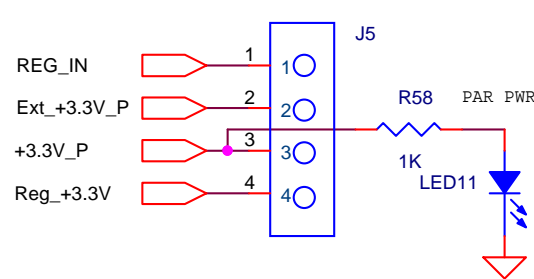
## INPUT POWER



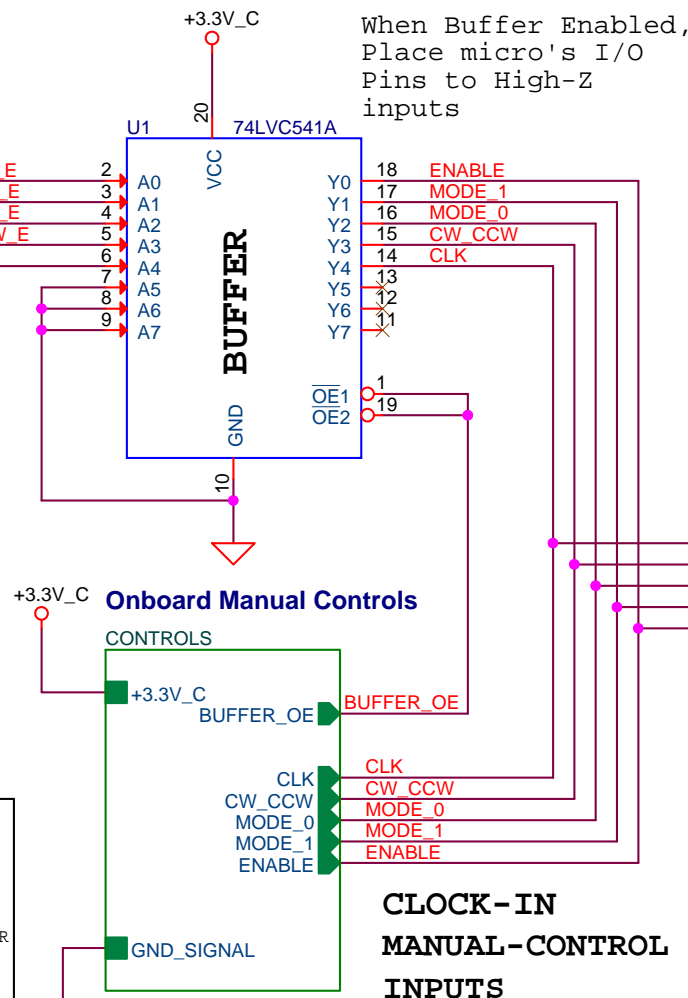
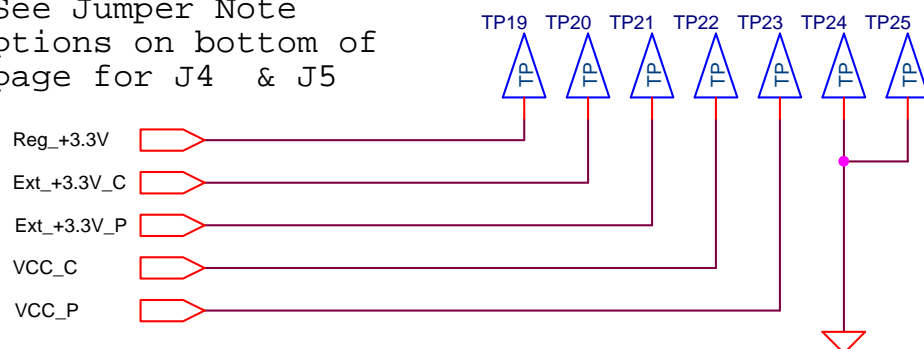
### CLOCK-IN POWER SOURCE SELECTOR\*



### PARALLEL-IN POWER SOURCE SELECTOR\*

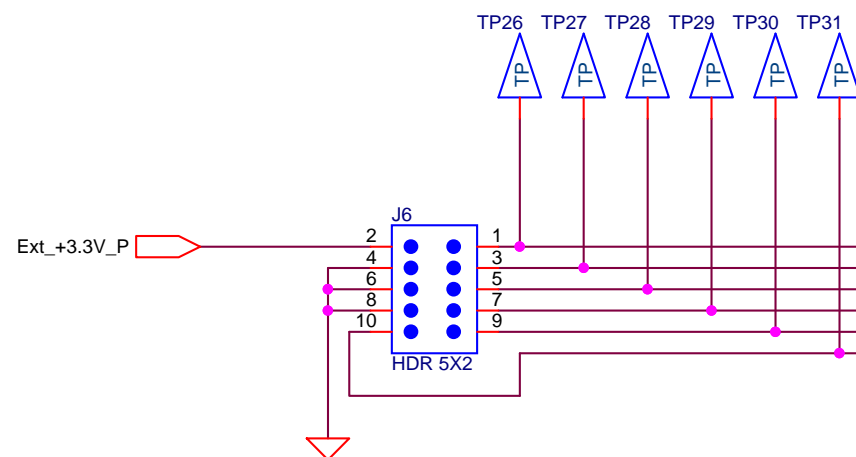


\*See Jumper Note  
Options on bottom of  
page for J4 & J5

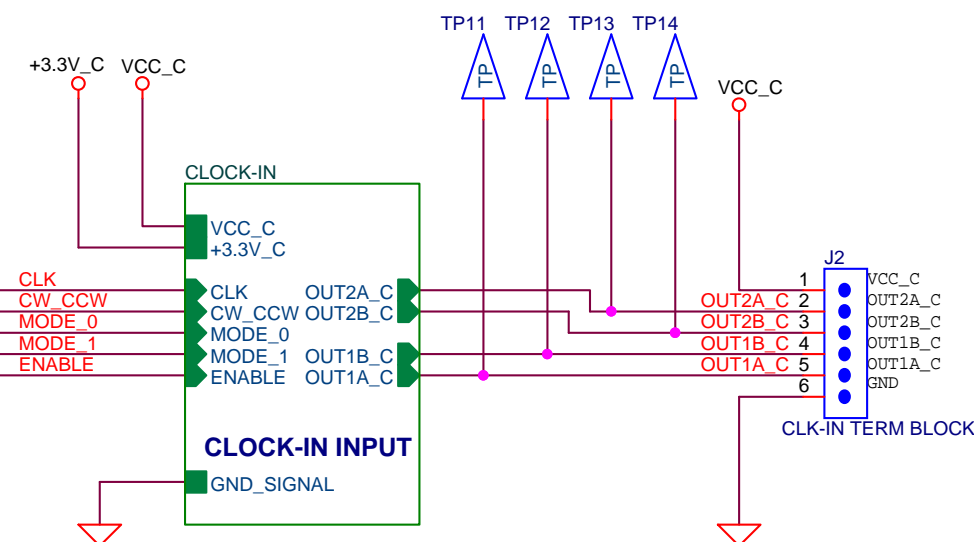


## CLOCK-IN MANUAL-CONTROL INPUTS

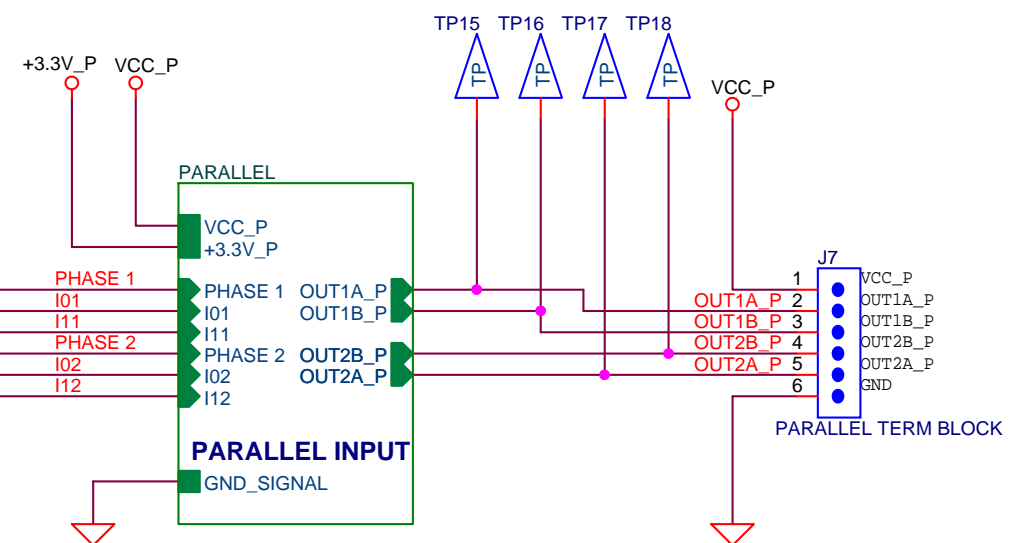
## PARALLEL EXTERNAL INPUTS



## CLOCK-IN EXTERNAL OUTPUTS



## PARALLEL EXTERNAL OUTPUTS



## NOTES:

- 1.) Unless otherwise stated, all Capacitor values are in MicroFarads ( $\mu\text{F}$ ); Resistor values are in Ohms.
- 2.) All Chip Resistors and Capacitors are in 0805 packages unless otherwise stated

\*OPTION #1: JMP Pin 4 to Pin 3 to Provide +3.3V\_C (or +3.3V\_P) from Regulated +3.3V Output

\*OPTION #2: JMP Pin 2 to Pin 3 to Provide +3.3V\_C (or +3.3V\_P) from External +3.3V Input

\*OPTION #3: JMP Pin 4 to Pin 3 AND Pin 1 to Pin2 to Provide +3.3V\_C (or +3.3V\_P) from External Input from any voltage from 4.5V to 30V

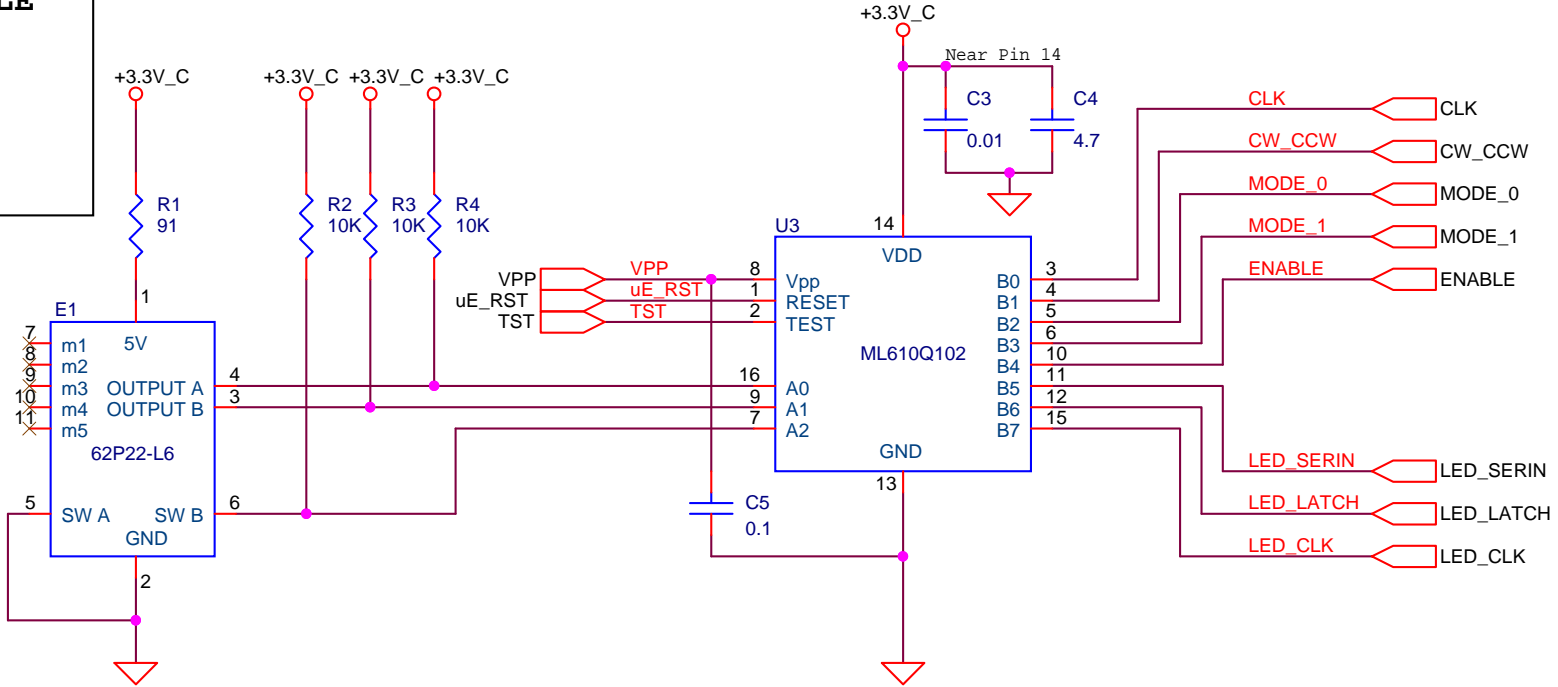
Title		
Rohm BD6387x Eval Board - Top Schematic		
Size B	Document Number <Doc>	Rev 7
Date:	Monday, February 11, 2013	Sheet 2 of 5

## ENCODER TRUTH TABLE

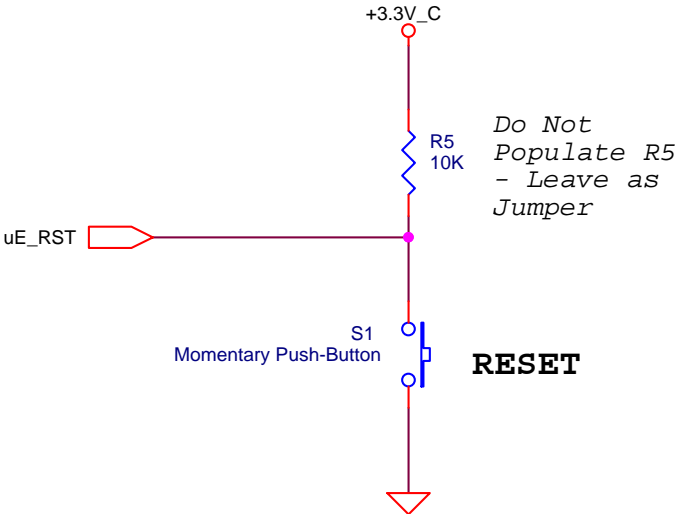
CLOCKWISE ROTATION

POSITION	OUTPUT A	OUTPUT B
1	LOW	LOW
2	HIGH	LOW
3	HIGH	HIGH
4	LOW	HIGH

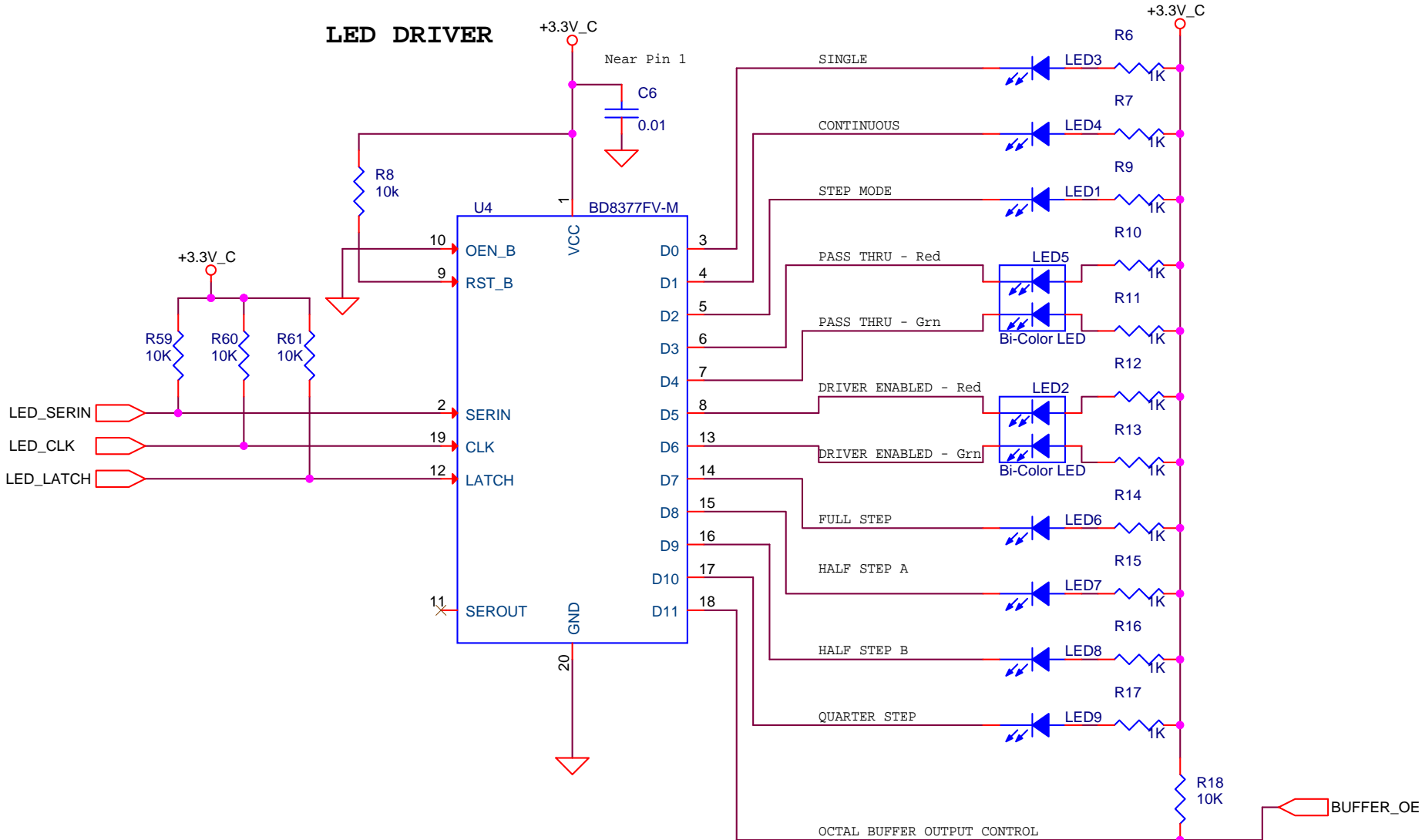
## MICROCONTROLLER



## MICROCONTROLLER RESET

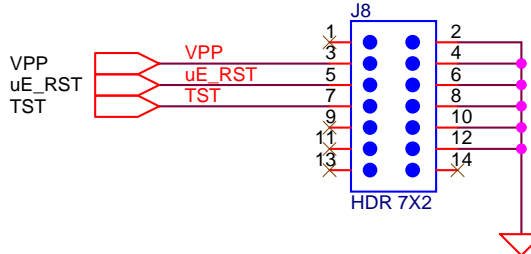


## LED DRIVER

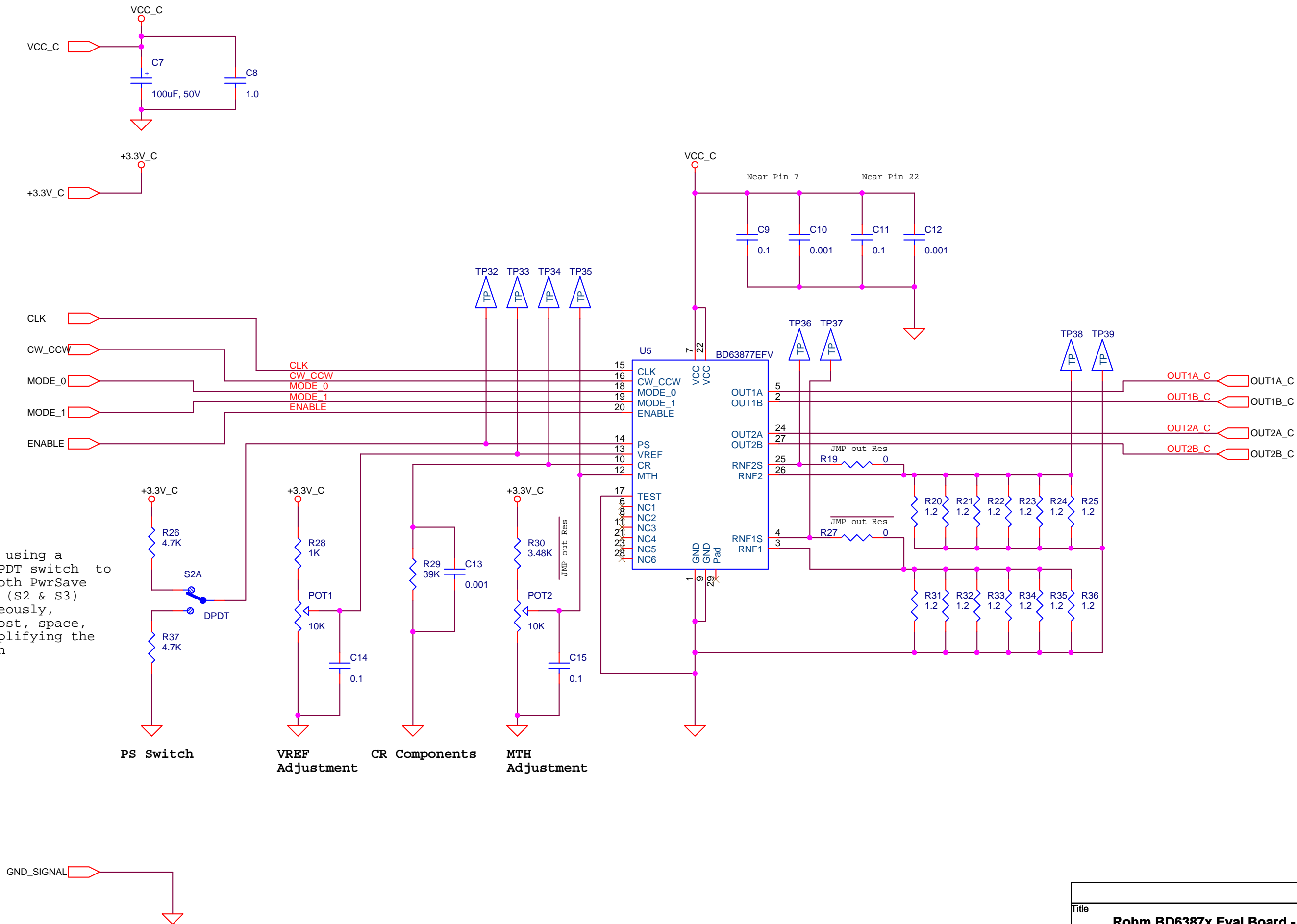


## MICROCONTROLLER DEBUGGER

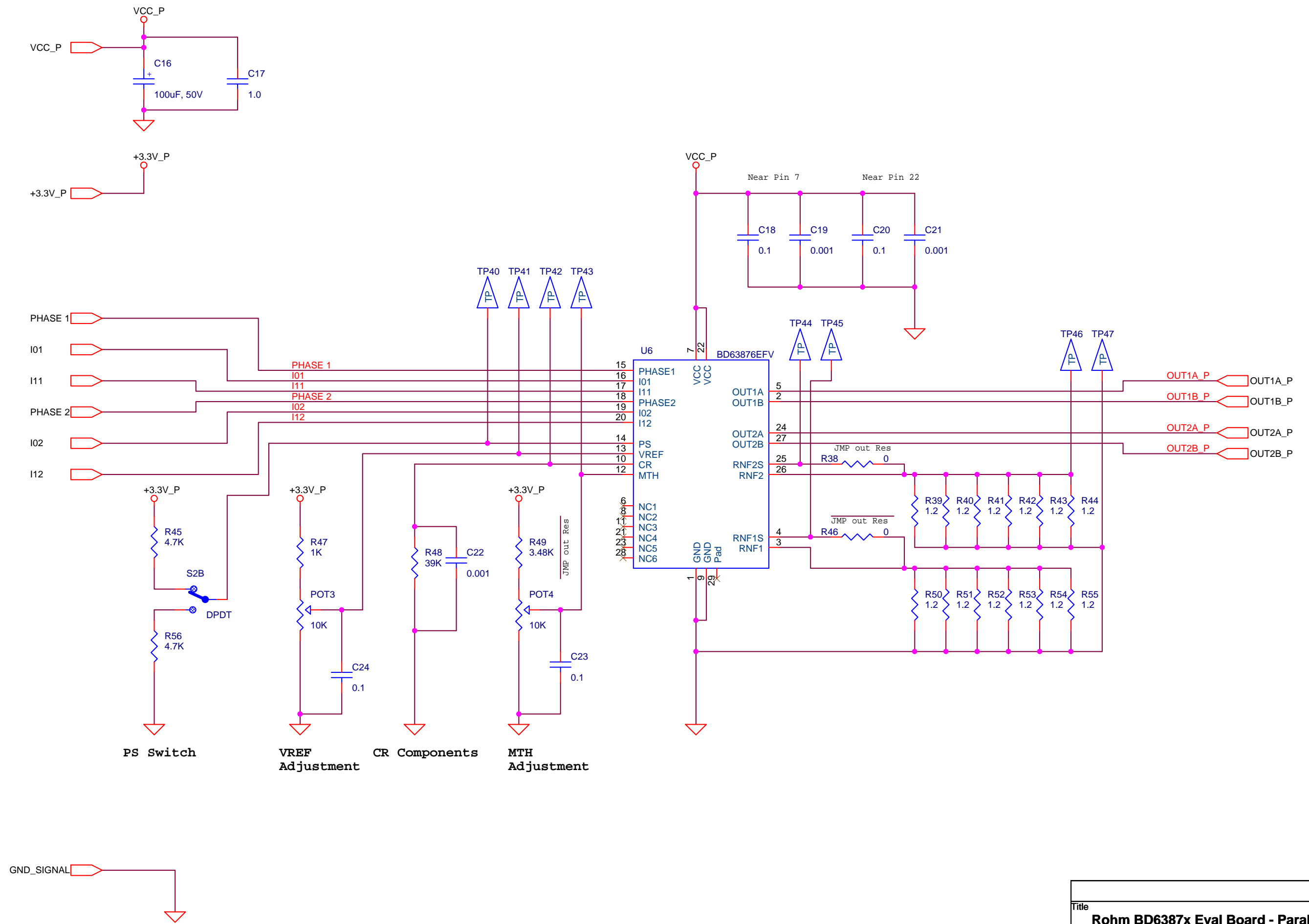
Pin1 Disconnected so as Not To Allow uEase to Power Board



Consider using a single SPDT switch to control both PwrSave Switches (S2 & S3) simultaneously, saving cost, space, and simplifying the operation



Title		
Rohm BD6387x Eval Board - Clock-In Schematic		
Size B	Document Number <Doc>	Rev 7
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Title		
Rohm BD6387x Eval Board - Parallel Schematic		
Size B	Document Number <Doc>	Rev 7
Date:	Monday, February 11, 2013	Sheet 5 of 5