

File: RP2040.kicad_sch

ADC GPIO pins have diode to VDDIO (other GPIO do not)
FET stops leakage through ADC3 pin diode into 3V3 net
when 3V3 supply is off (VSYS present but 3V3_EN low)

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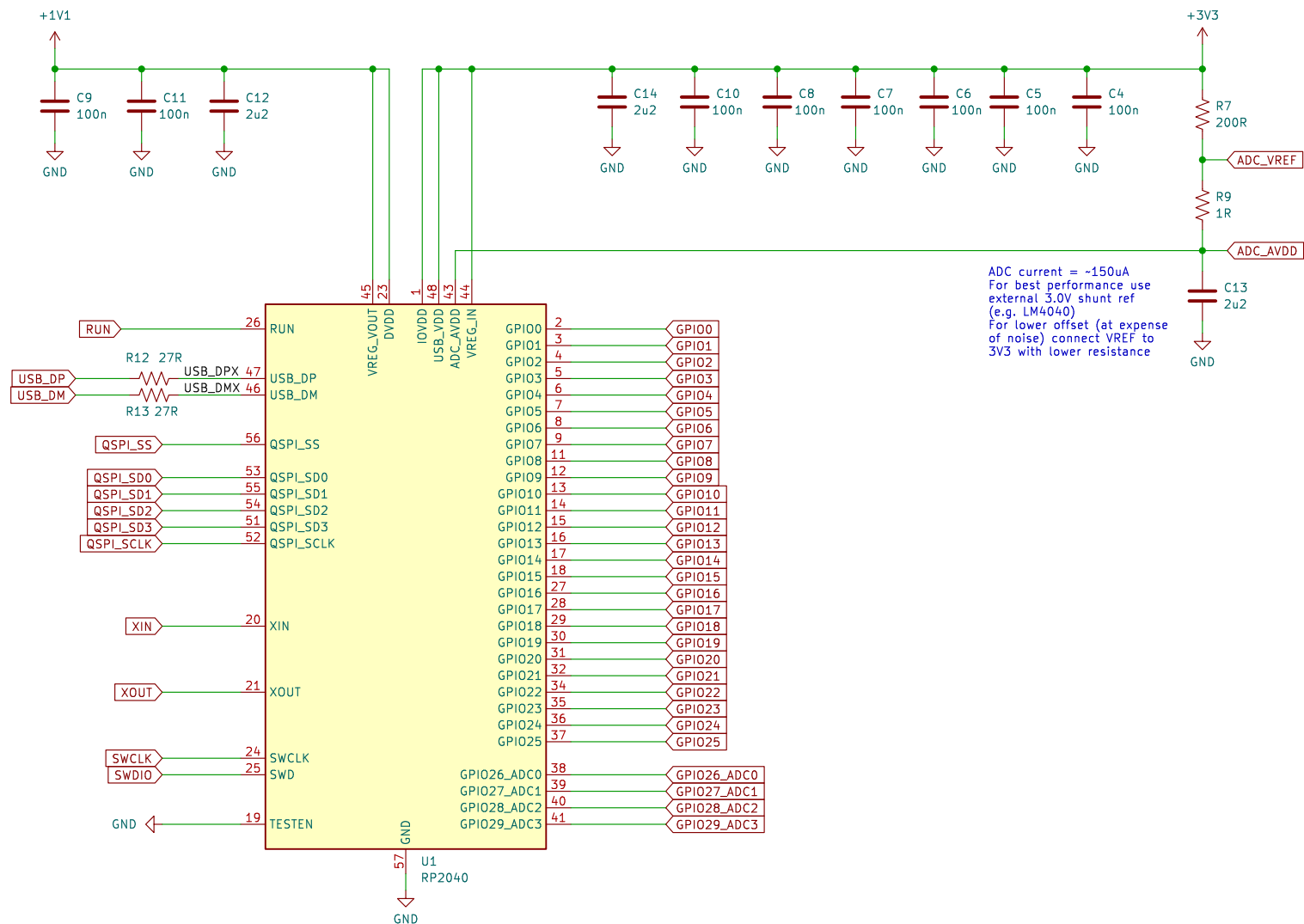
Title: project piCo

Size: A4 Date: 2024-01-25

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ADC current = ~150uA
For best performance use
external 3.0V shunt ref
(e.g. LM4040)
For lower offset (at expense
of noise) connect VREF to
3V3 with lower resistance

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