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<Bachelor-/Masterthesis>

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# **Chair of Electronic Design Automation**



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# **Declaration of Authorship**

I hereby declare that I have completed this thesis independently in accordance with the examination regulations without the assistance of third parties except for the support provided by my supervisor. I declare that I have cited all sources and materials used completely and accurately, and that I have marked everything that has been taken from other people's work unchanged, abbreviated or analogously.

Kaiserslautern,	5. Juni 2024		
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## Kurzfassung

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## **Abstract**

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## 1 Section

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#### 1.1 Subsection

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## 2 Another Section

### 2.1 Another Subsection

## A Appendix

## Literatur

- [1] A. Biere and W. Kunz, "SAT and ATPG: Boolean Engines for Formal Hardware Verification," in *Proceedings of the 2002 IEEE/ACM International Conference on Computer-Aided Design*, ser. ICCAD '02. Association for Computing Machinery, 2002, p. 782–785.
- [2] E. Clarke, A. Biere, R. Raimi, and Y. Zhu, "Bounded Model Checking Using Satisfiability Solving," *Formal Methods in System Design*, pp. 7–34, 2001.
- [3] M. R. Fadiheh, J. Müller, R. Brinkmann, S. Mitra, D. Stoffel, and W. Kunz, "A Formal Approach for Detecting Vulnerabilities to Transient Execution Attacks in Out-of-Order Processors," in 2020 57th ACM/IEEE Design Automation Conference (DAC), 2020, pp. 1–6.