≡   saved 6 minutes ago ☁

EDITOR   ✓ NAV-TLV   LOG

```
1  \m5_TLV_version 1d: tl-x.org
2 ▾ \m5
3
4     // ===============================================
5     // Welcome, new visitors! Try the "Learn" menu.
6     // ===============================================
7
8     //use(m5-1.0)   /// uncomment to use M5 macro library.
9 ▾ \SV
10    // Macro providing required top-level module definition, random
11    // stimulus support, and Verilator config.
12    m5_makerchip_module   // (Expanded in Nav-TLV pane.)
13 ▾ \TLV
14    // $reset = *reset;
15    // Sequential circuit (Fibonacci Series with reset control : 1,1,
16 ▾  $num[31:0] = *reset ? 1
17                      : >>1$num + >>2$num;
18    //...
19
20    // Assert these to end simulation (before the cycle limit).
21    *passed = *cyc_cnt > 40;
22    *failed = 1'b0;
23 ▾ \SV
24    endmodule
25
```