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EDITOR   ✓NAV-TLV   LOG

```
1    \m5_TLV_version 1d: tl-x.org
2 ▾  \m5
3
4       // ===============================================
5       // Welcome, new visitors! Try the "Learn" menu.
6       // ===============================================
7
8       //use(m5-1.0)    /// uncomment to use M5 macro library.
9 ▾  \SV
10      // Macro providing required top-level module definition, random
11      // stimulus support, and Verilator config.
12      m5_makerchip_module    // (Expanded in Nav-TLV pane.)
13 ▾ \TLV
14      //$reset = *reset;
15      // Arithmetic on Bit Arrays or Vectors....
16      //|Arith
17      $Sum_out[4:0] = $inp1[3:0] + $inp2[3:0];
18      $Diff_out[4:0] = $inp1[3:0] - $inp2[3:0];
19      $Mul_out[7:0] = $inp1[3:0] * $inp2[3:0];
20      $Div_out[7:0] = $inp1[3:0] / $inp2[3:0];
21      //...
22
23      // Assert these to end simulation (before the cycle limit).
24      *passed = *cyc_cnt > 40;
25      *failed = 1'b0;
26   \SV
```