



saved 16 minutes ago

EDITOR

✓ NAV-TLV

LOG

```
1  \m5_TLV_version 1d: tl-x.org
2  \m5
3
4      // =====
5      // Welcome, new visitors! Try the "Learn" menu.
6      // =====
7
8      //use(m5-1.0)    /// uncomment to use M5 macro library.
9  \SV
10     // Macro providing required top-level module definition, random
11     // stimulus support, and Verilator config.
12     m5_makerchip_module    // (Expanded in Nav-TLV pane.)
13 \TLV
14     // $reset = *reset;
15     // Sequential circuit (Free Running Counter)....
16     // Stimulus.....
17     $inp1[0:0] = 1;
18     $inp2[0:0] = 0;
19     $op[0:0] = *reset;
20
21     // Summation of next state (feedback) and trigger input "1".....
22     $sum_sq[3:0] = $inp1 + >>1$cnt[3:0];
23
24     // Mux (2x1) Operation with reset load with "0000" ...
25     $cnt[3:0] = ($op == 1) ? $inp2:
26                     $sum_sq;
27     // Assert these to end simulation (before the cycle limit).
28     *passed = *cyc_cnt > 40;
29     *failed = 1'b0;
30
31 \SV
```