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EDITOR

✓ NAV-TLV

LOG

```
1  \m5_TLV_version 1d: tl-x.org
2  \m5
3
4      // =====
5      // Welcome, new visitors! Try the "Learn" menu.
6      // =====
7
8      //use(m5-1.0)    /// uncomment to use M5 macro library.
9  \SV
10     // Macro providing required top-level module definition, random
11     // stimulus support, and Verilator config.
12     m5_makerchip_module    // (Expanded in Nav-TLV pane.)
13 \TLV
14     // Combinational Calculators for Addition, Subtraction, Multiplic
15     // $reset = *reset;
16     // Stimulus .....
17     $op[1:0] = *cyc_cnt[1:0];
18     $val1[31:0] = $rand1[3:0];
19     $val2[31:0] = $rand2[3:0];
20
21     // Arithmetic Functions (Add, Subt, Mult and Div).....
22     $Add_out[31:0] = $val1[31:0] + $val2[31:0];
23     $Sub_out[31:0] = $val1[31:0] - $val2[31:0];
24     $Mul_out[31:0] = $val1[31:0] * $val2[31:0];
25     $Div_out[31:0] = $val1[31:0] / $val2[31:0];
26
27     // Mux (4x1) Operation.....
28     $Calc_out[31:0] = ($op == 00) ? $Add_out:
29                       ($op == 01) ? $Sub_out:
30                       ($op == 10) ? $Mul_out:
31     $Div_out;
32
33     //...
34
35     // Assert these to end simulation (before the cycle limit).
36     *passed = *cyc_cnt > 40;
37     *failed = 1'b0;
38 \SV
39     endmodule
40
```