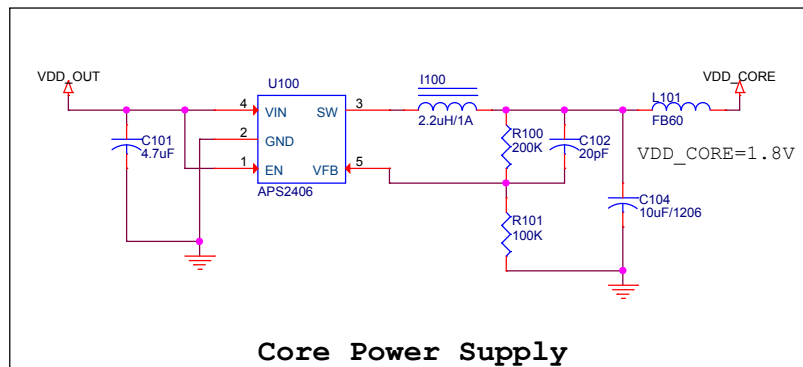
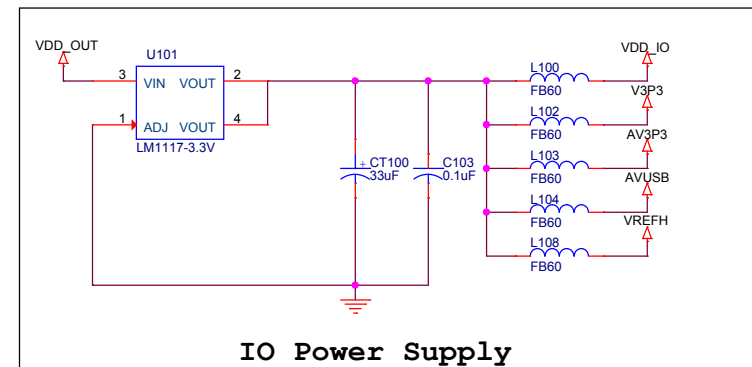


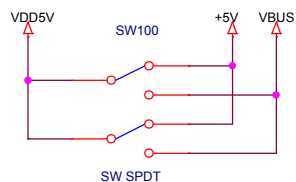
Wall Power



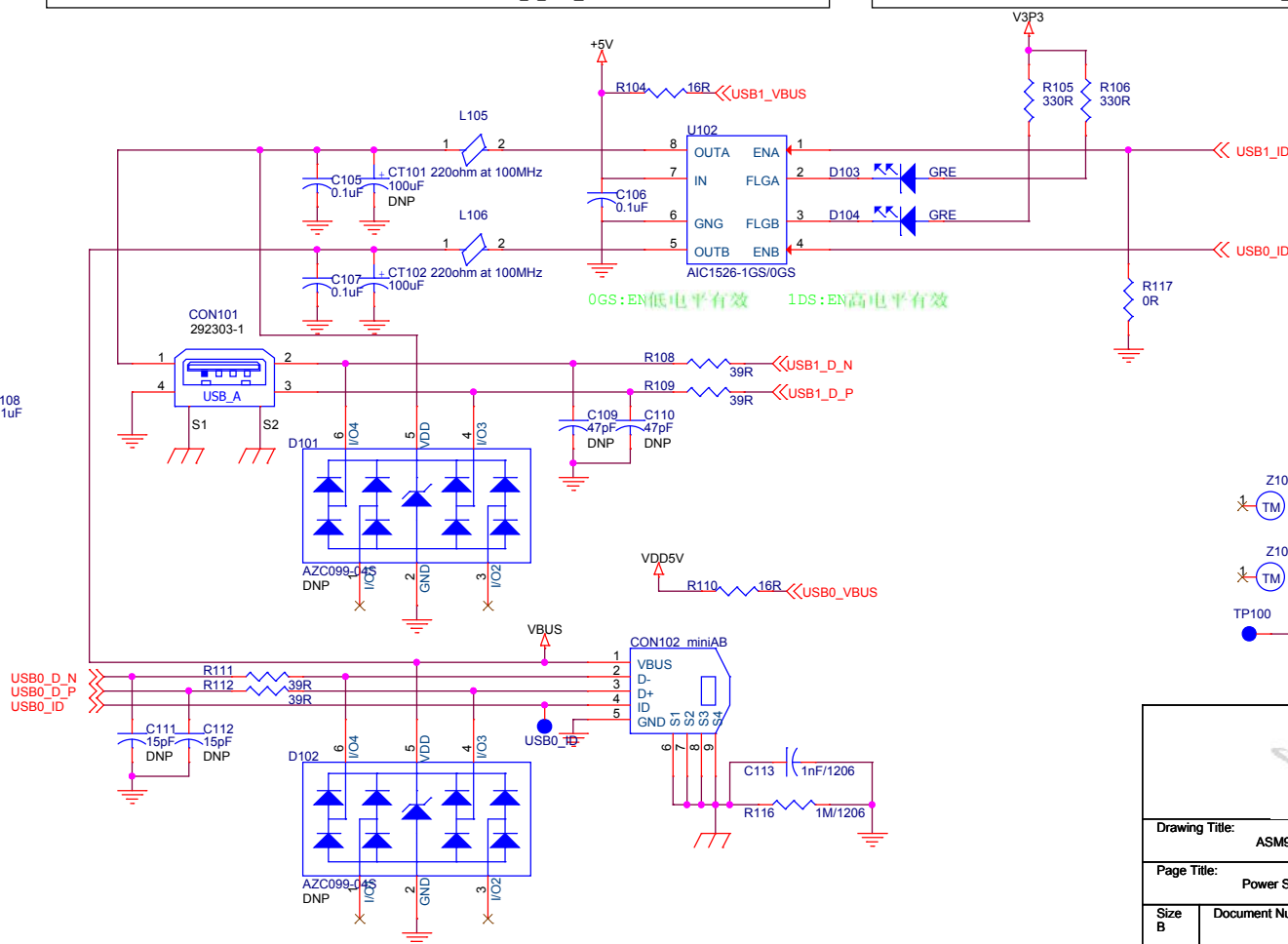
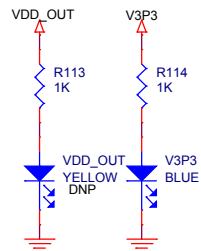
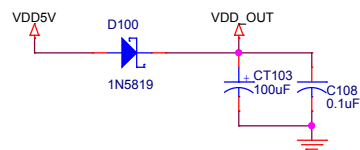
Core Power Supply



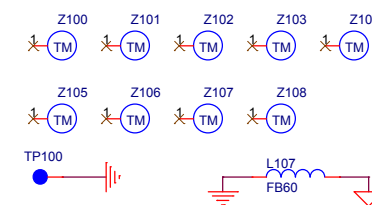
IO Power Supply



SW SPDT



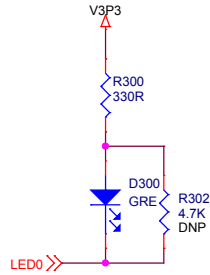
USB OTG



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BOOT

QSPI NOR NAND



GP14_5 >>> SD0_DAT3
GP14_4 >>> SD0_DAT2
GP14_3 >>> SD0_DAT1
GP14_2 >>> SD0_DAT0
GP14_1 >>> SD0_CMD
GP14_0 >>> SD0_CLK

SDRAM

GP16_0 >>> UART4_TXD_A
GP16_1 >>> UART5_TXD_A
GP16_2 >>> UART5_TXD
GP16_3 >>> UART5_RXD
GP16_4 >>> UART6_TXD
GP16_5 >>> UART6_RXD
GP16_6 >>> UART6_RTS
GP16_7 >>> LED0

GP17_0 >>> UART7_TXD
GP17_1 >>> UART7_RXD
GP17_2 >>> I2C1_SCL
GP17_3 >>> I2C1_SDA
GP17_4 >>> UART8_TXD
GP17_5 >>> UART8_RXD
GP17_6 >>> UART9_TXD
GP17_7 >>> UART9_RXD

GP14_2 >>> UART0_RTS
GP14_0 >>> UART0_TXD

GP13_7 >>> SPI1_MOSI
GP13_6 >>> SPI1_MISO
GP13_5 >>> SPI1_SEL
GP13_4 >>> SPI1_SCK

VDD_IO

68
159
161
164
165
167

69
160
162
163
166
168

GP15_0 >>> 44

GP14_5 >>> 43
GP14_4 >>> 42
GP14_3 >>> 41
GP14_2 >>> 40
GP14_1 >>> 39
GP14_0 >>> 38

GP13_7 >>> 34
GP13_6 >>> 33
GP13_5 >>> 32
GP13_4 >>> 31

U200B

VDD_DRAM
VDD_DRAM
VDD_DRAM
VDD_DRAM
VDD_DRAM
VDD_DRAM

VSS_DRAM
VSS_DRAM
VSS_DRAM
VSS_DRAM
VSS_DRAM
VSS_DRAM

GPIO15_0/EMI_ADDR0/RMII_MDC/SD0_CLK/I2S0_MCLK/UART4_TXD

GPIO14_5/EMI_ADDR3/NAND_D5/SD0_DAT3/I2S0_TX1/UART0_DSR
GPIO14_4/EMI_ADDR4/NAND_D4/SD0_DAT2/I2S0_TX0/UART0_DTR
GPIO14_3/EMI_ADDR5/NAND_D3/SD0_DAT1/I2S0_RX0/UART0_CTS
GPIO14_2/EMI_ADDR6/NAND_D2/SD0_DAT0/I2S0_LRC/UART0_RTS
GPIO14_1/EMI_ADDR7/NAND_D1/SD0_CMD/I2S0_BCLK/UART0_RXD/MCOB2
GPIO14_0/EMI_ADDR8/NAND_D0/SD0_CLK/I2S0_MCLK/UART0_TXD/MCOA2

GPIO13_7/EMI_ADDR9/NAND_CE1N/SPI1_MOSI/MCI2
GPIO13_6/EMI_ADDR10/NAND_CE0N/SPI1_MISO/UART9_RXD/MCOB1
GPIO13_5/EMI_ADDR10_PRE/NAND_RDY1/SPI1SEL/UART9_TXD/MCOA1
GPIO13_4/EMI_ADDR11/NAND_RDY0/SPI1_SCK/UART2_CTS/MCI1

ASM9260T_LQFP176_SIP_20130530

GPIO16_0/CT2_MAT0/UART4_TXD/MII_RXD2/EMI_DATA0
GPIO16_1/CT2_MAT1/UART4_RXD/MII_RXD3/EMI_DATA1
GPIO16_2/CT2_MAT2/UART5_TXD/MII_TXD2/EMI_DATA2
GPIO16_3/CT2_MAT3/UART5_RXD/MII_TXD3/EMI_DATA3
GPIO16_4/CT2_CAP1/UART6_TXD/MII_TX_CLK/EMI_DATA4
GPIO16_5/QEI_A/UART6_RXD/MII_CRS/EMI_DATA5
GPIO16_6/QEI_B/UART6_RTS/MII_COL/EMI_DATA6/CAN1_TX
GPIO16_7/QEI_INDEX/UART6_CTS/MII_RX_ER/EMI_DATA7/CAN1_RX

GPIO17_0/CT3_MAT0/UART7_TXD/I2S1_MCLK/RMII_REFCLK/EMI_DATA8
GPIO17_1/CT3_MAT1/UART7_RXD/I2S1_BCLK/EMI_DATA9
GPIO17_2/CT3_MAT2/UART7_RTS/I2S1_LRC/EMI_DATA10/I2C1_SCL
GPIO17_3/CT3_MAT3/UART7_CTS/I2S1_RX0/EMI_DATA11/I2C1_SDA
GPIO17_4/CT3_CAP/UART8_TXD/I2S1_TX0/EMI_DATA12
GPIO17_5/QEI_A/UART8_RXD/I2S1_TX1/RMII_REFCLK/EMI_DATA13
GPIO17_6/QEI_B/UART9_TXD/I2S1_TX2/MII_PPS_OUT/EMI_DATA14
GPIO17_7/QEI_INDEX/UART9_RXD/MII_PPS_OUT/EMI_DATA15

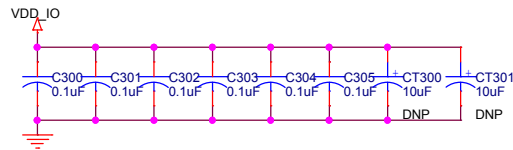
GPIO9_0/CT0_MAT0/UART3_RTS/CAM_DAT4/RMII_MDC/EMI_DATA16/I2C0_SCL
GPIO9_1/CT0_MAT1/UART3_CTS/CAM_DAT5/RMII_MDIO/EMI_DATA17/I2C0_SDA
GPIO9_2/CT0_MAT2/UART4_CLK/CAM_DAT6/RMII_CRS_DV/EMI_DATA18
GPIO9_3/CT0_MAT3/UART4_TXD/CAM_DAT7/RMII_RXD0/EMI_DATA19
GPIO9_4/CT0_CAP/UART4_RXD/CAM_DAT8/RMII_RXD1/EMI_DATA20
GPIO9_5/UART4_RTS/CAM_DAT9/RMII_TX_EN/EMI_DATA21/I2C1_SCL

GPIO8_1/UART2_TXD/CAM_PCLK/MII_RXD3/EMI_DATA25
GPIO8_2/UART2_RXD/CAM_VSYN/MII_TXD2/EMI_DATA26
GPIO8_3/UART2_RTS/CAM_HREF/MII_TXD3/EMI_DATA27
GPIO8_4/UART2_CTS/CAM_DAT0/MII_TX_CLK/EMI_DATA28
GPIO8_5/UART3_CLK/CAM_DAT1/MII_CRS/EMI_DATA29
GPIO8_6/UART3_TXD/CAM_DAT2/MII_COL/EMI_DATA30
GPIO8_7/UART3_RXD/CAM_DAT3/MII_RX_ER/EMI_DATA31

GPIO15_7/EMI_CKE/RMII_TXD1/UART7_RXD
GPIO15_6/EMI_CLK/RMII_TXD0/I2S0_TX0/UART7_TXD
GPIO15_5/EMI_WEN/RMII_TX_EN/SD0_DAT3/I2S0_TX1/UART6_RXD
GPIO15_4/EMI_CASN/RMII_RXD1/SD0_DAT2/I2S0_TX0/UART6_TXD
GPIO15_3/EMI_RASN/RMII_RXD0/SD0_DAT1/I2S0_RX0/UART5_RXD
GPIO15_2/EMI_DQM1/RMII_CRS_DV/SD0_DAT0/I2S0_LRC/UART5_TXD
GPIO15_1/EMI_DQM0/RMII_MDIO/SD0_CMD/I2S0_BCLK/UART4_RXD
GPIO15_0 >>> 61

RMII

GP15_7 >>> RMII_TXD1
GP15_6 >>> RMII_TXD0
GP15_5 >>> RMII_TX_EN
GP15_4 >>> RMII_RXD1
GP15_3 >>> RMII_RXD0
GP15_2 >>> RMII_CRS_DV
GP15_1 >>> RMII_MDIO
GP15_0 >>> RMII_MDC



GP8_1 >>> CAM_PCLK
GP8_2 >>> CAM_VSYN
GP8_3 >>> CAM_HREF
GP8_4 >>> CAM_DAT0
GP8_5 >>> CAM_DAT1
GP8_6 >>> CAM_DAT2
GP8_7 >>> CAM_DAT3

GP9_0 >>> CAM_DAT4
GP9_1 >>> CAM_DAT5
GP9_2 >>> CAM_DAT6
GP9_3 >>> CAM_DAT7
GP9_4 >>> CAM_DAT8
GP9_5 >>> CAM_DAT9

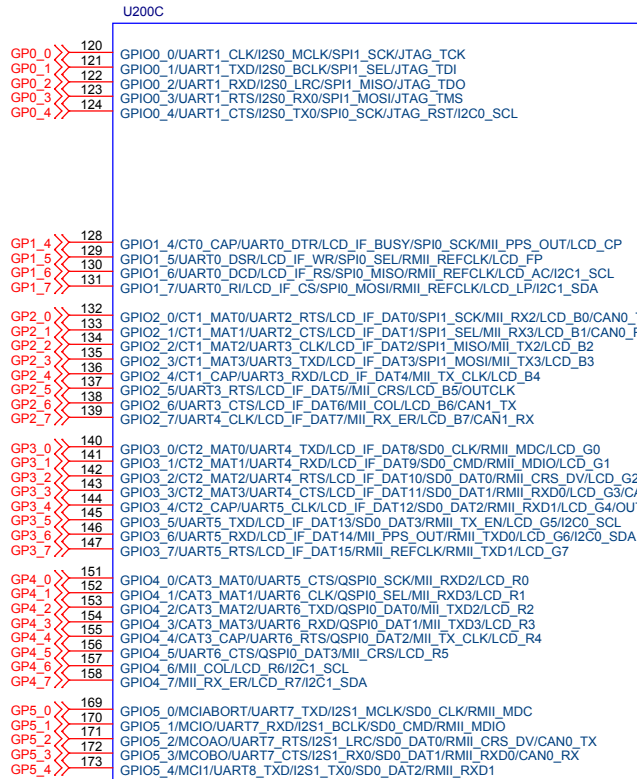
GP8_5 >>> UART3_CLK
GP8_6 >>> UART3_TXD
GP8_7 >>> UART3_RXD



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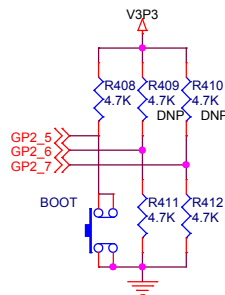
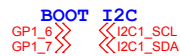
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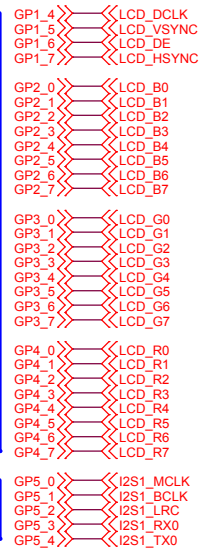
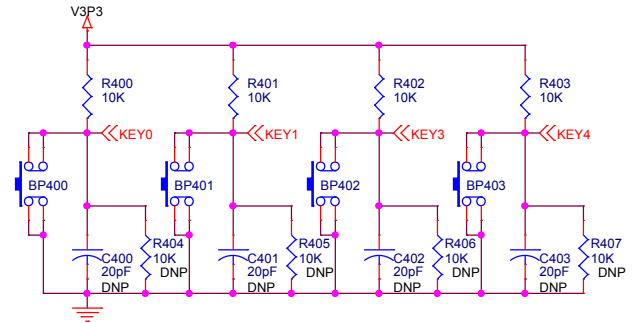
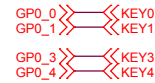


ASM9260T_LQFP176_SIP_20130530

GPIO2[5]	GPIO2[7]	GPIO2[6]	Boot Modes	Dest Addr	Code Length
1	0	0	I2C-EEPROM Mode 1	0x40000000	2KB
	0	1	Nor-Flash	0x0	-
	1	0	Nand-Flash Mode 1	0x40000000	4KB
	1	1	QUADSPI	0x40000000	4KB
0	0	X	ISP(UART/USB High Speed)	-	-
	1	X	ISP(UART/USB Full Speed)	-	-



BOOT MODE



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U200D

GP10_0 >> 4 GPIO10_0/CT1_MAT0/UART5_TXD/I2S0_MCLK/SPI0_SCK/EMI_CEN0
GP10_1 >> 5 GPIO10_1/CT1_MAT1/UART5_RXD/I2S0_BCLK/SPI0_SEL/RMII_REFCLK/EMI_DQM2
GP10_2 >> 6 GPIO10_2/CT1_MAT2/UART5_RTS/I2S0_LRC/SPI0_MISO/RMII_REFCLK/EMI_DQM3/CAN0_TX
GP10_3 >> 7 GPIO10_3/CT1_MAT3/UART5_CTS/I2S0_RX0/SPI0_MOSI/EMI_BSN2/CAN0_RX
GP10_4 >> 8 GPIO10_4/CT1_CAP/UART6_TXD/I2S0_TX0/SPI1_SCK/RMII_REFCLK/EMI_BSN3
GP10_5 >> 9 GPIO10_5/UART6_RXD/I2S0_TX1/SPI1_SEL/RMII_REFCLK/EMI_SSEN/CAN1_RX
GP10_6 >> 10 GPIO10_6/UART6_RTS/I2S0_TX2/SPI1_MISO/RMII_REFCLK/EMI_CEN1/CAN1_TX
GP10_7 >> 11 GPIO10_7/UART6_CTS/CAM_MCLK/SPI1_MOSI/RMII_REFCLK/EMI_CEN3

GP11_0 >> 12 GPIO11_0/CT2_MAT0/UART7_TXD/I2S1_MCLK/QSPI0_SCK/NAND_REN/EMI_ADV
GP11_1 >> 13 GPIO11_1/CT2_MAT1/UART7_RXD/I2S1_BCLK/QSPI0_SEL/NAND_WEN/EMI_RPN
GP11_2 >> 14 GPIO11_2/CT2_MAT2/UART7_RTS/I2S1_LRC/QSPI0_DAT0/NAND_ALE/EMI_BSN0
GP11_3 >> 15 GPIO11_3/CT2_MAT3/UART7_CTS/I2S1_RX0/QSPI0_DAT1/NAND_CLE/EMI_BSN1
GP11_4 >> 16 GPIO11_4/CT2_CAP/UART8_TXD/I2S1_TX0/QSPI0_DAT2/NAND_RDY0/EMI_SM_WEN
GP11_5 >> 17 GPIO11_5/UART8_RXD/I2S1_TX1/QSPI0_DAT3/NAND_RDY1/EMI_SM_OEN
GP11_6 >> 18 GPIO11_6/UART9_TXD/I2S1_TX2/NAND_CE0/EMI_RDY/I2C0_SCL
GP11_7 >> 19 GPIO11_7/UART9_RXD/CAM_MCLK/NAND_CE1/EMI_CEN2/I2C0_SDA

GP12_0 >> 23 GPIO12_0/CT3_MAT0/UART1_CLK/SD0_CLK/NAND_D0/EMI_ADDR16
GP12_1 >> 24 GPIO12_1/CT3_MAT1/UART1_TXD/SD0_CMD/NAND_D1/EMI_ADDR17
GP12_2 >> 25 GPIO12_2/CT3_MAT2/UART1_RXD/SD0_DAT0/NAND_D2/EMI_ADDR18
GP12_3 >> 26 GPIO12_3/CT3_MAT3/UART1_RTS/SD0_DAT1/NAND_D3/EMI_ADDR19
GP12_4 >> 27 GPIO12_4/CT3_CAP/UART1_CTS/SD0_DAT2/NAND_D4/EMI_ADDR20
GP12_5 >> 28 GPIO12_5/UART8_TXD/SD0_DAT3/NAND_D5/EMI_ADDR21
GP12_6 >> 29 GPIO12_6/UART8_RXD/CAM_CLK/NAND_D6/EMI_ADDR22/I2C1_SCL
GP12_7 >> 30 GPIO12_7/CAM_CLK/NAND_D7/EMI_ADDR23/I2C1_SDA

ASM9260T_LQFP176_SIP_20130530

NOR EMI_CE0

SDRAM EMI_CE2

Ethernet

GP10_7 >> << RMII_REFCLK

GP11_0 >> << QSPI0_SCK
GP11_1 >> << QSPI0_SEL
GP11_2 >> << QSPI0_D0
GP11_3 >> << QSPI0_D1
GP11_4 >> << QSPI0_D2
GP11_5 >> << QSPI0_D3

GP11_7 >> << CAM_MCLK

QSPI

GP10_0 >> << CT1_MAT0
GP10_1 >> << CT1_MAT1
GP10_2 >> << CT1_MAT2
GP10_3 >> << CT1_MAT3
GP10_4 >> << CT1_CAP
GP10_5 >> << CAN1_RX
GP10_6 >> << CAN1_TX

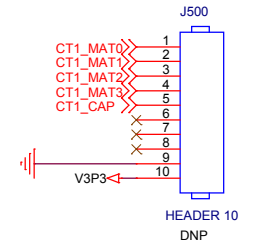
GP11_0 >> << NAND_REN
GP11_1 >> << NAND_WEN
GP11_2 >> << NAND_ALE
GP11_3 >> << NAND_CLE
GP11_4 >> << NAND_RDY0

GP11_6 >> << NAND_CE0N

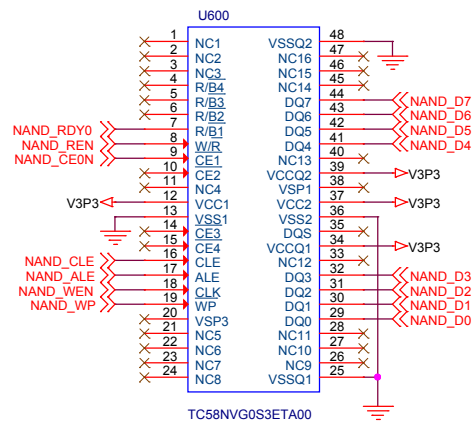
GP12_0 >> << NAND_D0
GP12_1 >> << NAND_D1
GP12_2 >> << NAND_D2
GP12_3 >> << NAND_D3
GP12_4 >> << NAND_D4
GP12_5 >> << NAND_D5
GP12_6 >> << NAND_D6
GP12_7 >> << NAND_D7

NAND

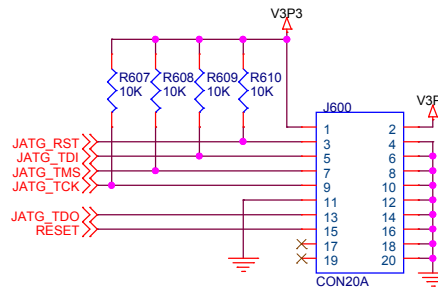
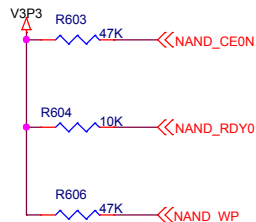
BOOT



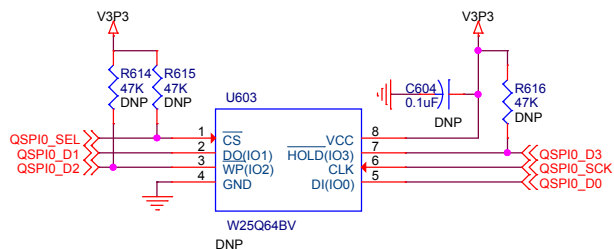
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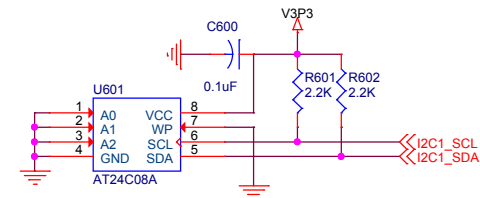
ONFI NAND



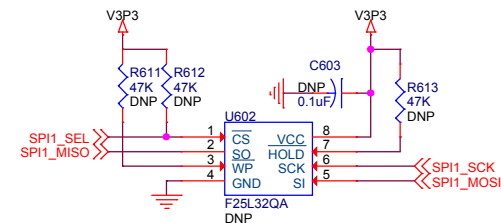
JTAG



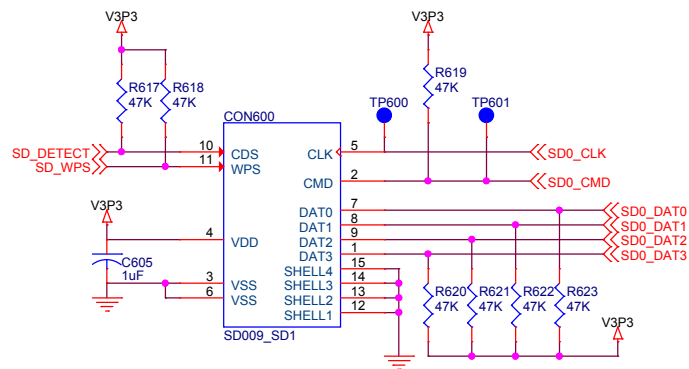
QSPI NOR FLASH



I2C EEPROM



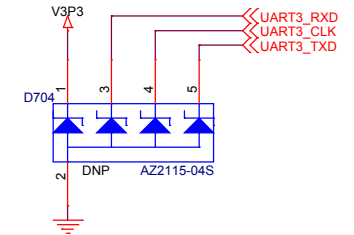
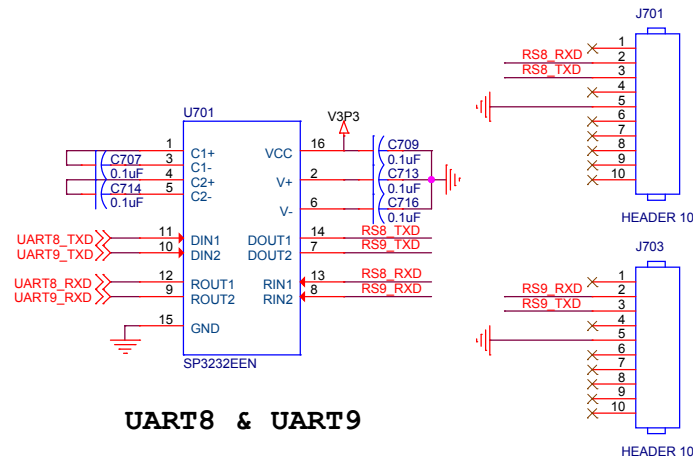
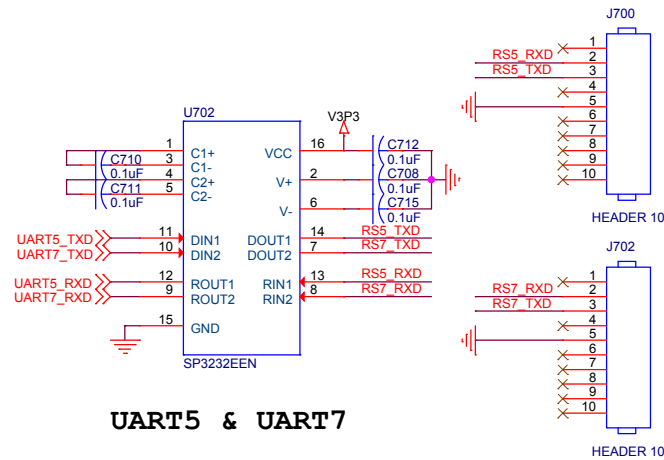
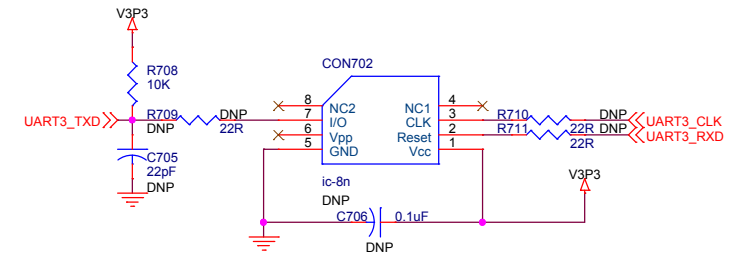
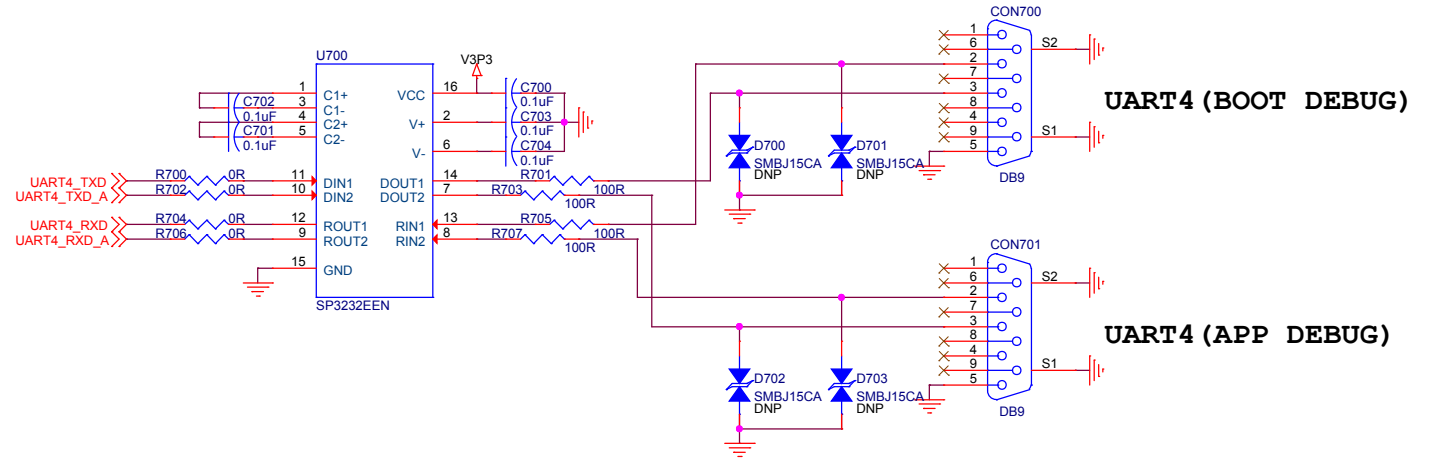
SPI FLASH




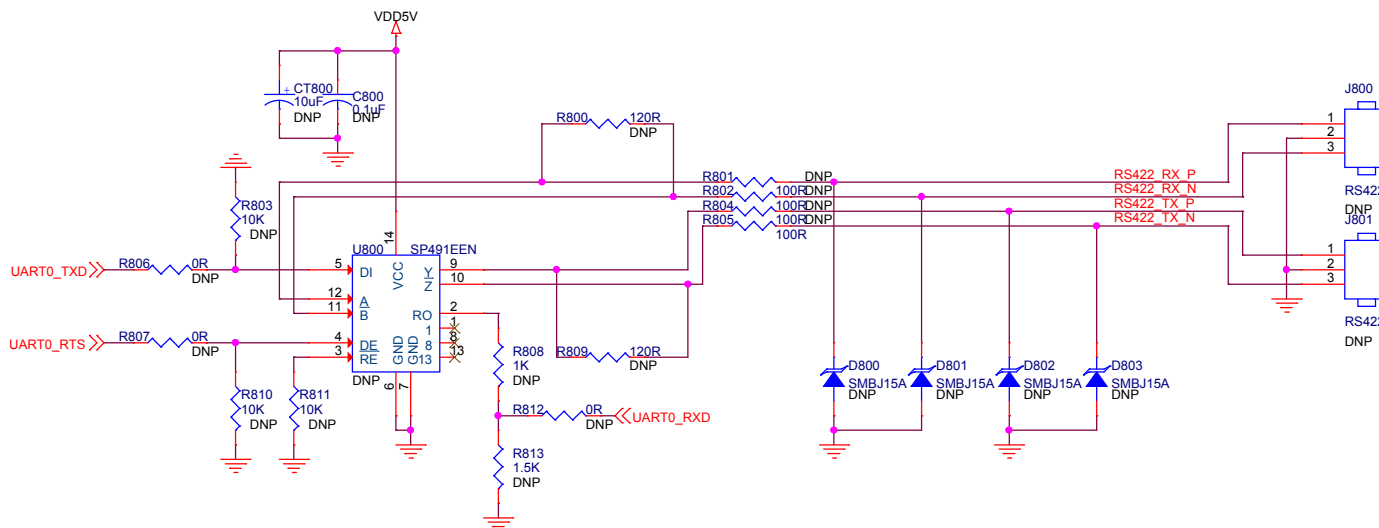
SD/MMC SOCKET



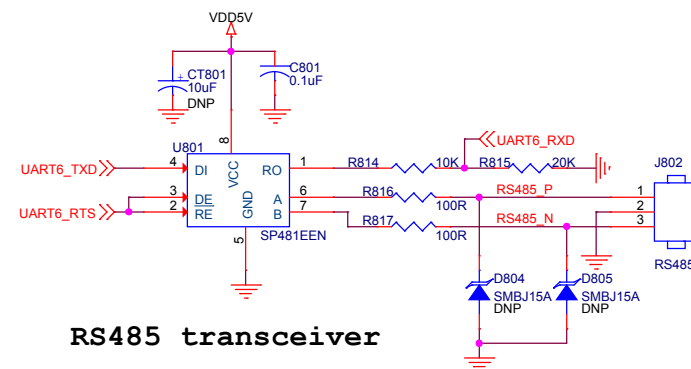
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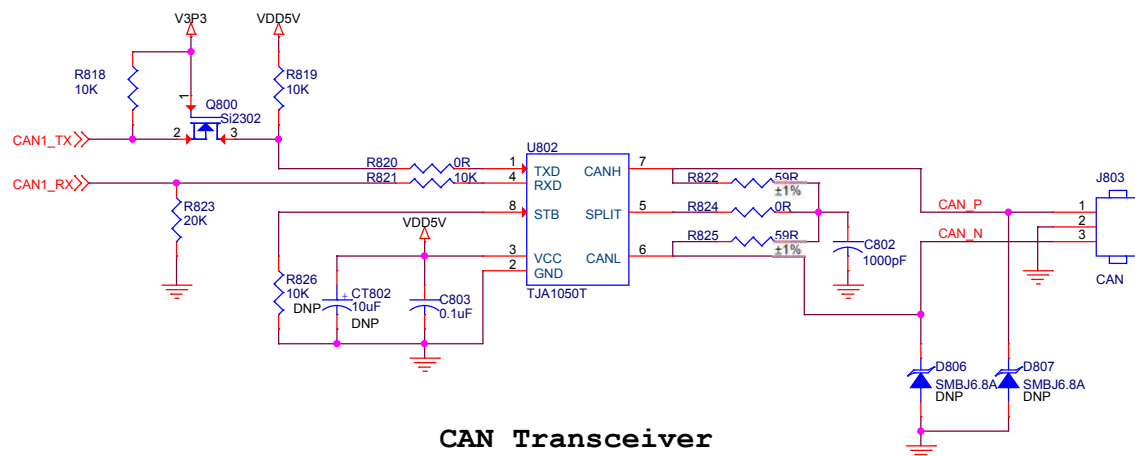
			
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RS422 transceiver



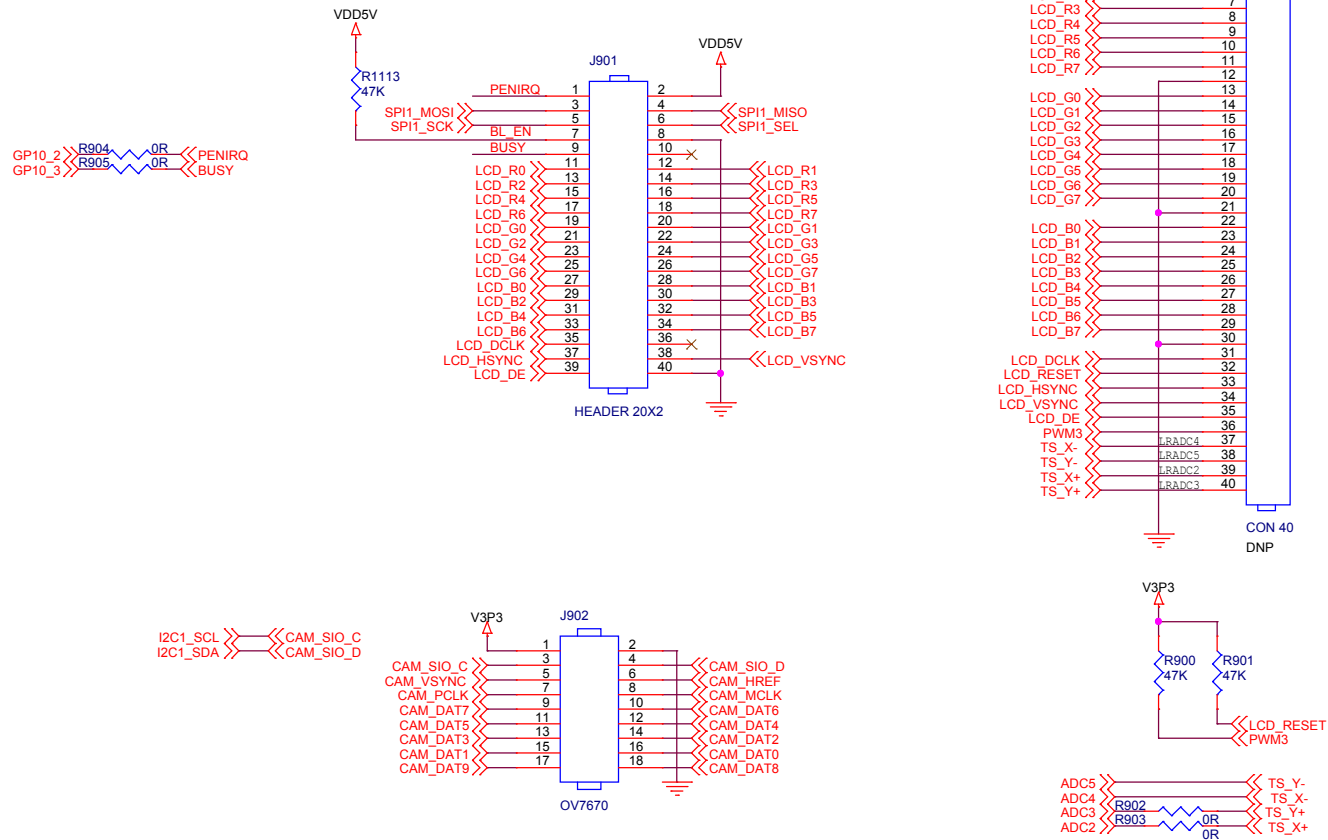
RS485 transceiver



CAN Transceiver



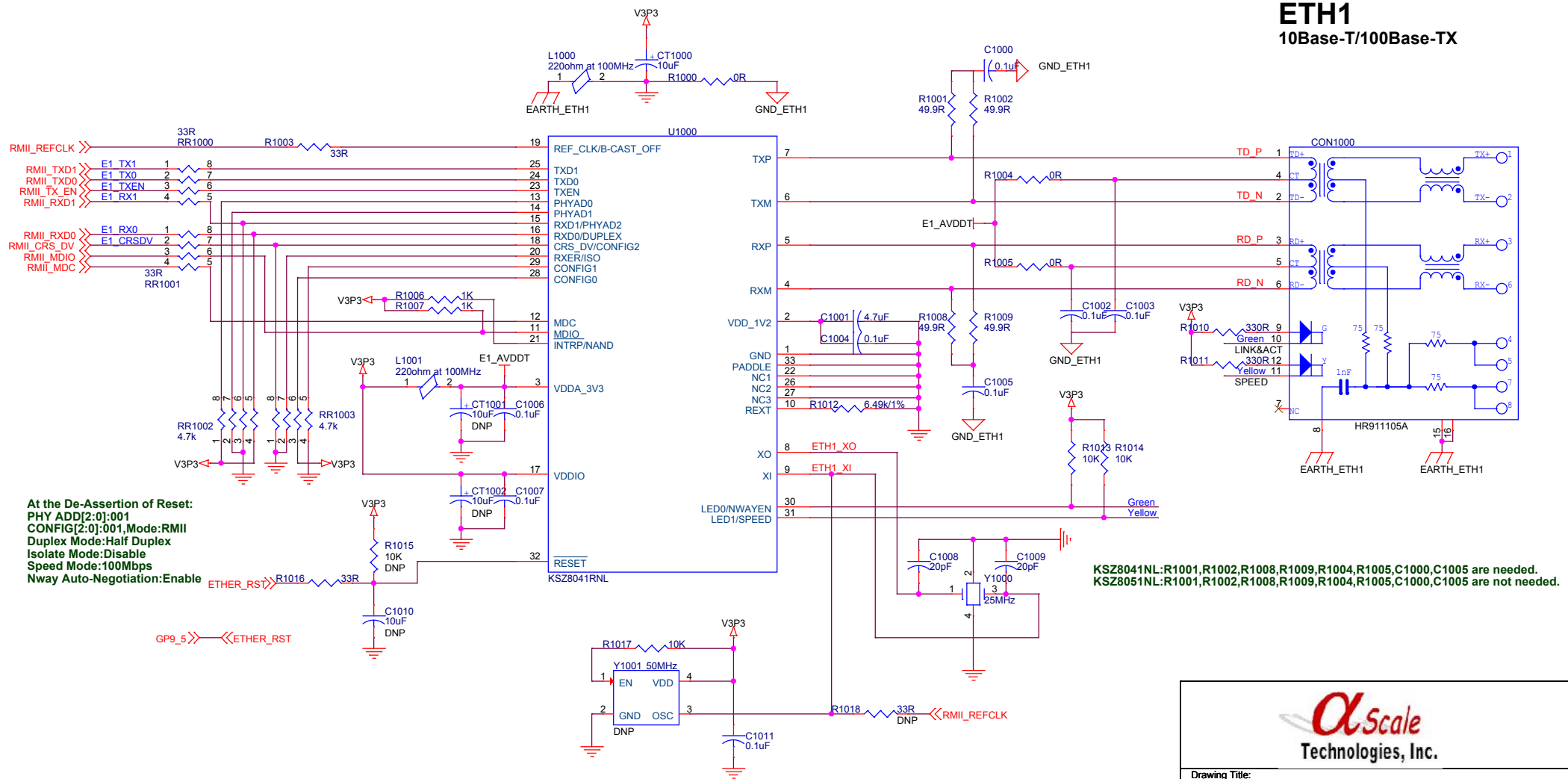
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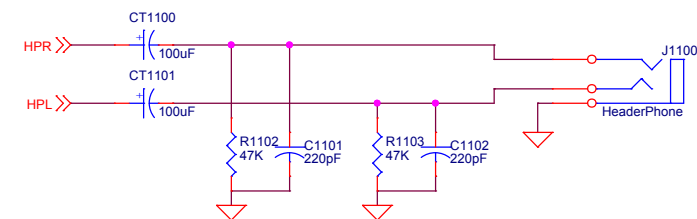
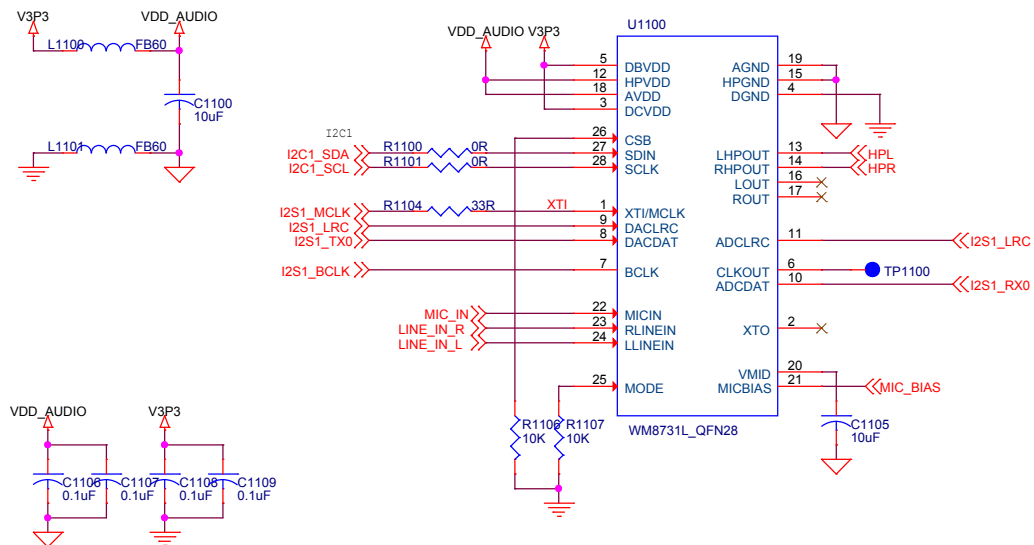
LCD Moudle



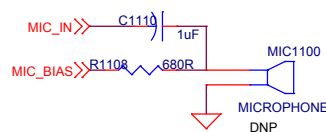
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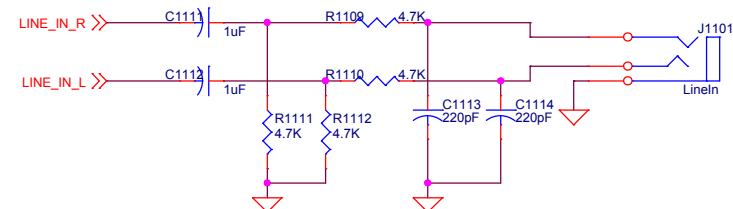
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HeaderPhone Out



MicPhone Input



LineIn



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